Application Report

SNVA027A – May 2004 – Revised April 2013

AN-776 20 Watt Simple Switcher Forward Converter

ABSTRACT

This application note provides information about the 20-watt simple switcher forward converter.

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1 Overview

A 20W, 5V at 4A, step-down regulator can be developed using the LM2577 Simple Switcher IC in a forward converter topology. This design allows the LM2577 IC to be used in step-down voltage applications at output power levels greater than the 1 A LM2575 and 3 A LM2576 buck regulators. In addition, the forward converter can easily provide galvanic isolation between input and output.

The design specifications are shown in Table 1.

Table 1. Design Specifications

V_i Range	20V–24V
V_o	5V
$I_{o(max)}$	4A
ΔV_o	20 mV

With the input and output conditions identified, the design procedure begins with the transformer design, followed by the output filter and snubber circuit design.

2 Transformer Design

1. Using the maximum switch voltage, input voltage, and snubber voltage, the transformer's primary-to-clamp windings turns ratio is calculated:

$$V_{SW} \geq V_{imax} + V_{imax}\,(N_p/N_c) + V_{snubber} \quad (1)$$

$$N_p/N_c \leq (V_{SW} - V_{imax} - V_{snubber})/V_{imax} \quad (2)$$

$$N_p/N_c \leq (60V - 24V - 5V)/24V = 1.29 \quad (3)$$

$$\Delta \text{ let } N_p/N_c = 1.25 \quad (4)$$

   The $V_{snubber}$ voltage is an estimate of the voltage spike caused by the transformer's primary leakage inductance.

2. The duty cycle, t_{on}/T, of the switch is determined by the volt-second balance of the primary winding.

   During t_{on};

$$V_i = L_p\,(\Delta i/T_{ON}) \rightarrow \Delta i = (V_i/L_P)\,t_{on}$$

   During t_{off};

$$V_i = (N_p/N_c) = L_p\,(\Delta i/t_{off}) \rightarrow \Delta i = (N_p/N_c)\,(V_i/L_p)\,t_{off}$$

   Setting Δi's equal;

$$(V_i/L_p)\,t_{on} = (N_p/N_c)\,(V_i/L_p)\,t_{off}$$

$$t_{on}/t_{off} = N_p/N_c$$

   Since $D = t_{on}/T = t_{on}/(t_{on} + t_{OFF})$

   max. duty cycle $= D_{max} = (N_p/N_c)/\,[(N_p/N_c) + 1]$

$$D_{max} = (1.25)/\,(1.25 + 1) = 0.56\ (56\%)$$

3. The output voltage equations of a forward converter provides the transformer's secondary-to-primary turns ratio:

$$V_o + V_{diode} \leq V_{imin} \times D_{max}\,(N_s/N_p)$$

$$N_s/N_p \geq (V_o + V_{diode})/\,(V_{imin} \times D_{max})$$

$$N_s/N_p \geq (5.5V)/(20V)(56\%) = 0.49$$

$$\Delta\text{let } N_s/N_p = 0.5$$

4. Calculate transformer's primary inductance by finding the maximum magnetizing current (Δi_{Lp}) that does not allow the maximum switch current to exceed it's 3 A limit (capital I for DC current, Δi for AC current, and lower case i for total current):

$$i_{sw} = i_{pri} = i_{Lo'} + \Delta i_{Lp} \quad (5)$$

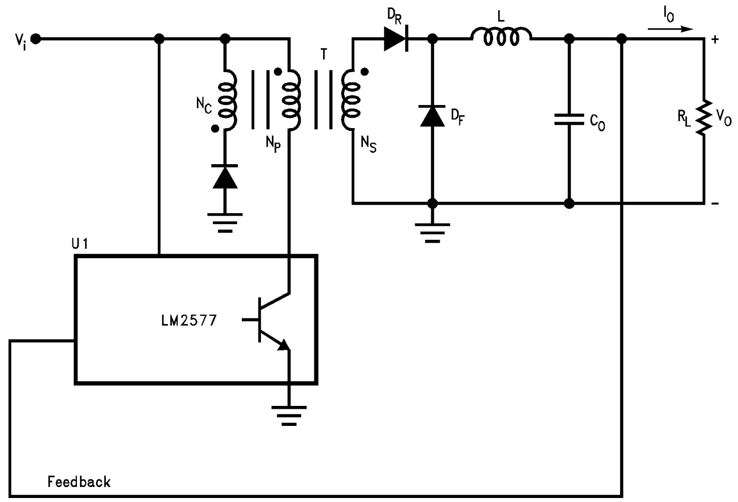


Figure 1. Basic Forward Converter

where i_{Lo}, is the reflected secondary current and Δi_{LP} is the primary inductance current.

$i_{Lo'} = i_{Lo}(N_s/N_p)$ (i_{Lo} reflected to primary)

$i_{Lo} = I_{Lo} \pm \Delta i_{Lo}/2$ (6)

Δi_{Lo} is the output inductor's ripple current (7)

$I_{Lo} = I_o$ (the load current) (8)

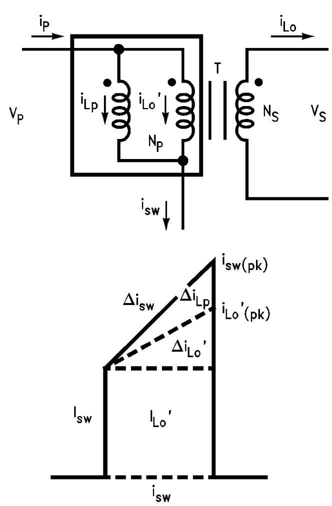
$i_{Lo'} = (I_o \pm \Delta i_{Lo}/2)(N_s/N_p)$ (9)

$i_{Lo'(pk)} = (I_{o(max)} + \Delta i_{Lo}/2)(N_s/N_p)$ (10)

$i_{sw} = I_{sw} + \Delta i_{sw}$ (11)

$i_{sw(pk)} = i_{Lo'(pk)} + \Delta i_{Lp(pk)}$ (12)

$i_{sw(pk)} = (I_{o(max)} + \Delta i_{Lo}/2)(N_s/N_p) + \Delta i_{Lp(pk)}$ (13)



Using standard inductors, a good practical value to set the output inductor current (Δi_{Lo}) to is 30% of the maximum load current (I_o). Thus;

$i_{sw(pk)} = (I_{o(max)} + 0.15\Delta i_{Lo})(N_s/N_p) + \Delta i_{Lp(pk)}$ (14)

$\Delta i_{Lp(pk)} = i_{sw(pk)} - (I_{o(max)} + 0.15\Delta i_{Lo})(N_s/N_p)$ (15)

$\Delta i_{Lp(pk)} = 3A - (4A + 0.15 \times 4A)(0.5) = 0.7A$ (16)

$L_p = V_{pri} \times \Delta t/\Delta i = (V_i - V_{sat})(t_{on}/\Delta i_{Lp(pk)})$ (17)

$= (V_{i(max)} - V_{sat})(D_{max}/(\Delta i_{Lp(pk)} \times f)$ (18)

$= (24V - 0.8V)(0.56/0.7 \times 52\text{ kHz})$ (19)

$L_p = 357\ \mu H$ (20)

$\Delta let\ L_p = 350\ \mu H$ (21)

3 Output Filter—Inductor

The first component calculated in the design is the output inductor, using the current-to-voltage relationship of an inductor:

$$V_L = L_o\ (\Delta i_{Lo}/t_{on}) \tag{22}$$

Choosing an inductor ripple current value of $0.3I_o$ and a maximum output current of 4A:

$$\Delta i_{Lo} = 0.3\ (4A) = 1.2A \tag{23}$$

During t_{on};

$$V_L = V_S - V_D - V_o\ [\text{where } V_s = (V_i - V_{sat})(N_s/N_p)] \tag{24}$$

Thus,

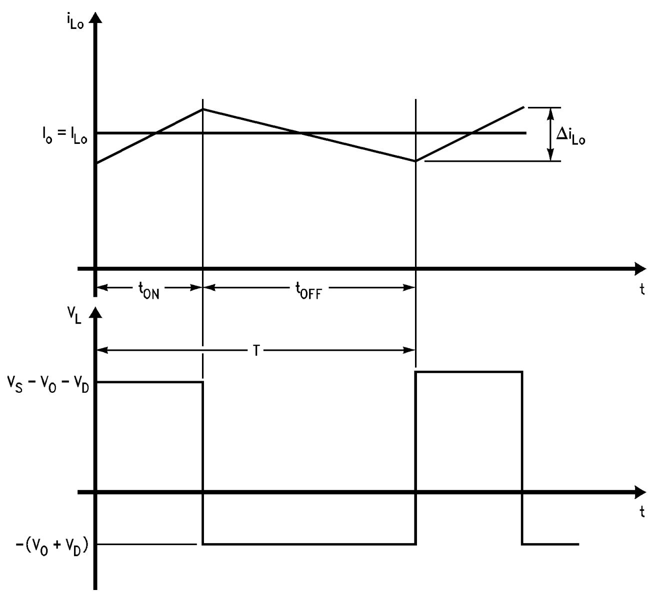
$[(V_i - V_{sat})(N_s/N_p) - V_d - V_o] = L_o\ (\Delta i_{Lo}/D)\ f$

$L_o = [(V_i - V_{sat})(N_s/N_p) - V_d - V_o] \times D/\Delta i_{Lo} \times f$

$L_o = [(24V-0.8V)(0.5)-0.5V-5V]\ 56\%/1.2A\times 52\ kHz$

$L_O = 55\ \mu H$

Δ let $L_o = 60\ \mu H$



4 Output Filter—Capacitor

Since the output capacitor's current is equal to inductor's ripple current, the output capacitor's value can be found using the inductor's ripple current. Starting with the current-voltage relationship, the output capacitance is calculated:

$\Delta V_o = 1/C_o \int i\ dt$

$= \Delta i_{Lo}/\ 4C_o\ (TR/2)$

$= (\Delta i_{Lo} \bullet T)/\ 8C_o$

$C_o = (\Delta i_{Lo} \bullet T)/\ 8\Delta V_o$

However, the equivalent series resistance (ESR) of the capacitor multiplied by the inductor's ripple current creates a parasitic output ripple voltage equal to:

$$\Delta V_o = ESR_{co} \bullet \Delta i_{Lo} = ESR_{co} \bullet 0.3\ I_o \tag{25}$$

This parasitic voltage is usually much larger than the inherent ripple voltage. Hence, the output capacitor parameter of interest, when calculating the output ripple voltage, is the equivalent series resistance (the capacitance of the output capacitor will be determined by the frequency response analysis). Using a standard-grade capacitor with ESR of 0.05Ω produces a total output ripple voltage of:

$$\Delta V_o = 0.05\Omega \bullet 1.2A \cong 60 \text{ mV} \tag{26}$$

To get output ripple voltage of 20 mV or less (as was part of the design specs) requires a capacitor with ESR of less than 17 mΩ.

5 Snubber Circuit

A snubber circuit (C_S, R_S, D_S) is added to reduce the voltage spike at the switch, which is caused by the transformer's leakage inductance. It is designed as follows: when the switch is off,

$$V_R = V_{CE} - V_{IN} - V_D \tag{27}$$

$$V_{LL} = V_D + V_R - V_{IN}(N_p/N_c) \tag{28}$$

Substituting for V_R, the voltage across the leakage inductance, V_{LL}, is,

$$V_{LL} = V_{CE} - V_{IN}(1 + N_p/N_c) \tag{29}$$

Using the current-voltage relationship of inductors,

$$t_S = I_{PRI}(L_L/V_{LL}) \tag{30}$$

Substituting for V_{LL},

$$t_S = I_{PRI}\ L_L/(V_{CE} - V_{IN}(1 + N_p/N_c)) \tag{31}$$

Calculating for the average leakage inductance current, $I_{LL(AVE)}$,

$$I_{LL(AVE)} = I_{PRI(MAX)}\ (t_S)/2T \tag{32}$$

$$= I_{PRI(MAX)}{}^2\ L_L f/2(V_{CE} - V_{IN}(1 + N_p/N_c)) \tag{33}$$

Solving for the snubber resistor;

$$R_S = V_R/I_{LL(AVE)} \tag{34}$$

Substituting $I_{LL(AVE)}$ and V_R results in,

$$R_S = 2\ (V_{CE} - V_{IN}(1 + N_p/N_c))\ X \tag{35}$$

$$(V_{CE} - V_{IN} - V_D)/(L_L\ (I_{PRI(MAX)})^2 f) \tag{36}$$

Choosing L_L to equal 10% of L_p,

$$R_S = 2\ (65V - 24V - 1V)\ X\ (65V - 24V(2.25))/ \tag{37}$$

$$(7\ \mu H\ (3A)^2\ 52\ kHz) \tag{38}$$

$$= 268.9\Omega \cong 270\Omega \tag{39}$$

Using the current-voltage relationship of capacitors,

$$\Delta V_R = (T - t_S)\ I_c/C_S = (T - t_S)\ V_R/R_S C_S \cong V_R/R_S C_S f \tag{40}$$

The capacitor C_S equates to,

$$C_S = V_R/R_S\ f\ \Delta V_R \tag{41}$$

$$C_S = 40V/(270\Omega)(52\ kHz)\ 10V = 0.28\ \mu F \cong 0.33\ \mu F \tag{42}$$

The snubber diode has a current rating of 1A peak and a reverse voltage rating of 30V.

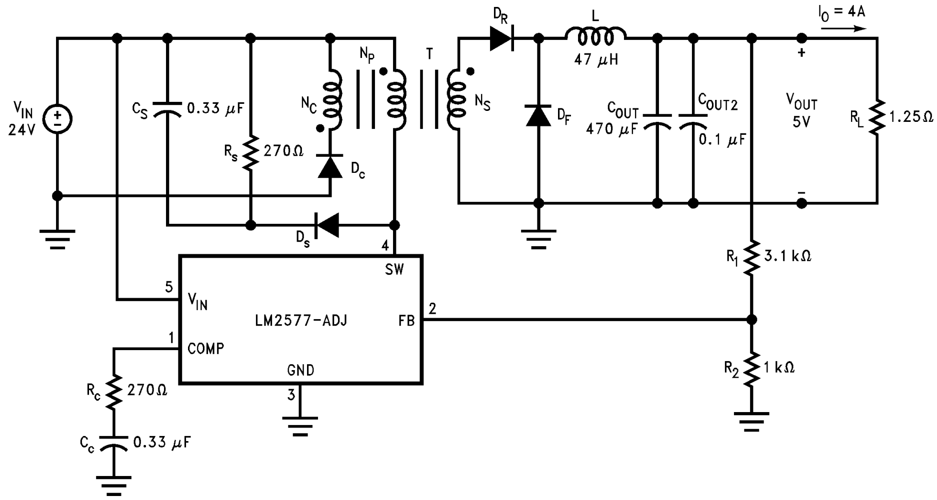
6 Other Components

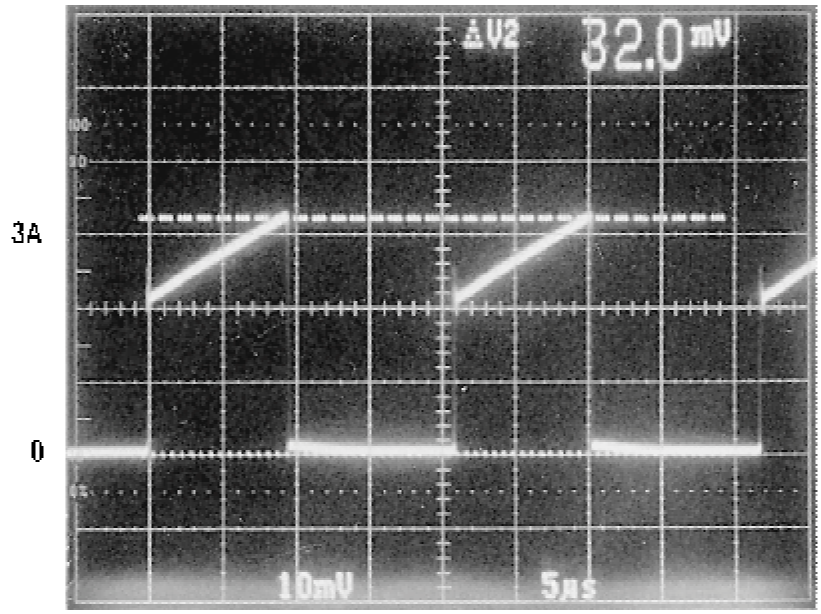
Diodes, D_R and D_F, used in the secondary are 5A, 30V Schottky diodes. The same diode type is used for D_c, however a lower current diode could have been used.

A compensation network of R_c and C_c optimizes the regulator's stability and transient response and provides a soft-start function for a well-controlled power-up.

The finished circuit is shown below.

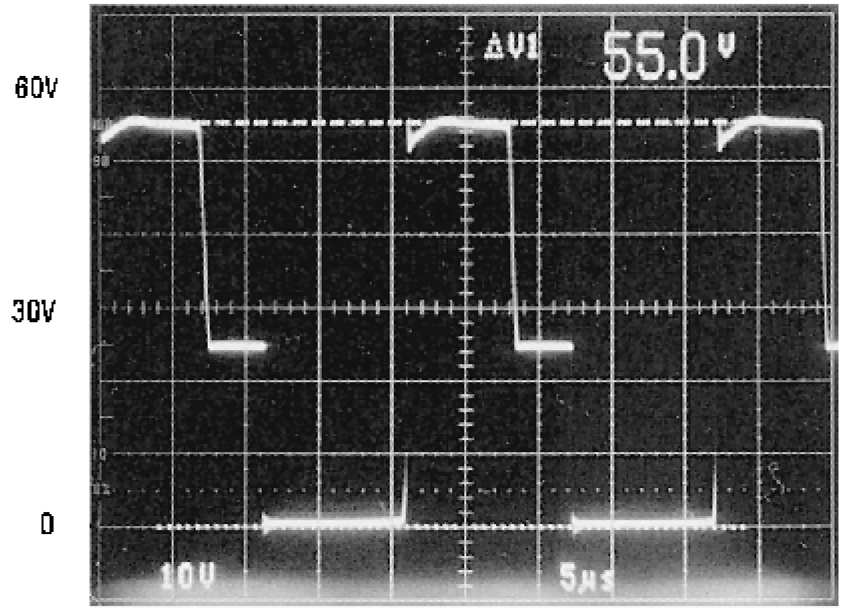
Figure 2. 5V, 4 A Forward Converter Circuit Schematic





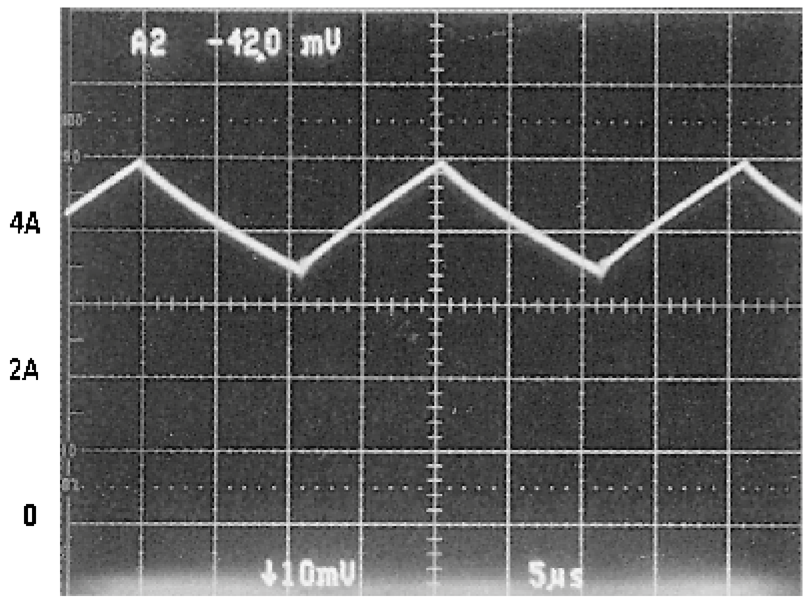
Vertical: 1 A/div
Horizontal: 5 µs/div

Figure 3. Switch Current



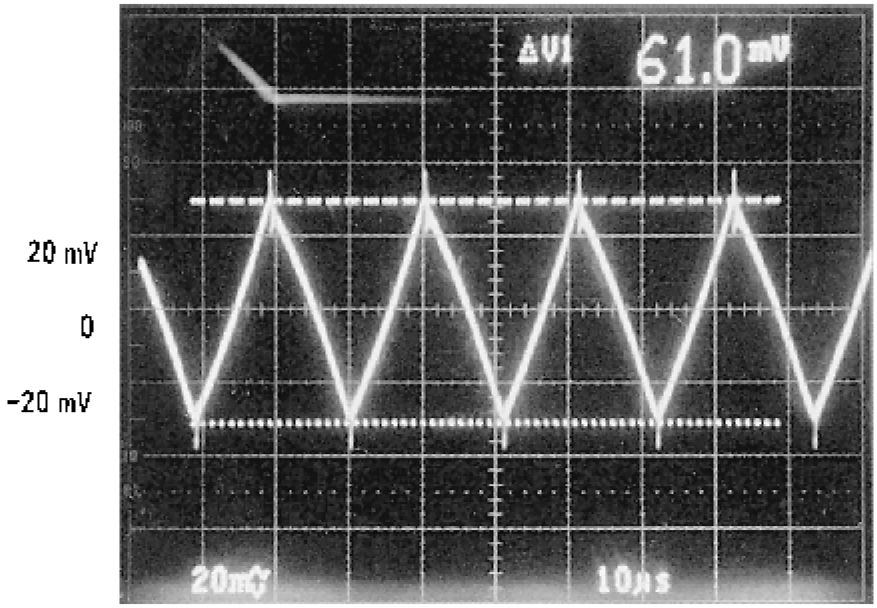
Vertical: 10 V/div
Horizontal: 5 µs/div

Figure 4. Switch Voltage



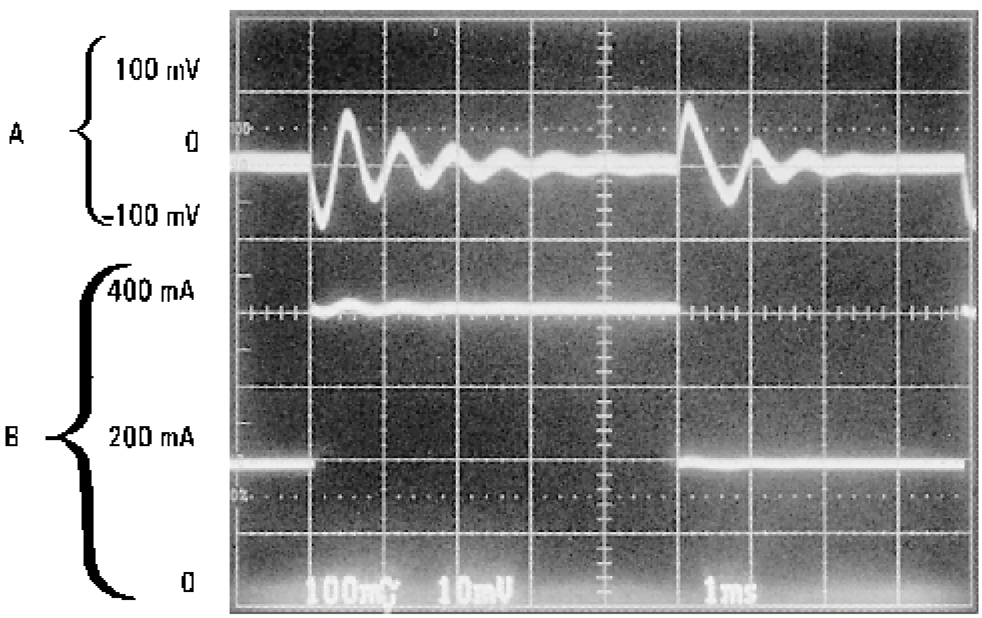
Vertical: 1 A/div
Horizontal: 5 µs/div

Figure 5. Inductor Current



Vertical: 20 mV/div
Horizontal: 10 µs/div

Figure 6. Output Ripple Voltage



A: Output Voltage Change, 100 mV/div
B: Output Current, 200 mA/div
Horizontal: 10 ms/div

Figure 7. Load Step Response