

TAS3103A Errata

1 Impact of MCLK Frequency Change on PLL

1.1 Problem Description

When MCLK is stopped and then restarted, or if the MCLK frequency is changed, it is possible for the TAS3103A PLL to lose lock. This can cause the TAS3103A to enter an indeterminate state.

1.2 System Impact

One or more register values may be corrupted, the device may stop responding to I²C communication, or the device may not operate correctly.

1.3 Workaround

Reset the device when changing MCLK frequency and when stopping and restarting MCLK.

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