

Application Report SPMA041G–January 2012–Revised October 2015

# Using the CMSIS DSP Library in Code Composer Studio™ for TM4C MCUs

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#### ABSTRACT

This application report describes the process required to build the ARM® CMSIS DSP library in Code Composer Studio v6.1 with ARM Compiler version up to 5.2.5. This document also describes how to use Code Composer Studio v6.1 to build, run, and verify the 11 ARM DSP example projects that are included in the CMSIS package.

Project collateral and source code discussed in this application report can be downloaded from the following URL: http://www.ti.com/lit/zip/spma041.

**NOTE:** This document applies to both the TM4C Series and the Stellaris® Cortex®-M4 MCUs. All screen captures reflect the TM4C version of the device.

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#### 1 Introduction

Many microcontroller-based applications can benefit from the use of an efficient digital signal processing (DSP) library. To that end, ARM has developed a set of functions called the CMSIS DSP library that is compatible with all Cortex M3 and M4 processors and that is specifically designed to use ARM assembly instructions to quickly and easily handle various complex DSP functions. Currently, ARM supplies example projects for use in their Keil uVision IDE that are meant to show how to build their CMSIS DSP libraries and run them on an M3 or M4. This application report details the steps that are necessary to build these DSP libraries inside Code Composer Studio version 6 and run these example applications on a TM4C Series TM4C129 Connected LaunchPad.

### 2 CMSIS DSP Library

To build the CMSIS DSP library, download and extract the source code from the ARM CMSIS website: http://cmsis.arm.com. The source code for the DSP library and example projects are in this directory:

CMSIS-<version>/CMSIS/DSP\_Lib

A full description of the DSP libraries, including a description of examples, the data structures used, and an API for each available function, is in the ARM-provided documentation at this location:

1



CMSIS DSP Library

2

CMSIS-<version>/CMSIS/Documentation/DSP/html/index.html



If ARM releases a future update to CMSIS, you might need to download and install a patch to the DSP library in order to provide support for new functionality and to fix any bugs that ARM discovers in the CMSIS source code. After you download the patch files from the ARM web site, follow these instructions to install:

- 1. Unzip the patch file.
- 2. Navigate to the patch directory and copy any files found in that directory to the corresponding location of the CMSIS DSP library.
- 3. Overwrite existing files when prompted.

For example, if the patch directory contains a file named arm\_common\_tables.c in the CMSIS/DSP\_Lib/Source/CommonTables directory, copy this file into the same directory (CMSIS/DSP\_Lib/Source/CommonTables) of your original CMSIS installation, overwriting the arm\_common\_tables.c that already exists in the original installation directory.

After the CMSIS source code has been downloaded, you must download and unzip the CCS CMSIS Patch Files. This CCS CMSIS zip package is located on the Texas Instruments' website at <a href="http://www.ti.com/lit/zip/spma041">http://www.ti.com/lit/zip/spma041</a>. The zip package contains a set of support files that are needed for building and running the CMSIS DSP library in Code Composer Studio. After you download the zip package, run the unzip application and select a location in which to extract the files.

### 3 Building the DSP Library in Code Composer Studio v6.1

This section details the steps required to build the ARM CMSIS DSP library from source. It is possible to skip this section by using a precompiled .lib (such as one of those found in CMSIS-<version>/CMSIS/Lib/ARM or CMSIS-<version>/CMSIS/Lib/GCC), but doing so requires changing the Code Composer Studio compiler settings to call floating-point functions in a way that is different from the default Code Composer Studio settings. This requires rebuilding all .lib files that are used in a project with the DSP libraries, most notably the TivaWare<sup>™</sup> for C Series Software driverlib, grlib, and usblib libraries. This method is not recommended and the process is not described in this application report. Also this application report has been updated for the support of CMSIS release r4p2 onwards.

### 3.1 Adding the CCS-Required Header Files to the DSP Libraries

To compile the CMSIS DSP libraries using Code Composer Studio, you must modify the DSP library include files, add a Code Composer Studio specific include file, and add a new assembly file. The zip package contains pre-modified versions of these files, which can be used during the build process or you can elect to modify the files yourself by using the following steps:

- 1. Copy arm\_math.h and cmsis\_ccs.h from this application report into the CMSIS/Include directory
- 2. Copy arm\_bitreversal2.asm from this application report into CMSIS/DSP\_Lib/Source/TransformFunctions.

### 3.2 Creating the dsplib Project

Before building the DSP library in Code Composer Studio, you must create a project for the library. You can build a project by completing the following steps:

- 1. Launch CCSv6.1 and select an empty workspace.
- 2. Select File  $\rightarrow$  New  $\rightarrow$  CCS Project. The New Code Composer Studio Project window will be displayed.
- 3. Select Target as TM4C Series and then use the drop down menu to select TM4C1294NCPDT. Select the Connection as Stellaris In-Circuit Debug Interface (see Figure 1).
- 4. In the Project name, type dsplib-cm4f and keep the check box ticked for the Use default location.
- 5. In Advanced settings, select:
  - Output type: Static Library
  - Output format: eabi (ELF)
  - Device endianness: little
- 6. In Project templates and examples (see Figure 2), select Empty Project.
- 7. Click **Finish** to create the project. The dsplib-cm4f project appears in the Project Explorer.



😵 New CCS Project	😨 New CCS Project
CCS Project Create a new CCS Project.	CCS Project Create a new CCS Project.
Iarget:       Tiva C Series       Tiva TM4C1294NCPDT         Connection:       Stellaris In-Circuit Debug Interface       Verify         Contex M [ARM]       Project name:       dsplib-cm4f         Use gefault location       Location:       CAUsersha0876236\workspace_bootloader\dsplib-cm4f       Browse         Compiler version:       Tiv5.2.5       More         • Advanced settings       Output type:       Static Library       •         Output format:       eabi (ELF)       •       Browse         Linker command file:       •       Browse       Browse         Project templates and examples       •       Project templates and examples	Iarget:       Tiva C Series       Tiva TM4C1294NCPDT         Connection:       Stellaris In-Circuit Debug Interface       Verify         Cortex M [ARM]       Project name:       dsplib-cm4f         I Use gefault location
Image: Seck         Mext >         Finish         Cancel	(2)     < Back

Figure 1. Creating the dsplib Project

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Figure 2. Creating the dsplib Project

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### 3.3 Adding the dsplib Source Code

Before adding the dsplib source code to the project, you should familiarize yourself with the CMSIS library structure. Open your preferred file navigation tool and navigate to the directory where the CMSIS .zip file downloaded from ARM was extracted. Then, descend to CMSIS-<*version*>/CMSIS/DSP\_Lib/Source/. This is the directory ARM uses to group the DSP functions into various sub-categories. The ARM directory contains the project files necessary to build the DSP library in uVision with ARM's compiler, and the GCC directory contains the project files to build the DSP library in uVision using the open source GCC compiler. All other directories contain the source code necessary to build the category of functions indicated by the directory name.

To add the dsplib source code to the dsplib project in Code Composer Studio:

- 1. Right-click the dsplib-cm4f project in the Project Explorer and click Import...
- 2. Click General to expand and then click File System. Click Next.
- 3. Click the Browse button and navigate to the location of the CMSIS DSP library source code.
- 4. Select the top level Source directory and click OK.
- 5. When the Source directory appears in the Import window, click the checkbox beside the folder to select all of the contents of that folder to be imported.
- 6. Deselect the ARM and GCC folders by clicking to the left of the checkbox.
- 7. Click the TransformFunctions folder, which causes the contents of that folder to be displayed in the panel on the right.
- 8. Uncheck the box beside arm\_bitreversal2.S.
- 9. Make sure that the *Into Folder:* text field contains the name of the DSP library project where you want to import the files (for this example, dsplib-cm4f).
- 10. Check the Overwrite existing resources without warning. Verify the *Create top-level folder* check box is deselected.
- 11. Click the Advanced button, then click to select the Create links in workspace checkbox.
- 12. Verify the Create link locations relative to: checkbox is selected. If it is not, click to select it.



Building the DSP Library in Code Composer Studio v6.1

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13. Verify that the drop-down menu of environment variables is set to PROJECT\_LOC (see Figure 3). If there are no variables listed in the drop-down menu, select Edit Variables... and add a variable to represent the location of the dsplib project file.

😯 Import		
<ul> <li>Import</li> <li>File system         Import resources from the local file system.     </li> <li>From directory: C:\CMSIS-SP-00300-r4p2-00rel0\CMSIS\D9         Import P2-00rel0\CMSIS\D9         Import P2-00rel0\CMSIS\D9         Import P2-00rel0\CMSIS\D9      </li> <li>Import P2-00rel0\CMSIS\D9         Import P2-00rel0\CMSIS\D9      </li> <li>Import P2-00rel0\CMSIS\D9      </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li> <li>Import P2-00rel0\CMSIS\D9         </li></ul>	SP_Lib\Source SP_Lib\Source	Browse
▶       ✓ ➢ FilteringFunctions         ▶       ✓ ➢ GCC         ▶       ✓ ➢ MatrixFunctions         ▶       ✓ ➢ StatisticsFunctions         ▶       ✓ ➢ SupportFunctions         ▶       ✓ ➢ SupportFunctions         Filter Types       Select All         Into folder:       dsplib-cm4f	<ul> <li>arm_cfft_radix2_init_f32.c</li> <li>arm_cfft_radix2_init_q15.c</li> <li>arm_cfft_radix2_init_q31.c</li> <li>arm_cfft_radix2_q15.c</li> <li>arm_cfft_radix2_q31.c</li> </ul>	Ţ.
Options          Options         Overwrite existing resources without warning         Create top-level folder         << Advanced	▼	
?	< <u>B</u> ack <u>N</u> ext > <u>Finish</u>	Cancel

Figure 3. Importing the DSP\_Lib Source Code



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14. Click Finish to link the DSP\_Lib source code into the project.



Figure 4. The Project Explorer Window After the DSP\_Lib Code has Been Imported

### 3.4 Editing the dsplib Project Settings

After linking in all the source files, change the following default Code Composer Studio project settings:

- 1. Right-click the dsplib-cm4f project in the Project Explorer and select Properties.
- 2. Expand the Build entry, and then expand the ARM Compiler entry.
- 3. Confirm that the *Target\_processor version (--silicon\_version, -mv)* entry matches your processor in the Processor Options panel (see Figure 5). For this example, the Target processor should be 7M4 (as opposed to 7M3 for any of the Stellaris Cortex-M3 products).



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Properties for dsplib-cm4f		
type filter text	Processor Options	$\diamondsuit \bullet \bullet \Rightarrow \bullet$
<ul> <li>Resource</li> <li>General</li> <li>Build</li> <li>ARM Compiler</li> <li>Processor Options</li> </ul>	Configuration: Debug [ Active ]	▼ Manage Configurations
Optimization Include Options	Target processor version (silicon_version, -mv)	7M4 •
MISRA-C:2004	Designate code state, 16-bit (thumb) or 32-bit (code_state)	<b>1</b> 6 <b>•</b>
<ul> <li>Advanced Options</li> <li>ARM Archiver</li> </ul>	Specify floating point support (float_support)	FPv4SPD16
Debug	Application binary interface. [See 'General' page to edit] (abi)	eabi 👻
Show advanced settings		OK Cancel

Figure 5. The Processor Settings for a Cortex-M4 Processor With Hardware FPU Support



4. Click the *Optimization level (--opt\_level, -O)* drop-down menu in the Optimization panel and select 2 (see Figure 6).

Properties for dsplib-cm4f		
type filter text	Optimization	<b>(-) ▼ (-) ▼ ▼</b>
<ul> <li>Resource</li> <li>General</li> <li>Build</li> <li>ARM Compiler</li> <li>Processor Options</li> </ul>	Configuration: Debug [Active]	Manage Configurations
Optimization Include Options	Optimization level (opt_level, -O)	2 Global Optimizations
MISRA-C:2004	Speed vs. size trade-offs (opt_for_speed, -mf)	None 0 size 5 speed
	Floating Point mode (fp_mode)	▼
Show advanced settings		OK Cancel

Figure 6. The Proper Optimization Settings for Compiling the DSP\_Lib Source Code

5. Expand the Advanced Options section of the ARM Compiler pane, and select Assembler Options.



Building the DSP Library in Code Composer Studio v6.1

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6. Click the Use unified assembly language (--ual) checkbox to select that option (see Figure 7).

Properties for dsplib-cm4f		
type filter text	Assembler Options	\$
<ul> <li>Resource</li> <li>General</li> <li>Build</li> <li>ARM Compiler</li> <li>Brossnor Octions</li> </ul>	Configuration: Debug [Active]	Manage Configurations
Optimization Include Options MISRA-C:2004 Advanced Options Advanced Debug Options Language Options	Keep the generated assembly language (.asm) file (keep_asm, -k) Source interlist Generate listing file (asm_listing, -al) Keep local symbols in output file (output_all_syms, -as) Do not generate clink for const sections (no const clink)	
Parser Preprocessing Opti Predefined Symbols Diagnostic Options Runtime Model Options	Simulate source '.copy filename' (copy_file, -ahc)	<b>월</b> 🌒 월 중[ 윤]
Advanced Optimizations Entry/Exit Hook Options Library Function Assumpt Assembler Options File Type Specifier Directory Specifier Default File Extensions Command Files ARM Archiver Debug	Symbol names are not case-significant (syms_ignore_case, -ac) Undefine assembly symbol NAME (asm_undefine, -au)	<b>원</b> 씨 원 상  산
	Pre-define assembly symbol NAME (asm_define, -ad)	<b>월</b> 🌒 🗟 장  문
	Generate first-level assembly include file list (asm_includes, -api) Generate assembly dependency information (asm_dependency, -apd) Use unified assembly language (ual)	<u>B</u> rowse
	Simulate source '.include filename' (include_file, -ahi)	<b>월</b> 🌒 🗟 주  산
4	Generate cross reference file (cross_reference, -ax)	
Show advanced settings		OK Cancel

Figure 7. Setting the Assembler to Use Unified Assembly Language



7. Click the Emit diagnostic identifier numbers (--display\_error\_number, -pden) checkbox in the Diagnostic Options panel to deselect.

Properties for dsplib-cm4f		
type filter text	Diagnostic Options	$\Leftrightarrow \bullet \Rightarrow \bullet$
<ul> <li>Resource</li> <li>General</li> <li>Build</li> <li>ARM Compiler</li> </ul>	Configuration: Debug [ Active ]	Manage Configurations)
Processor Options Optimization Include Options MISRA-C:2004 Advanced Options Advanced Debug Options	Quiet Level Treat diagnostic <id> as remark (diag_remark, -pdsr)</id>	▲ 월 월 상 말
Language Options Parser Preprocessing Opti Predefined Symbols Diagnostic Options	Treat diagnostic <id> as error (diag_error, -pdse)</id>	<ul> <li>월 월 월 등 [ £ ]</li> </ul>
Kuntime Model Options Advanced Optimizations Entry/Exit Hook Options Library Function Assumpt Assembler Options File Type Specifier	Emit diagnostic identifier numbers (display_error_number, -pden)     Treat warnings as errors (emit_warnings_as_errors, -pdew)     Suppress diagnostic <id> (diag_suppress, -pds)</id>	(1 산 년 월 (1 산 1
Directory Specifier Default File Extensions Command Files ARM Archiver Debug	Issue remarks (issue_remarks, -pdr) Verbose diagnostics (verbose_diagnostics, -pdv) Treat diagnostic <id> as warning (diag_warning, -pdsw)</id>	图 图 资 [ 公]
	ZZS Set error limit to <count> (set_error_limit, -pdel) Generate user information file (gen_aux_user_info, -b) Output diagnostic to .err file (write_diagnostics_file, -pdf)</count>	
Show advanced settings	Suppress warnings (no_warnings, -pdw) Wrap diagnostic messages (diag_wrap) Off	OK Cancel

Figure 8. Verifying That Diagnostic Identifier Numbers will not be Emitted



Building the DSP Library in Code Composer Studio v6.1

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8. Add the DSP library CMSIS-<version>/CMSIS/Include directory to the compiler's include path in the Include Options panel. This is done by pressing the Add button by the Add dir to #include search path (--include\_path, -I) (see Figure 9), then either typing in the path to the CMSIS Include directory or clicking Browse and navigating to the Include directory and navigating to the Include directory (CMSIS-<version>/CMSIS/Include).

Properties for dsplib-cm4f	
type filter text	Include Options $\diamondsuit$ $\checkmark$ $\checkmark$ $\checkmark$
General Build ARM Compiler Processor Options Optimization	Configuration: Debug [Active]
Include Options MISRA-C:2004	Add dir to #include search path (include_path, -I) 🛛 🗿 🖓 🖓
ULP Advisor Advanced Options ARM Archiver	Image: Add directory path         Directory:         C:\CMSIS-SP-00300-r4p2-00rel0\CMSIS\Include         Workspace         Variables         Browse         Image: Add directory path         Image: C:\CMSIS-SP-00300-r4p2-00rel0\CMSIS\Include         Image: C:\CMSIS-SP
Show advanced settings	OK Cancel

Figure 9. Adding the CMSIS Top Level Include Directory to the Compiler's #include Search Path



9. Expand the Advanced Options menu again and select the Predefined Symbols panel. Create a symbol to tell the DSP library to use Cortex-M4 based math functions. Click the Add... button in the Pre-define NAME (--define, -D) area. In the Enter Value dialog box, type ARM\_MATH\_CM4 into the Pre-define NAME (--define, -D) field and click OK (see Figure 10). Click the Add... button again type \_\_\_FPU\_PRESENT=1 into the *Pre-define NAME (--define, -D)* field and click **OK**.

Properties for dsplib-cm4f		
type filter text	Predefined Symbols	↓ ↓ ↓ ▼
General		
⊿ Build		
ARM Compiler	Configuration: Debug [ Active ]	Manage Configurations
Processor Options		
Optimization		
Include Options		
MISRA-C:2004	Pre-define NAME (define, -D)	🗐 🛍 📓 습니 ½
ULP Advisor	ARM_MATH_CM4	
Advanced Options		
Advanced Debug Options		
Language Options		
Parser Preprocessing Opti		
Predefined Symbols		
Diagnostic Options		
Runtime Model Options		
Advanced Optimizations		
Entry/Exit Hook Options		
Assemblar Ontions	Undefine NAME (undefine, -U)	🛃 🔊 🗟 신 문)
File Type Specifier		
Directory Specifier		
Default File Extensions		
Command Files		
ARM Archiver		
,		
4		
Show advanced settings		OK Cancel

Figure 10. Adding Project Level #defines for the Processor Characteristics



10. Select the *on* option from the *Place each function in a separate subsection (--gen\_func\_subsections, - ms)* drop-down menu in the Runtime Model Options panel (see Figure 11).

Properties for dsplib-cm4f		
type filter text	Runtime Model Options	↓ ↓ ↓
Resource		1.
General		
⊿ Build	Configuration: Debug [ Active ]	▼ Manage Configurations
▲ ARM Compiler		
Processor Options Optimization		
Include Options	Place each function in a separate subsection (gen_func_subsections, -ms)	on
MISRA-C:2004	Use linker-generated file to remove dead functions (use_dead_funcs_list)	
Advanced Options Advanced Debug Options	Allow reassociation of sat arithmetic (sat_reassoc)	off 🔹
Language Options	Allow reassociation of FP arithmetic (fp_reassoc)	off 🗸
Parser Preprocessing Opti	Enums may be char/short, instead of int (small_enum,small-enum)	
Diagnostic Options	Enable dynamic stack overflow checking (stack_overflow_check, -mo)	
Runtime Model Options	Set the size (in bits) of the C/C++ type wchar_t (16,32) (wchar_t)	<b>1</b> 6 <b>•</b>
Advanced Optimizations Entry/Exit Hook Options	Specify whether constants can be embedded in code sections (embedded_constants)	on 🔹
Library Function Assumpt	Generates SIMD instructions targeting Neon (neon)	
Assembler Options File Type Specifier	Specify length of maximum branch chain (max_branch_chain, -ab)	
Directory Specifier	Chars signed by default (signed_chars, -mc)	
Default File Extensions	No dual state support (disable_dual_state, -md)	
Command Files	Compile for breakpoint-based profiling (profile:breakpt)	
ARM Archiver Debug	Generate unaligned loads and stores (unaligned_access)	on 🔹
bebug	Designate enum type (Default is packed for EABI) (enum_type)	packed 🔹
	Force alignment of structures to <bytecount> bytes (align_structs)</bytecount>	
	Reserve as global register (global_register, -r)	
	Compile for power profiling (profile:power)	
	Enable 16 bit code (thumb_state, -mt)	
	Use ELF common symbols (common)	on •
Show advanced settings		OK Cancel

Figure 11. The Proper Runtime Model Options for Compiling the DSP\_Lib Source Code

#### 3.5 Building the dsplib Source Code

Build the CMSIS DSP libraries by right-clicking dsplib-cm4f in the Project Explorer and selecting *Build Project.* Depending on hardware, this build might take up to ten minutes to complete. After the build is finished, the resulting dsplib-cm4f.lib file is created in the Debug folder of the project workspace.

**NOTE:** The CMSIS file structure currently contains a directory located at CMSIS-<*version>/*CMSIS/Lib that is intended for storing compiled library files. It is recommended for organization's sake that this directory be used for storing the Code Composer Studio compiled CMSIS DSP libraries. To do so, create a CCS/M4 sub-directory inside CMSIS/Lib, then copy the .lib that was generated by the above steps into the Code Composer Studio sub-directory.

#### 4 ARM Example Projects

The ARM CMSIS download contains eleven example projects that demonstrate how to use the various DSP library functions. This section details the steps required to create the same projects in Code Composer Studio v6.1, compile the projects, and run them on a TM4C microcontroller. These steps are focused on running the code on an EK-TM4C129 Connected LaunchPad, but can be easily modified to work with any other TM4C or Stellaris MCU (see http://www.ti.com/tool/ek-tm4c1294xl).



### 4.1 Creating the ARM Example Projects

The source code for all of the example projects can be found at CMSIS-

<version>/CMSIS/DSP\_Lib/Examples. Projects for each of the ARM examples can be created in Code Composer Studio via the following steps:

- 1. Launch CCSv6.1 and select either an empty workspace or the workspace used in the previous section to build the DSP library.
- 2. Select File > New > CCS Project. The New CCS Project window will be displayed.
- 3. Type ti\_cortexM4\_<example name> in the *Project name* field.
- 4. Select Executable from the Output Advanced settings.
- 5. Make the following selections from the Target Device:
  - Target: TM4C Series
  - TM4C1294NCPDT
  - Connections: Stellaris In-Circuit Debug Interface
- 6. Select the Empty Project in the Project templates and examples field.
- 7. Click **Finish** to create the project (see Figure 12 and Figure 13). The project now appears in the Project Explorer.



ARM Example Projects

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😵 New CCS Project 📃 📃 💌						
CCS Project Create a new CCS Project.						
Target: Connection:	Target:       Tiva C Series       Tiva TM4C1294NCPDT         Connection:       Stellaris In-Circuit Debug Interface       Verify					
Cortex N	1 [ARM]					
Project nam	ne: ti_corte	exM4_arm_matrix_example				
Loc Compiler ve	ation: C:\Use	rs\a0876236\workspace_bootloader\ti_cortexM4_arm_m	Browse More			
✓ Advanced	l settings					
Output typ	pe:	Executable 🔹				
Output for	rmat:	eabi (ELF) 👻				
Device end	dianness:	little 🗸				
Linker con	nmand file:	<automatic></automatic>	Browse			
Runtime s	upport library:	<automatic></automatic>	Browse			
Project templates and examples						
?		< Back Next > Finish	Cancel			

Figure 12. The New CCS Project Window With Options Set to Build the arm\_dotproduct\_example Project

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|--|

😵 New CCS Pr	oject			
CCS Project Create a new	CCS Project.			
Target: Connection:	Tiva C Series     Tiva TM4C1294NCPDT       Stellaris In-Circuit Debug Interface <ul> <li> </li></ul>	Verify		
Cortex N Project nan	ne: ti_cortexM4_arm_matrix_example			
Use defa	ault location cation: C:\Users\a0876236\workspace_bootloader\ti_cortexM4_arm_m	Browse		
Compiler v	ersion: TI v5.2.5	More		
✓ Project te	emplates and examples	itialized for		
type filter text       Creates an empty project fully initialized for the selected device.         Empty Project       the selected device.         Empty Project (with main.c)       Empty Assembly-only Project         Empty RTSC Project       Empty RTSC Project         Basic Examples       Hello World         SYS/BIOS       Total and the selected device.				
?	< Back Next > Finish	Cancel		

Figure 13. The New CCS Project Window With Options Set to Build the arm\_dotproduct\_example Project





#### Figure 14. The Project Explorer After the arm\_matrix\_example Project has Been Created

#### 4.2 Adding the Example Source Code

Once the project is created, it is necessary to point the project to the source files necessary for compilation:

- 1. Right-click the project in the Project Explorer and select Add Files...
- 2. Navigate to the CMSIS-<version>/CMSIS/DSP\_Lib/Examples/<example> directory. Select all of the C source file and click **Open** (see Figure 15).

Add files to ti_cortexM4_arm_matrix_example							
Organize  Vew fold	ler				:==	•	0
🔆 Favorites	Name	Date modified	Туре	Size			
🧮 Desktop	🐌 RTE	28-Nov-14 3:11 PM	File folder				
🗼 Downloads	Abstract.txt	23-Sep-14 11:57 AM	TXT File	1 KB			
🗐 Recent Places	🗿 arm_matrix_example.ini	23-Sep-14 11:57 AM	Configuration sett	1 KB			
	arm_matrix_example.uvoptx	23-Sep-14 11:57 AM	UVOPTX File	29 KB			
ز Libraries	🖻 arm_matrix_example.uvprojx	23-Sep-14 11:57 AM	µVision5 Project	130 KB			
	arm_matrix_example_f32.c	23-Sep-14 11:57 AM	C File	9 KB			
🖳 Computer	math_helper.c	28-Nov-14 11:00 P	C File	11 KB			
🏭 OSDisk (C:)	math_helper.h	23-Sep-14 11:57 AM	H File	4 KB			
🛛 🕅 Data (D:)							
📭 Network							
Filer	name: "math_helper.c" "arm_matrix_example_	_f32.c"		▼ **			-
					Open 😽	Cance	I

Figure 15. Adding the Source Files for arm\_matrix\_example to the Project



 Select the Link to files radio button, check the Create link locations relative to: checkbox, and select PROJECT\_LOC from the drop-down menu when the File Operation dialog box appears (see Figure 16). Press OK to add the source file(s) to the project.

File Operation		×
Select how files should be imported into th	e project:	
Copy files		
Link to files		
Create link locations relative to:	PROJECT_LOC	•
Configure Drag and Drop Settings		
?	ОК	Cancel

Figure 16. Selecting the Proper Options to Link the Source Files Into the Project

### 4.3 Editing the Example Project Settings

Before building the example projects, it is necessary to properly configure the project settings:

- 1. Right-click the project in the Project Explorer and select *Properties*.
- 2. Expand the *Build* entry, and then expand the *ARM Compiler* entry.
- 3. Confirm that the *Target\_processor version (--silicon\_version, -mv)* entry matches your processor in the Processor Options panel (see Figure 17). For this example, the Target processor should be 7M4 (as opposed to 7M3 for any of the Stellaris Cortex-M3 products).

Properties for ti_cortexM4_arm_matrix_e	xample	
type filter text	Processor Options	⇔ ▼ ⇔ ▼
General		
⊿ Build		
ARM Compiler	Configuration: Debug [ Active ]	<ul> <li>Manage Configurations</li> </ul>
Processor Options		
Optimization		
Include Options	Transferration ( siling service and)	7644
MISRA-C:2004	larget processor version (silicon_version, -mv)	71014
Advanced Options	Designate code state, 16-bit (thumb) or 32-bit (code_state)	16 -
Advanced Debug Options	Constitution print support ( floot support)	
Language Options	specify floating point support (float_support)	FPV4SPD10
Parser Preprocessing Opti	Application binary interface. [See 'General' page to edit] (abi)	eabi 👻
Predefined Symbols	✓ Little endian code [See 'General' page to edit] (little endian	-me)
Diagnostic Options		, , , , , , , , , , , , , , , , , , ,
Advanced Optimizations		
Entry/Exit Hook Ontions		
Library Function Assumpt		
Assembler Options		
File Type Specifier		
Directory Specifier		
Default File Extensions		
Command Files		
ARM Linker		
ARM Hex Utility [Disabled]		
۰ III +		
Show advanced settings		OK Cancel

Figure 17. The Processor Options Used for Building the Example on a Cortex-M4 Process With hardware FPU Support



4. Click the *Optimization level (--opt\_level, -O)* drop-down menu and select 2 in the Optimization panel (see Figure 18).

Properties for ti_cortexM4_arm_matrix_ex	xample	
type filter text	Optimization	
▷ Resource		
General		
⊿ Build	Configuration: Debug [ Active ]	<ul> <li>Manage Configurations</li> </ul>
▲ ARM Compiler		
Processor Options		
Optimization		
Include Options	Optimization level (opt_level, -O)	2 Global Optimizations
MISRA-C:2004		
ULP Advisor	Speed vs. size trade-offs (opt_for_speed, -mf)	0
Advanced Options		none 0 size 5 speed
Advanced Debug Opti	Floating Point mode (fp_mode)	strict
Language Options		
Parser Preprocessing C 😑		
Predefined Symbols		
Diagnostic Options		
Runtime Model Option		
Advanced Optimizatio		
Entry/Exit Hook Option		
Library Function Assur		
Assembler Options		
File Type Specifier		
Directory Specifier		
Default File Extensions		
Command Files		
ARM Linker		
ARM Hex Utility [Disabled]		
Show advanced settings		OK Cancel

### Figure 18. The Proper Optimization Settings for Compiling the Example Projects

5. Expand the Advanced Options section of the ARM Compiler pane, and select Assembler Options.



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6. Click the Use unified assembly language (--ual) checkbox to select that option (see Figure 19).

Properties for ti_cortexM4_arm_matrix_e	xample	
type filter text	Assembler Options	↓ ↓ ↓ ↓
General Build ARM Compiler Processor Options	Configuration: Debug [ Active ]	Manage Configurations
Include Options MISRA-C:2004 Advanced Options Advanced Debug Options Language Options Parser Preprocessing Opti	<ul> <li>Keep the generated assembly language (.asm) file (keep_asm, -k)</li> <li>Source interlist</li> <li>Generate listing file (asm_listing, -al)</li> <li>Keep local symbols in output file (output_all_syms, -as)</li> <li>Do not generate .clink for .const sections (no const clink)</li> </ul>	v
Predefined Symbols Diagnostic Options Runtime Model Options Advanced Optimizations	Simulate source '.copy filename' (copy_file, -ahc)	4월 紀 월 등 문」
Entry/Exit Hook Options Library Function Assumpt	Symbol names are not case-significant (syms_ignore_case, -ac)	
Assembler Options File Type Specifier Directory Specifier	Undefine assembly symbol NAME (asm_undefine, -au)           Aliases:asm_undefine, -au           Undefine assembly symbol NAME	<mark>劉</mark> 紀 魯 行 산
Command Files ▷ ARM Linker ARM Hex Utility [Disabled]	Pre-define assembly symbol NAME (asm_define, -ad)	🛃 🗐 🖉 상 산
	Generate first-level assembly include file list (asm_includes, -api)	, ,
	Generate assembly dependency information (asm_dependency, -apd)           Image: Constraint of the system of the	<u>B</u> rowse
	Simulate source '.include filename' (include_file, -ahi)	• 월 起 월 한 문
۰ III • • • • • • • • • • • • • • • • •	Generate cross reference file (cross_reference, -ax)	
Show advanced settings		OK Cancel

Figure 19. The Proper Assembler Options Needed for Compiling the Example Projects

7. Expand the Advanced Options menu again and select the Predefined Symbols panel. Create a symbol to tell the DSP library to use Cortex-M4 based math functions. Click the Add... button in the Pre-define NAME (--define, -D) area. In the Enter Value dialog box, type ARM\_MATH\_CM4 into the Pre-define NAME (--define, -D) field and click OK (see Figure 20). Click the Add... button in the Pre-define NAME (--define, -D) area. In the Enter Value dialog box, type \_\_FPU\_PRESENT=1 into the Pre-define NAME (--define -D) field and click OK.

Properties for ti_cortexM4_arm_matrix_e	xample	
type filter text	Predefined Symbols	↓ ↓ ↓ ↓
General		
⊿ Build		^
ARM Compiler	Configuration: Debug [ Active ]	<ul> <li>Manage Configurations</li> </ul>
Processor Options		
Optimization		
Include Options		
MISRA-C:2004	Pre-define NAME (define, -D)	19 11 11 11 11 11 11 11 11 11 11 11 11 1
ULP Advisor	CCS= "CCS" DART_TMAC1204NCDDT	
Advanced Options	ARM MATH CM4	
Advanced Debug Options	FPU_PRESENT=1	
Language Options		
Parser Preprocessing Opti		
Predefined Symbols		-
Diagnostic Options		E
Advanced Optimizations		
Entry/Exit Hook Ontions		
Library Function Assumpt	Undefine NAME (undefine, -U)	🛃 🔊 🗟 친 문
Assembler Options		
File Type Specifier		
Directory Specifier		
Default File Extensions		
Command Files		
ARM Linker		
ARM Hex Utility [Disabled]		
< >		-
Show advanced settings		OK Cancel

Figure 20. Adding the Pre-Processor Statements Necessary for Building an Example Project on a Cortex-M4 Part With Hardware FPU Support



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8. Look for the Add button to #include search path (--include\_path, -I) field in the Include Options section (see Figure 22).

Properties for ti_cortexM4_arm_matrix_e	xample and a second	
type filter text	Include Options	← → ⇒ →
<ul> <li>Resource</li> <li>General</li> <li>Build</li> <li>ARM Compiler</li> <li>Processor Options</li> <li>Optimization</li> </ul>	Configuration: Debug [ Active ]	▼ Manage Configurations
Include Options MISRA-C:2004 ULP Advisor ▲ Advanced Options Advanced Options Language Options Parser Preprocessing Opti Predefined Symbols Diagnostic Options Runtime Model Options Advanced Optimizations Entry/Exit Hook Options Library Function Assumpt Assembler Options File Type Specifier Directory Specifier Default File Extensions Command Files ▲ ARM Linker ARM Linker ARM Hex Utility [Disabled] Debug	"SICG TOOL ROOTI/include" "C:(CMSIS-SP-00300-r4p2-00rel0/CMSIS\Include"  © Edit directory path © irectory: © (C:(CMSIS-SP-00300-r4p2-00rel0/CMSIS\Include" © Workspace Variables Browse Spt © K Cancel	<ul> <li> <ul> <li> </li> <li> <li></li></li></ul></li></ul>
Show advanced settings		OK Cancel

Figure 21. Compiler's #include Search Path Modified to Contain Both the Base CMSIS Include Directory and the Example Projects' Common Include Directory



 Click the Add... button again, then the Browse button and browse to the Include directory located in the CMSIS directory, then click OK (see Figure 21).

Browse For Folder	x
Select a folder from file system:	
▲ 📕 CMSIS-SP-00300-r3p2-00rel1	*
A 📕 CMSIS	
Documentation	
DSP_Lib	
🔒 Include	
Dib Lib	
📔 RTOS	
SVD	
Device	Ŧ
Folder: Include	
Make New Folder OK Cance	

Figure 22. Using the File System Option to Add the Base CMSIS Include Directory to the Compiler's #include Search Path



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10. Select the *on* option from the *Place each function in a separate subsection (--gen\_func\_subsections, - ms)* drop-down menu (see Figure 23), in the Runtime Model Options panel.

Properties for ti_cortexM4_arm_matrix_e	xample	- 0 <b>X</b>
type filter text	Runtime Model Options	↓ → ⇒ ▼
General Build ARM Compiler Processor Options Optimization	Configuration: Debug [Active]	ge Configurations
Include Options MISRA-C:2004 ULP Advisor	Place each function in a separate subsection (gen_func_subsections, -ms) Use linker-generated file to remove dead functions (use_dead_funcs_list)	on 💌
<ul> <li>Advanced Options Advanced Debug Options</li> </ul>	Allow reassociation of sat arithmetic (sat_reassoc)	off 🔹
Language Options Parser Preprocessing Opti	Allow reassociation of FP arithmetic (fp_reassoc)	off 🔹
Predefined Symbols Diagnostic Options	Enums may be char/short, instead of int (small_enum,small-enum) Enable dynamic stack overflow checking (stack_overflow_check, -mo)	
Runtime Model Options Advanced Optimizations	Set the size (in bits) of the C/C++ type wchar_t (16,32) (wchar_t)	16 •
Entry/Exit Hook Options Library Function Assumpt	Specify whether constants can be embedded in code sections (embedded_constants) Generates SIMD instructions targeting Neon (neon)	on 🔻
Assembler Options File Type Specifier	Specify length of maximum branch chain (max_branch_chain, -ab)	
Directory Specifier Default File Extensions	<ul> <li>Chars signed by default (signed_chars, -mc)</li> <li>No dual state support (disable_dual_state, -md)</li> </ul>	
Command Files ARM Linker	Compile for breakpoint-based profiling (profile:breakpt)	
ARM Hex Utility [Disabled]	Designate enum type (Default is packed for EABI) (enum_type)	packed V
	Force alignment of structures to <bytecount> bytes (align_structs)</bytecount>	
	Reserve as global register (global_register, -r)	
	Compile to power profiling (profile.power)     Enable 16 bit code (thumb_state, -mt)	
	Use ELF common symbols (common)	on 🔹
۰ III ا	Specify how to treat plain chars (signed/unsigned) (plain_char)  Prevent generation of branch chains in Thumb mode (disable_branch_chaining)	unsigned 🔻
Show advanced settings	ОК	Cancel

### Figure 23. The Runtime Model Options Set Up for Compiling the Example Projects

- 11. Open the File Search Path panel in the ARM Linker section.
- 12. Create an entry for the precompiled CMSIS DSP binary (.lib) that will be used in the *Include library file or command file as input (--library, -l)* area (see Figure 24). For this example, the library file created in section three will be used, so click on the *Add...* button, then the *File system...* button and navigate to the location of the .lib you want to use. If you built the precompiled binary from scratch as detailed in section 3 without changing the default project location, the .lib will be found at C:\Users\<user\_name>\CCS workspaces\<your workspace>\dsplib-cm4f\Debug\dsplib-cm4f.lib. When you have found the binary, click **Open**, then **OK**.



Properties for ti_cortexM4_arm_matrix_e	cample	
type filter text	File Search Path	← → → → →
General		
⊿ Build	Configuration Debug [Active]	- Manage Carfingetions
ARM Compiler		• Manage Configurations
Ontimization		
Include Options		
MISRA-C:2004	Include library file or command file as input (library, -l)	🛃 📾 🖗 🖓 🖓 I
Advanced Options	"libc.a"	
Advanced Debug Options	C:\Users\au870230\Workspace_vo_u\dsplib-cm4t\Debug\dsplib-cm4t.lib	
Language Options		
Parser Preprocessing Opti Dredefined Symbols		
Diagnostic Options		
Runtime Model Options		
Advanced Optimizations		
Entry/Exit Hook Options		
Library Function Assumpt		
Assembler Options	Add adirs to library coards noth ( - coards noth - i)	
Directory Specifier		
Default File Extensions	"\${CG_TOOL_ROOT}/include"	
Command Files		
ARM Linker		
Basic Options		
File Search Path		
Advanced Options ARM Hey Utility [Disabled]		
Additive outility [Disabled]		
	Reread libraries; resolve backward references (reread_libs, -x)	
	Search libraries in priority order (priority, -priority)	
< >	Disable automatic RTS selection (disable_auto_rts)	
<ol> <li>Show advanced settings</li> </ol>		
<u>Show advanced settings</u>		Cancel

Figure 24. The Linker's File Search Path Modified to Include the dsplib Binary Compiled in Section 3



#### 4.4 Building, Running, and Verifying the Project

Once the project has been created, the source code has been added to the work space, and the project properties have been properly configured, the project can be built by right clicking on it in the Project Explorer and selecting Build Project.

If this is the first time that Code Composer Studio is being used to connect to a target via the Stellaris In-Circuit Debug Interface, it might be necessary to install the proper drivers before it is possible to connect to the target to run code. Instructions for doing this can be found in the Code Composer Studiov6.1 Quick Start Guide, available at http://processors.wiki.ti.com/index.php/Category:Code\_Composer\_Studio\_v6.

Once the code has been built and the proper drivers have been installed, you can run your code by using the following steps:



1. Press the *Debug* icon in the Code Composer Studio toolbar (see Figure 25).

Figure 25. The Debug Context Being Displayed After the arm\_matrix\_example Project has Been Set Up for Debugging

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<ol> <li>It takes a moment for Code Composer Studio to connect to the MCU and the connection has been established and the flash programmed with the MCU will run until it reaches the project's <i>main()</i> function (see Figure 26) F8) to cause the program to start executing.</li> </ol>	d download the code. Once compiled project code, the . Press the <i>Resume</i> button (or
🐞 Debug ⊠	🦹 ▽ 🗆 🗖
<ul> <li>Stellaris In-Circuit Debug Interface/CORTEX_M4_0 (Suspended - HW Breakpoin)</li> <li>main() at arm_matrix_example_f32.c:162 0x00000CE8</li> <li></li></ul>	nt) formation)
arm_matrix_example_f32.c 🔀	
<pre>146 int32_t main(void) 147 { 148 149 arm_matrix_instance_f32 A; /* Matrix A Instance */ 150 arm_matrix_instance_f32 AT; /* Matrix AT(A transpose) ins 151 arm_matrix_instance_f32 ATMA; /* Matrix ATMA( AT multiply w 152 arm_matrix_instance_f32 ATMAI; /* Matrix ATMAI(Inverse of AT 153 arm_matrix_instance_f32 B; /* Matrix B instance */ 154 arm_matrix_instance_f32 X; /* Matrix X(Unknown Matrix) in 155 156 uint32_t srcRows, srcColumns; /* Temporary variables */ 157 arm_status status; 158 159 /* Initialise A Matrix Instance with numRows, numCols and dat 160 srcRows = 4; 161 srcColumns = 4; 162 arm_mat_init_f32(&amp;A, srcRows, srcColumns, (float32_t *)A_f32) 163 14 </pre>	<pre>stance */ with A) instance */ TMA) instance */ instance */ ta array(A_f32) */ );</pre>
📮 Console 💥 📑 📑 📑 🖬 🖬 🖬	Diterre
ti_cortexIM4_arm_matrix_example CORTEX_M4_0: GEL Output: Memory Map Initialization Complete	0 items Description

Figure 26. The arm\_matrix\_example Project, After it has Been Loaded Into Flash and the Startup Code has run to the main() Function



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3. After a few seconds have passed, the program will run to completion (see Figure 27). Press the suspend button, which will halt the processor and show you what line of code is being executed.

<ul> <li>Debug X</li> <li>ti_cortexM4_arm_matrix_example [Code Com</li> <li>Stellaris In-Circuit Debug Interface/CORT</li> <li>main() at arm_matrix_example_f32.c:2</li> <li>_c_int00() at boot.asm:217 0x000011A2</li> </ul>	poser Studio - Device Debugging] EX_M4_0 (Suspended) 30 0x00000D82 ? (_c_int00 does not contain frame information)
arm_matrix_example_f32.c 🔀	
<pre>217 status = ARM_MATH_TEST_FAILURE; 218 } 219 220 221 /*</pre>	the PASS check. /* main function does not return */
-	
E Console 🛛	
CORTEX_M4_0: GEL Output: Memory Map Initialization Complete	*
	Writable Smart Insert

Figure 27. The arm\_matrix\_example Project Having Run to Successful Completion

4. For every function other than the class marks example, the program will have halted in one of two while loops. If the program did not successfully execute, it will be caught in a while loop surrounded by an if statement with a test condition of (status != ARM\_MATH\_SUCCESS). If the program did successfully execute, it is caught in a while loop found immediately after the previously mentioned if statement. For the class marks example, there is no built in method by which the microcontroller's execution state can be verified.



#### 4.5 Source Code Modifications

For almost all of the ARM example projects, the above steps can be followed in a similar manner to build and run the ARM-provided source code. There is one project, though, that require modifications to the source code to properly build and run on the TM4C123G Launchpad.

The linear interpolation example contains a table of values meant to represent a waveform of sin(x) as x goes from negative pi to 2\*pi by increments of 0.00005. This granularity causes the resulting compiled binary to be too large in size for the TM4C series launchpad. An alternate data file, ti\_linear\_interp\_data\_37968.c, has been provided along with this application report that represents the same array given increments of 0.00025 instead. This causes the compiled binary to be small enough to fit into a part with a flash size of 256 kB and an SRAM size of 32 kB for the TM4C123 Platform devices. This necessitates a change in the linear interpolation example code as well (as the size and name of the statically allocated array has been changed), so when adding the source code for this example, it is necessary to use the ti\_linear\_interp\_example\_f32.c file included with this application report

The linear interpolation example also contains a bug that might cause it to give the appearance of failing when executing. The purpose of the example is to show the difference in accuracy that can be achieved by using the CMSIS DSP library's linear interpolation sin function, which uses both cubic interpolation and linear interpolation to derive its return values, and the library's standard sin function, which uses only cubic interpolation. The method that is used to compare the accuracy of these two functions is to calculate the signal-to-noise ratio of both signals with respect to a pre-calculated signal that is known to be correct. Unfortunately, the method of using linear interpolation gives a result that almost exactly matches the pre-calculated signal, which causes the SNR function to attempt to take the log of a value divided by 0. As such, the function's self-test method cannot be assumed trustworthy. The user should instead use the debugger to verify that the 10-element-long arrays representing the sin values are indeed more accurate when using the linear interpolation functions than when using the standard functions. This can be done using the following steps:

- 1. Select the Expression view in the Code Composer Studio debugger context
- 2. Click *Add new expression*, and type in testRefSinOutput32\_f32. This will add the array containing the pre-calculated reference sin output to the expressions list.
- 3. Click the arrow to the left of testRefSinOutput32\_f32 to display all elements of the array.
- 4. Click *Add new expressions*, and type testOutput. This will add the array containing the sin values as calculated by the CMSIS DSP\_Lib sin function that uses cubic interpolation to the expression list.
- 5. Click the arrow to the left of testOutput to display all elements of the array.
- 6. Click *Add new expressions*, and type testLinIntOutput. This will add the array containing the sin values as calculated by the CMSIS DSPlib that uses both cubic and linear interpolation sin function to the expression list.
- 7. Click the arrow to the left of testLinIntOutput to display all elements of the array (see Figure 28).

<u> </u>	
( 'nnci	nusion
001101	usion

(X)= Variables	Registers	🖄 🍂 📄 🕂 💥 🔆	🖗   📬 🛃   🏟 🍸 🗖 🗖
Expression	Туре	Value	Address
▲ (= testRefSinOutput32_f32	float[10]	0x200001C0	0x200001C0
(×)= [0]	float	-0.6049607	0x200001C0
(×)= [1]	float	-0.5970903	0x200001C4
(×)= [2]	float	0.1871404	0x200001C8
(×)= [3]	float	0.4187721	0x200001CC
(×)= [4]	float	-0.9885888	0x200001D0
(×)= <mark>[</mark> 5]	float	0.9763384	0x200001D4
(×)= <mark>[6</mark> ]	float	0.9769039	0x200001D8
(×)= [7]	float	-0.05649545	0x200001DC
(×)= <mark>[8</mark> ]	float	0.4720337	0x200001E0
(×)= [9]	float	0.2593119	0x200001E4
a 🍃 testLinIntOutput	float[10]	0x20000408	0x20000408
(×)= [0]	float	-0.6049607	0x20000408
(×)= [1]	float	-0.5970903	0x2000040C
(×)= [2]	float	0.1871404	0x20000410
(×)= [3]	float	0.418772	0x20000414
(×)= [4]	float	-0.9885888	0x20000418
(×)= <mark>[</mark> 5]	float	0.9763384	0x2000041C
(×)= <mark>[6</mark> ]	float	0.9769039	0x20000420
(×)= [7]	float	-0.05649542	0x20000424
(×)= <mark>[8</mark> ]	float	0.4720336	0x20000428
(×)= <mark>[</mark> 9]	float	0.2593119	0x2000042C
🔺 🥭 testOutput	float[10]	0x20000430	0x20000430
(×)= [0]	float	-0.6049607	0x20000430
(×)= [1]	float	-0.5970906	0x20000434
(×)= [2]	float	0.1871404	0x20000438
(x)= [3]	float	0.4187721	0x2000043C
(×)= [4]	float	-0.9885888	0x20000440
(×)= [5]	float	0.9763384	0x20000444
(×)= [6]	float	0.9769038	0x20000448
(×)= [7]	float	-0.05649538	0x2000044C
(×)= [8]	float	0.4720337	0x20000450
(×)= [9]	float	0.2593119	0x20000454
🕂 Add new expression			
•			

#### Figure 28. Using the Debugger to Examine the Results of the linear\_interp\_example Project

8. If you manually examine the values stored at each element, you will see that for the most part, the sin values calculated using both cubic and linear interpolation are closer to the reference values than those calculated using only cubic interpolation. In the example above, this is especially noticeable on element 7 of the output arrays.

#### 5 Conclusion

Using the information provided in this document, combined with the resources available from ARM's CMSIS website, it is possible to easily and quickly implement various complex DSP algorithms. While it is possible to code a number of these functions independently, the result would likely lead to a much greater development time and produce less efficient code. It is highly recommended that anytime a Texas Instruments' TM4C or Stellaris microcontroller is being used for an application that requires complex DSP functionality, the procedure listed here should be followed to ensure accurate, reliable, efficient code.



#### 6 References

The following related documents and software are available on the TM4C Series web site at: http://www.ti.com/product/tm4c1294ncpdt

- Tiva TM4C1294NCPDT Microcontroller Data Sheet (SPMS433)
- Tiva C Series TM4C129x Microcontrollers Silicon Revisions 1, 2, and 3 Silicon Errata (SPMZ850)

References

- The source code for the CMSIS DSP Library and example code can be downloaded from ARM's CMSIS website: cmsis.arm.com.
- A quick start guide for using Texas Instruments' Code Composer Studio v6.1 can be found on the TI processor wiki at: http://processors.wiki.ti.com/index.php/Category:Code\_Composer\_Studio\_v6.



## **Revision History**

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from I	F Revision	(May 2015)	to G	Revision
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### Page

_		
•	Updates were made in the Abstract	1
•	Information was updated in Section 3.1.	3
•	Information was updated in Section 3.2.	3
•	Information was updated in Section 3.4.	7
•	Information was updated in Section 4.1	15
•	Information was updated in Section 4.3	20
•	information was updated in Section 4.3.	20

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