

## ***TI814x-DDR3-Init-U-Boot***

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### **ABSTRACT**

This article describes how to initialize the DDR3 and uBoot on the TMS320DM814x DaVinci™ digital media processors (DSP) and AM387x ARM® microprocessors (MPUs) PG2.x samples using the software leveling process.

This article has been contributed to the TI Embedded Processors Wiki. To see the most recently updated version or to contribute, visit this topic at:

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