WHITE PAPER

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TEXAS INSTRUMENTS

C6Accel: TI SoC developers accelerate performance with ready-to-use DSP kernels

Introduction

Texas Instruments (TI) has a long history in offering optimized production-quality Digital Signal Processing (DSP) software in the form of signal-processing libraries and math libraries that have enabled DSP users to achieve optimized application performance. ARM® developers using TI SoC processors expressed a desire to leverage this DSP software using a programming interface that is familiar to them. C6Accel is an algorithm that contains a library of signal processing and math functions that can be accessed from the ARM. C6Accel simplifies the ARM user experience in leveraging the DSP by providing a library of ARM APIs. These APIs abstract complexities of using DSP in a multi-core SoC environment. By using C6Accel, an ARM SoC developer can use the DSP as an accelerator and create a more compelling application by adding differentiating features to their application. Advanced users can explore maximum flexibility by adding their own custom DSP kernels to the C6Accel algorithm which is provided in complete source in the package.

SoC View with C6Accel

Many ARM application developers on TI SoC limit the DSP usage to multimedia codecs only. The ever growing performance of the DSP core and availability of specialized hardware accelerators imply that the DSP MHz may not be completely utilized by the just the codec operation. Thus an application developer can develop a more compelling application by making use of the remaining MHz on the DSP. The following diagram shows a SoC view of C6Accel sitting alongside other multimedia codecs.

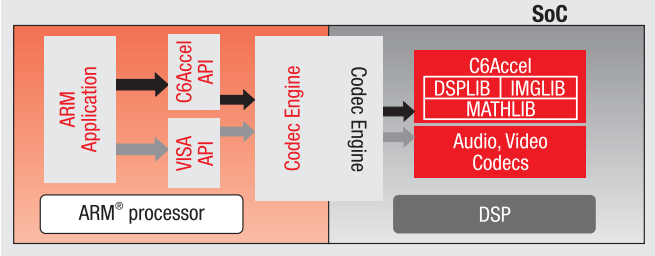

Figure 1. SoC view of C6Accel.

C6Accel is an algorithm that adheres to TI's specified algorithm interface called eXpressDSP™ Algorithm Interoperability Standard (xDAIS). TI provides a framework called Codec Engine that allows efficient execution of algorithms written with the xDAIS interface.

The application code calls C6Accel APIs. These APIs then pass through the Codec Engine interface and invoke the C6Accel algorithm on the DSP. On the DSP, the algorithm executes the appropriate DSP processing using the parameters. The C6Accel includes 100s of core DSP functions that are useful in various signal processing applications.

Building a C6Accel-based application

An application developer will start by identifying the processing steps that are better suited for execution on the DSP. These processing steps can be offloaded to the DSP by invoking C6Accel APIs. C6Accel provides two access methods for the ARM.

The simplest ARM interface involves use of C6Accel API calls which hide the Codec Engine details and allows the ARM user to leverage the DSP as a black box. This access method

allows the ARM® user to call a single DSP function per invocation. In addition, significant performance boost can be achieved by using these APIs in the asynchronous calling mode. The asynchronous calling feature is explained in later section of this document. This feature truly enables parallel operation of ARM and the DSP.

The second access method allows the ARM user to combine many API calls into a single C6Accel invocation. Such usage reduces the calling overhead associated with the API calls and thus improves the overall efficiency. This level of control requires basic knowledge of Codec Engine APIs and the C6Accel design. More details are provided in later section of this document.

Advanced users can maximize the DSP usage by adding their own custom functions to the C6Accel xDAIS algorithm. To enable this, the C6Accel is provided in complete source code and is accompanied by documentation that walks a user through this process. The user is expected to have basic understanding of the Codec Engine framework and the DSP architecture to explore this level of flexibility.

Application benefits

The C6Accel algorithm contains key kernels from floating- and fixed-point signal-processing libraries and math libraries. From the digital signal processing libraries it includes the key signal analysis and filtering kernels like the FFTs, IIR, FIR filters and vector/matrix operations. From the imaging library, it includes several color space conversion kernels, image analysis, filtering and enhancement kernels. C6Accel contains various arithmetic and trigonometric functions from the math libraries. Both fixed-point and floating-point math kernels are included. For the floating point, both single-precision and double-precision math operations are supported. Performance benchmarks are provided along with detailed documentation.

Table 1. Table illustrates type of functions in different categories of functions in C6Accel

Categories	Types of functions
DSP library	Filtering /FFT Vector/Matrix functions
Image library	Color space conversion Edge detection/Image filtering Image analysis and arithmetic
Math library	Datatype conversion Arithmetic (addsp, subsp, divsp, mulsp, intsp, etc.) Trignometic (expsp, logsp, powsp, sinsp, cossp, atansp, etc.)

There are many applications that will find value in using C6Accel. For example, audio applications can be enhanced by implementing high-quality audio algorithms that utilize high-precision IIR and FIR filters. Additional audio channels can be added and with more compelling effects by leveraging the DSP. Complex analytics can be implemented in imaging systems by adding algorithms to do image enhancement or object detection. The various math kernels are useful in test and measurement and Human Machine Interface (HMI) systems. The FFTs and data filtering kernels help offload key signal-processing steps in power protection systems from the ARM to the DSP.

Features and benefits of using C6Accel

- **Simple design**: C6Accel design provides a simple mechanism for identification and execution of a functionality to be executed on the DSP. C6Accel xDAIS algorithm code can be used as a template to add custom DSP algorithms which can then be accessed from the ARM® application.
- **Easy to interface**: C6Accel simplifies the ARM user experience in leveraging the DSP by providing a library of ARM side APIs that abstract complexities faced in a multi-core environment. C6Accel can be configured into an application like any other multimedia codec.
- **Differentiated feature from DSP architecture**: DSP kernels included in C6Accel are optimized for the DSP core allowing ARM users take advantage of the DSP performance on their SoC device. The C6Accel also allows the ARM developer to exploit the unique architectural features of the DSP. For example, on an OMAP-L13x device, C6Accel allows a fixed-point ARM to perform efficient floating-point processing using the floating-point C674x DSP.
- **Parallel processing**: Asynchronous execution feature of the C6Accel APIs enables parallel execution of the ARM and the DSP. This feature helps developers maximize the overall system performance.

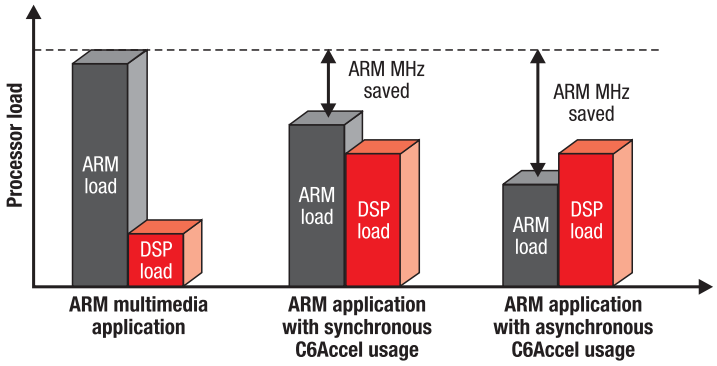

Figure 2. Processor load view to demonstrate benefits of C6Accel usage.

- **Efficient task list form of C6Accel invocation**: In case of multiple DSP kernels call, C6Accel design supports an efficient calling technique of combining multiple kernel calls into a task list that helps reduce the calling overheads.

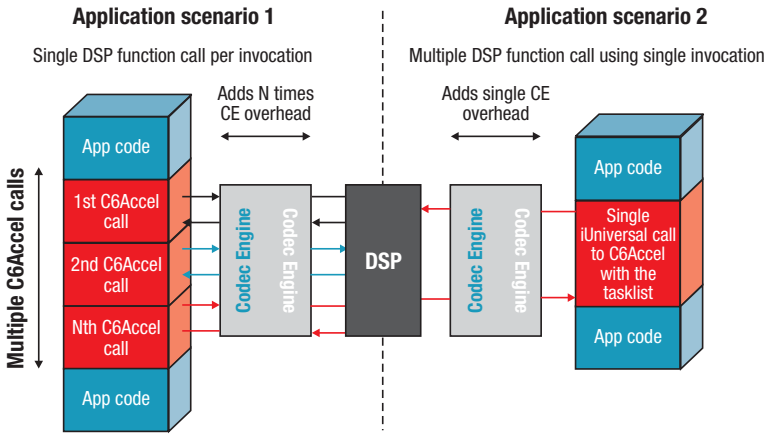

Figure 3. Diagram shows a calling multiple DSP function using a single DSP function per invocation and benefits of calling multiple DSP functions using a single invocation.

- **Availability of tested and benchmarked kernels**: Availability of production-ready DSP kernels reduces learning curve and time to market for ARM® SoC developers. All the C6Accel kernels are tested and benchmarked in a sample application included in the package. The ability to easily benchmark the C6Accel kernels allows application developers to make informed decisions on when using DSP acceleration is suitable. The calling overheads associated with C6Accel may imply that for very small buffer sizes, using C6Accel may not be most efficient.

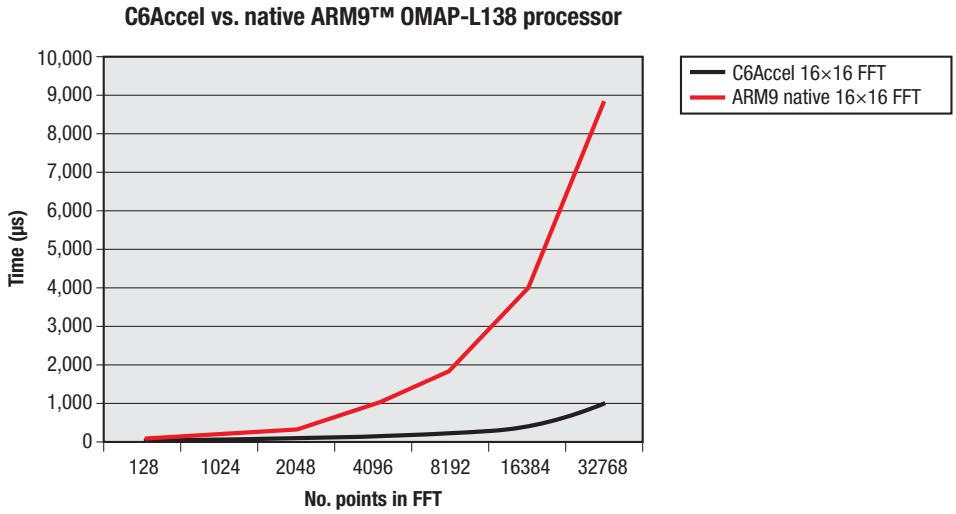

Figure 4. Graph of execution time vs. data size while using C6Accel and ARM for data processing.

- **Extensibility**: C6Accel provides a common method and API to add other or custom signal processing features to the application to fully utilize the performance of the DSP core, or to further enhance the features of the existing codecs with pre-/post-filtering capabilities.

Conclusion

C6Accel is a powerful tool that enables ARM application to leverage the DSP as an accelerator on TI TMS230C6000™ DSP + ARM devices. C6Accel is built in a standard xDM Codec Engine-compliant iUniversal interface which allows execution alongside other multimedia codecs thereby allowing applications to do more processing on the DSP. C6Accel`s simple design and ease of use makes it an ideal tool for SoC developers to leverage the DSP with varying levels of control and flexibility. For more information on the tool and specific details on any of the topics covered in this article, please refer to the online documentation hosted on TI's embedded processors wiki site. **processors.wiki.ti.com/**

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