

## Load Disconnect for the TPS6510x

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### ABSTRACT

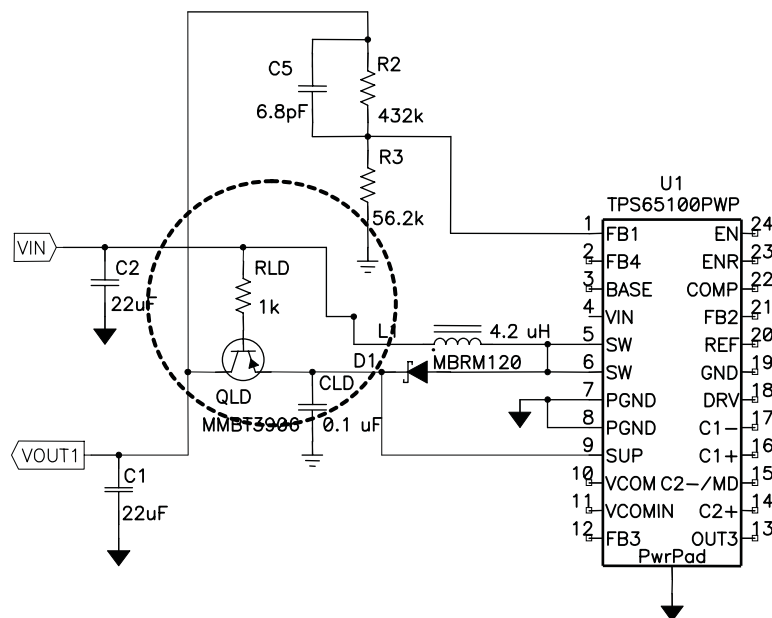
Many boost converters have an external rectifier diode. The input voltage of the TPS6510x main boost converter is connected to the output voltage when the device is disabled. Due to the direct pass from the input to the output, the converter has no short-circuit protection. This application report describes how to use a PNP transistor and some passive components to disconnect the boost converter's input voltage from the output voltage (known as load disconnect) when the device is disabled as well as when under short-circuit conditions.

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### 1 Description

Figure 1 shows a portion of the schematic for the TPS65100EVM that has been modified to include PNP transistor  $Q_{(LD)}$ , resistor  $R_{(LD)}$ , and capacitor  $C_{(LD)}$ , all of which form the load disconnect circuit.



**Figure 1. TPS65100 With Load Disconnect**

The SUP is connected before Q<sub>(LD)</sub>, but the feedback network and output capacitor are connected after Q<sub>(LD)</sub>. This allows a regulated output voltage even with the use of the transistor Q<sub>(LD)</sub>. Select Q<sub>(LD)</sub> to have a low saturation voltage (V<sub>(SAT)</sub>) and high beta (B) in order to prevent degradation of efficiency. C<sub>(LD)</sub> is required to set a defined bias operation point for Q<sub>(LD)</sub>, and should be between 0.1 μF and 1 μF. R<sub>(LD)</sub> should be sized according to the following equation:

$$R_{(LD)} \leq \frac{B_{(MIN)} \times (V_{(OUT1)} + V_{(SAT) \min} - V_{IN \max} - V_{(be) \max})}{I_{OUT \max}}$$

where B<sub>(MIN)</sub> is the minimum beta, V<sub>(SAT) min</sub> is the minimum saturation voltage, V<sub>(be) max</sub> is the maximum base to emitter voltage of Q<sub>(LD)</sub> and I<sub>OUT max</sub> is the desired short-circuit current limit. The selected I<sub>OUT max</sub> should be at least higher than the maximum load current in the application to allow design margin for load transient.

Figure 2 shows the output voltage (CH1, set to 10 V), the output current (CH3, set to 100 mA) and the input current (CH4) before and after a short-circuit event.

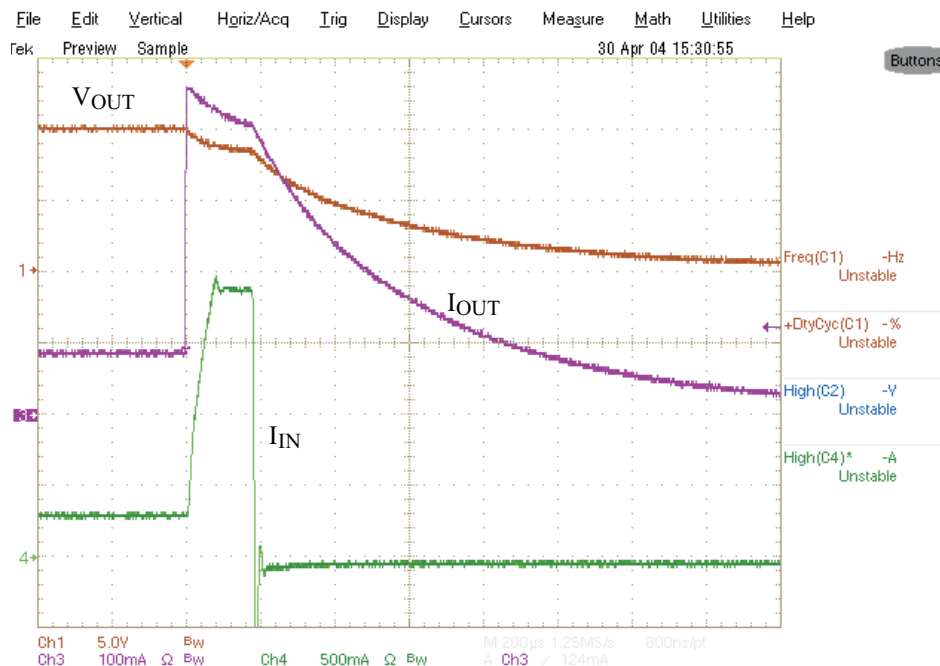


Figure 2. TPS65100 Before and After Short Circuit

The TPS6510x must be reenabled or the input voltage power cycled after a short-circuit event in order to reset the IC internal-protection circuitry and restart the device.

### Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

#### Changes from Original (May 2004) to A Revision

Page

- Deleted references to TPS6514x throughout document. .... 1

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