

## Description

The TS5A2053 is a single-pole double-throw (SPDT) analog switch that is designed to operate from 1.65 V to 5.5 V. This device can handle both digital and analog signals, and signals up to  $V_+$  can be transmitted in either direction.

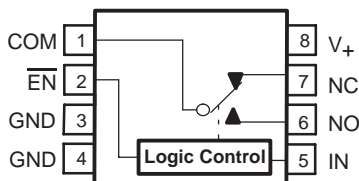
## Applications

- Cell Phones
- Portable Audio Video Equipment
- Battery-Powered Equipment
- Low-Voltage Data-Acquisition Systems
- Test Equipment
- Communication Circuits

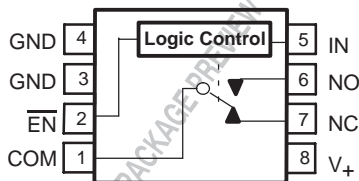
## Features

- Low ON-State Resistance (10 Ω)
- Control Inputs Are 5-V Tolerant
- Low Charge Injection
- Excellent ON-Resistance Matching
- Low Total Harmonic Distortion (THD)
- 1.65-V to 5.5-V Single-Supply Operation
- Latch-Up Performance Exceeds 100 mA Per JESD 78, Class II
- ESD Performance Tested Per JESD 22
  - 2000-V Human-Body Model (A114-B, Class II)
  - 1000-V Charged-Device Model (C101)

SSOP OR VSSOP PACKAGE  
(TOP VIEW)



YEP OR YZP PACKAGE  
(BOTTOM VIEW)



FUNCTION TABLE

| $\overline{EN}$ | IN | NC TO COM,<br>COM TO NC | NO TO COM,<br>COM TO NO |
|-----------------|----|-------------------------|-------------------------|
| L               | L  | ON                      | OFF                     |
| L               | H  | OFF                     | ON                      |
| H               | X  | OFF                     | OFF                     |

## Summary of Characteristics

$V_+ = 5\text{ V}$  and  $T_A = 25\text{ }^\circ\text{C}$

| Configuration                                   | Single Pole Double Throw (SPDT) |
|---|---------------------------------|
| Number of channels                              | 1                               |
| ON-state resistance ( $r_{ON}$ )                | 7.5 Ω                           |
| ON-state resistance match ( $\Delta r_{ON}$ )   | 0.8 Ω                           |
| ON-state resistance flatness ( $r_{ON(FLAT)}$ ) | 1.7 Ω                           |
| Turn-on/turn-off time ( $t_{ON}/t_{OFF}$ )      | 6.8 ns/4.1 ns                   |
| Charge injection ( $Q_C$ )                      | 3 pC                            |
| Bandwidth (BW)                                  | 330 MHz                         |
| OFF isolation ( $O_{ISO}$ )                     | -64 dB at 10 MHz                |
| Crosstalk ( $X_{TALK}$ )                        | -68 dB at 10 MHz                |
| Total harmonic distortion (THD)                 | 0.01%                           |
| Leakage current ( $I_{COM(OFF)}$ )              | ±10 nA                          |
| Power-supply current ( $I_+$ )                  | 0.1 μA                          |
| Package options                                 | 8-pin DSBGA, SSOP, or VSSOP     |



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

# TS5A2053 SINGLE-CHANNEL 10-Ω SPDT ANALOG SWITCH WITH ENABLE

SCDS183A – JANUARY 2005 – REVISED APRIL 2006

## ORDERING INFORMATION

| T <sub>A</sub> | PACKAGE <sup>(1)</sup>   |               | ORDERABLE PART NUMBER | TOP-SIDE MARKING <sup>(2)</sup> |
|----------------|--|---------------|-----------------------|---------------------------------|
| -40°C to 85°C  | NanoStar™ – WCSP (DSBGA)<br>0.23-mm Large Bump – YEP           | Tape and reel | TS5A2053YEPR          |                                 |
|                | NanoFree™ – WCSP (DSBGA)<br>0.23-mm Large Bump – YZP (Pb-free) |               | TS5A2053YZPR          |                                 |
|                | SSOP – DCT   | Tape and reel | TS5A2053DCTR          | JAF_ _ _                        |
|                | VSSOP – DCU  | Tape and reel | TS5A2053DCUR          | JAF_                            |

(1) Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at [www.ti.com/sc/package](http://www.ti.com/sc/package).

(2) DCT: The actual top-side marking has three additional characters that designate the year, month, and assembly/test site.

DCU: The actual top-side marking has one additional character that designates the assembly/test site.

YEP/YZP: The actual top-side marking has three preceding characters to denote year, month, and sequence code, and one following character to designate the assembly/test site. Pin 1 identifier indicates solder-bump composition (1 = SnPb, ● = Pb-free).

## Absolute Minimum and Maximum Ratings<sup>(1)(2)</sup>

over operating free-air temperature range (unless otherwise noted)

|  |   | MIN   | MAX                  | UNIT      |
|--|---|---|----------------------|-----------|
| V <sub>+</sub>   | Supply voltage range <sup>(3)</sup>           | -0.5  | 6.5                  | V         |
| V <sub>NO</sub><br>V <sub>NC</sub><br>V <sub>COM</sub> | Analog voltage range <sup>(3)(4)(5)</sup>     | -0.5  | V <sub>+</sub> + 0.5 | V         |
| I <sub>K</sub>   | Analog port diode current                     | V <sub>NC</sub> , V <sub>NO</sub> , V <sub>COM</sub> < 0 or V <sub>NO</sub> , V <sub>NC</sub> , V <sub>COM</sub> > V <sub>+</sub> |                      | -50 50 mA |
| I <sub>NO</sub><br>I <sub>NC</sub><br>I <sub>COM</sub> | On-state switch current                       | V <sub>NC</sub> , V <sub>NO</sub> , V <sub>COM</sub> = 0 to V <sub>+</sub>  |                      | -50 50 mA |
| V <sub>I</sub>   | Digital input voltage range <sup>(3)(4)</sup> | -0.5  | 6.5                  | V         |
| I <sub>IK</sub>  | Digital input clamp current                   | V <sub>I</sub> < 0  |                      | -50 mA    |
| I <sub>+</sub>   | Continuous current through V <sub>+</sub>     |   | 100                  | mA        |
| I <sub>GND</sub>                                       | Continuous current through GND                | -100  |                      | mA        |
| θ <sub>JA</sub>  | Package thermal impedance <sup>(6)</sup>      | DCT package   |                      | 220       |
|  |   | DCU package   |                      | 227       |
|  |   | YEP/YZP package   |                      | 102       |
| T <sub>stg</sub>                                       | Storage temperature range                     | -65   | 150                  | °C        |

(1) Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those specified is not implied.

(2) The algebraic convention, whereby the most negative value is a minimum and the most positive value is a maximum

(3) All voltages are with respect to ground, unless otherwise specified.

(4) The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

(5) This value is limited to 5.5 V maximum.

(6) The package thermal impedance is calculated in accordance with JESD 51-7.

**Electrical Characteristics for 5-V Supply<sup>(1)</sup>**
 $V_+ = 4.5\text{ V to }5.5\text{ V}$ ,  $T_A = -40^\circ\text{C to }85^\circ\text{C}$  (unless otherwise noted)

| PARAMETER                                  | SYMBOL                     | TEST CONDITIONS  | $T_A$        | $V_+$ | MIN              | TYP         | MAX              | UNIT |
|--|----------------------------|--|--------------|-------|------------------|-------------|------------------|------|
| <b>Analog Switch</b>                       |                            |  |              |       |                  |             |                  |      |
| Analog signal range                        | $V_{COM}, V_{NO}, V_{NC}$  |  |              |       | 0                |             | $V_+$            | V    |
| ON-state resistance                        | $r_{on}$                   | $0 \leq (V_{NO} \text{ or } V_{NC}) \leq V_+$ ,<br>$I_{COM} = -32\text{ mA}$ ,<br>Switch ON,<br>See Figure 13  | 25°C<br>Full | 4.5 V |                  | 7.5<br>13.8 | 16               | Ω    |
| ON-state resistance match between channels | $\Delta r_{on}$            | $V_{NO} \text{ or } V_{NC} = 3.15\text{ V}$ ,<br>$I_{COM} = -32\text{ mA}$ ,<br>Switch ON,<br>See Figure 13  | 25°C<br>Full | 4.5 V |                  | 0.8         | 4.5              | Ω    |
| ON-state resistance flatness               | $r_{on(Flat)}$             | $0 \leq (V_{NO} \text{ or } V_{NC}) \leq V_+$ ,<br>$I_{COM} = -32\text{ mA}$ ,<br>Switch ON,<br>See Figure 13  | 25°C<br>Full | 4.5 V |                  | 1.7         | 4.5              | Ω    |
| NO, NC OFF leakage current                 | $I_{NO(OFF)}, I_{NC(OFF)}$ | $V_{NO} \text{ or } V_{NC} = 1\text{ V}$ ,<br>$V_{COM} = 4.5\text{ V}$ ,<br>or<br>$V_{NO} \text{ or } V_{NC} = 4.5\text{ V}$ ,<br>$V_{COM} = 1\text{ V}$ ,<br>Switch OFF,<br>See Figure 14 | 25°C<br>Full | 5.5 V | -100<br>-200     | 5           | 100<br>200       | nA   |
| COM OFF leakage current                    | $I_{COM(OFF)}$             | $V_{COM} = 1\text{ V}$ ,<br>$V_{NO} \text{ or } V_{NC} = 4.5\text{ V}$ ,<br>or<br>$V_{COM} = 4.5\text{ V}$ ,<br>$V_{NO} \text{ or } V_{NC} = 1\text{ V}$ ,<br>Switch OFF,<br>See Figure 14 | 25°C<br>Full | 5.5 V | -100<br>-200     | -1          | 100<br>200       | nA   |
| NO, NC ON leakage current                  | $I_{NO(ON)}, I_{NC(ON)}$   | $V_{NO} = 1\text{ V}, V_{COM} = \text{Open}$ ,<br>or<br>$V_{NO} = 4.5\text{ V}, V_{COM} = \text{Open}$ ,<br>Switch ON,<br>See Figure 15  | 25°C<br>Full | 5.5 V | -100<br>-200     | 5.5         | 100<br>200       | nA   |
| COM ON leakage current                     | $I_{COM(ON)}$              | $V_{COM} = 1\text{ V}$ ,<br>$V_{NO} \text{ or } V_{NC} = \text{Open}$ ,<br>or<br>$V_{COM} = 4.5\text{ V}$ ,<br>$V_{NO} \text{ or } V_{NC} = \text{Open}$ ,<br>Switch ON,<br>See Figure 15  | 25°C<br>Full | 5.5 V | -100<br>-200     | -1          | 100<br>200       | nA   |
| <b>Digital Control Inputs (IN, EN)</b>     |                            |  |              |       |                  |             |                  |      |
| Input logic high                           | $V_{IH}$                   |  | Full         |       | $V_+ \times 0.7$ |             | 5.5              | V    |
| Input logic low                            | $V_{IL}$                   |  | Full         |       | 0                |             | $V_+ \times 0.3$ | V    |
| Input leakage current                      | $I_{IH}, I_{IL}$           | $V_I = 5.5\text{ V or }0$  | 25°C<br>Full | 5.5 V | -0.1<br>-1       | 0.05        | 0.1<br>1         | μA   |

<sup>(1)</sup> The algebraic convention, whereby the most negative value is a minimum and the most positive value is a maximum

**TS5A2053**  
**SINGLE-CHANNEL 10-Ω SPDT ANALOG SWITCH**  
**WITH ENABLE**



SCDS183A – JANUARY 2005 – REVISED APRIL 2006

**Electrical Characteristics for 5-V Supply<sup>(1)</sup> (continued)**

V<sub>+</sub> = 4.5 V to 5.5 V, T<sub>A</sub> = -40°C to 85°C (unless otherwise noted)

| PARAMETER                    | SYMBOL   | TEST CONDITIONS  | T <sub>A</sub> | V <sub>+</sub> | MIN | TYP  | MAX | UNIT |
|------------------------------|--|--|----------------|----------------|-----|------|-----|------|
| <b>Dynamic</b>               |  |  |                |                |     |      |     |      |
| Turn-on time                 | t <sub>ON</sub>                                | V <sub>COM</sub> = 3 V,<br>R <sub>L</sub> = 300 Ω,<br>C <sub>L</sub> = 35 pF,<br>See Figure 17 | 25°C           | 5 V            | 3.8 | 5.3  | 6.8 | ns   |
|                              |  |  | Full           | 4.5 V to 5.5 V | 3   |      | 7.1 |      |
| Turn-off time                | t <sub>OFF</sub>                               | V <sub>COM</sub> = 3 V,<br>R <sub>L</sub> = 300 Ω,<br>C <sub>L</sub> = 35 pF,<br>See Figure 17 | 25°C           | 5 V            | 0.8 | 1.9  | 4.1 | ns   |
|                              |  |  | Full           | 4.5 V to 5.5 V | 0.4 |      | 4.5 |      |
| Charge injection             | Q <sub>C</sub>                                 | V <sub>GEN</sub> = 0,<br>R <sub>GEN</sub> = 0,<br>C <sub>L</sub> = 0.1 nF,<br>See Figure 21    | 25°C           | 5 V            |     | 3    |     | pC   |
| NO, NC<br>OFF capacitance    | C <sub>NO(OFF)</sub> ,<br>C <sub>NC(OFF)</sub> | V <sub>NO</sub> or V <sub>NC</sub> = V <sub>+</sub> or GND,<br>Switch OFF,<br>See Figure 16    | 25°C           | 5 V            |     | 6    |     | pF   |
| COM<br>OFF capacitance       | C <sub>COM(OFF)</sub>                          | V <sub>COM</sub> = V <sub>+</sub> or GND,<br>Switch OFF,<br>See Figure 16                      | 25°C           | 5 V            |     | 9.5  |     | pF   |
| NO, NC<br>ON capacitance     | C <sub>NO(ON)</sub> ,<br>C <sub>NC(ON)</sub>   | V <sub>NO</sub> or V <sub>NC</sub> = V <sub>+</sub> or GND,<br>Switch ON,<br>See Figure 16     | 25°C           | 5 V            |     | 18   |     | pF   |
| COM<br>ON capacitance        | C <sub>COM(ON)</sub>                           | V <sub>COM</sub> = V <sub>+</sub> or GND,<br>Switch ON,<br>See Figure 16                       | 25°C           | 5 V            |     | 18   |     | pF   |
| Digital input<br>capacitance | C <sub>I</sub>                                 | V <sub>I</sub> = V <sub>+</sub> or GND,<br>See Figure 16                                       | 25°C           | 5 V            |     | 2.5  |     | pF   |
| Bandwidth                    | BW   | R <sub>L</sub> = 50 Ω,<br>Switch ON,<br>See Figure 18  | 25°C           | 5 V            |     | 330  |     | MHz  |
| OFF isolation                | O <sub>ISO</sub>                               | R <sub>L</sub> = 50 Ω,<br>f = 10 MHz,<br>Switch OFF,<br>See Figure 19                          | 25°C           | 5 V            |     | -64  |     | dB   |
| Crosstalk                    | X <sub>TALK</sub>                              | R <sub>L</sub> = 50 Ω,<br>f = 10 MHz,<br>Switch ON,<br>See Figure 20                           | 25°C           | 5 V            |     | -68  |     | dB   |
| Total harmonic<br>distortion | THD  | R <sub>L</sub> = 600 Ω,<br>C <sub>L</sub> = 50 pF,<br>f = 20 Hz to 20 kHz,<br>See Figure 22    | 25°C           | 5 V            |     | 0.01 |     | %    |
| <b>Supply</b>                |  |  |                |                |     |      |     |      |
| Positive supply<br>current   | I <sub>+</sub>                                 | V <sub>I</sub> = V <sub>+</sub> or GND,<br>Switch ON or OFF                                    | 25°C           | 5.5 V          | 0.1 | 1    | 5   | μA   |
|                              |  |  | Full           |                |     |      |     |      |

(1) The algebraic convention, whereby the most negative value is a minimum and the most positive value is a maximum

**Electrical Characteristics for 3.3-V Supply<sup>(1)</sup>**
 $V_+ = 3\text{ V to }3.6\text{ V}$ ,  $T_A = -40^\circ\text{C to }85^\circ\text{C}$  (unless otherwise noted)

| PARAMETER                                  | SYMBOL                                | TEST CONDITIONS  | $T_A$                        | $V_+$        | MIN   | TYP              | MAX              | UNIT |
|--|---------------------------------------|--|------------------------------|--------------|-------|------------------|------------------|------|
| <b>Analog Switch</b>                       |                                       |  |                              |              |       |                  |                  |      |
| Analog signal range                        | $V_{COM}$ ,<br>$V_{NO}$ ,<br>$V_{NC}$ |  |                              |              | 0     |                  | $V_+$            | V    |
| ON-state resistance                        | $r_{on}$                              | $0 \leq (V_{NO} \text{ or } V_{NC}) \leq V_+$ ,<br>$I_{COM} = -24\text{ mA}$ ,   | Switch ON,<br>See Figure 13  | 25°C<br>Full | 3 V   | 13.2             | 20               | Ω    |
| ON-state resistance match between channels | $\Delta r_{on}$                       | $V_{NO} \text{ or } V_{NC} = 2.1\text{ V}$ ,<br>$I_{COM} = -24\text{ mA}$ ,  | Switch ON,<br>See Figure 13  | 25°C<br>Full | 3 V   | 1                | 5.5              | Ω    |
| ON-state resistance flatness               | $r_{on(Flat)}$                        | $0 \leq (V_{NO} \text{ or } V_{NC}) \leq V_+$ ,<br>$I_{COM} = -24\text{ mA}$ ,   | Switch ON,<br>See Figure 13  | 25°C<br>Full | 3 V   | 5.3              | 11               | Ω    |
| NO, NC OFF leakage current                 | $I_{NO(OFF)}$ ,<br>$I_{NC(OFF)}$      | $V_{NO} \text{ or } V_{NC} = 1\text{ V}$ , $V_{COM} = 3\text{ V}$ ,<br>or<br>$V_{NO} \text{ or } V_{NC} = 3\text{ V}$ , $V_{COM} = 1\text{ V}$ ,   | Switch OFF,<br>See Figure 14 | 25°C<br>Full | 3.6 V | -100             | 4<br>200         | nA   |
| COM OFF leakage current                    | $I_{COM(OFF)}$                        | $V_{COM} = 1\text{ V}$ , $V_{NO} \text{ or } V_{NC} = 3\text{ V}$ ,<br>or<br>$V_{COM} = 3\text{ V}$ , $V_{NO} \text{ or } V_{NC} = 1\text{ V}$ ,   | Switch OFF,<br>See Figure 14 | 25°C<br>Full | 3.6 V | -100             | -1<br>200        | nA   |
| NO, NC ON leakage current                  | $I_{NO(ON)}$ ,<br>$I_{NC(ON)}$        | $V_{NO} \text{ or } V_{NC} = 1\text{ V}$ , $V_{COM} = \text{Open}$ ,<br>or<br>$V_{NO} \text{ or } V_{NC} = 3\text{ V}$ , $V_{COM} = \text{Open}$ , | Switch ON,<br>See Figure 15  | 25°C<br>Full | 3.6 V | -100             | 4.5<br>200       | nA   |
| COM ON leakage current                     | $I_{COM(ON)}$                         | $V_{COM} = 1\text{ V}$ , $V_{NO} \text{ or } V_{NC} = \text{Open}$ ,<br>or<br>$V_{COM} = 3\text{ V}$ , $V_{NO} \text{ or } V_{NC} = \text{Open}$ , | Switch ON,<br>See Figure 15  | 25°C<br>Full | 3.6 V | -100             | -1<br>200        | nA   |
| <b>Digital Control Inputs (IN, EN)</b>     |                                       |  |                              |              |       |                  |                  |      |
| Input logic high                           | $V_{IH}$                              |  |                              | Full         |       | $V_+ \times 0.7$ | 5.5              | V    |
| Input logic low                            | $V_{IL}$                              |  |                              | Full         |       | 0                | $V_+ \times 0.3$ | V    |
| Input leakage current                      | $I_{IH}$ , $I_{IL}$                   | $V_I = 5.5\text{ V or }0$  |                              | 25°C<br>Full | 3.6 V | -0.1             | 0.05<br>0.1      | μA   |

(1) The algebraic convention, whereby the most negative value is a minimum and the most positive value is a maximum

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SCDS183A – JANUARY 2005 – REVISED APRIL 2006

**Electrical Characteristics for 3.3-V Supply<sup>(1)</sup> (continued)**

$V_+ = 3\text{ V to }3.6\text{ V}$ ,  $T_A = -40^\circ\text{C to }85^\circ\text{C}$  (unless otherwise noted)

| PARAMETER                    | SYMBOL         | TEST CONDITIONS                                   | $T_A$  | $V_+$ | MIN          | TYP | MAX   | UNIT          |    |
|------------------------------|----------------|---|--|-------|--------------|-----|-------|---------------|----|
| <b>Dynamic</b>               |                |   |  |       |              |     |       |               |    |
| Turn-on time                 | $t_{ON}$       | $V_{COM} = 2\text{ V}$ ,<br>$R_L = 300\ \Omega$ , | $C_L = 35\text{ pF}$ ,<br>See Figure 17                | 25°C  | 3.3 V        | 5   | 6.4   | 7.9           | ns |
|                              |                |   |  | Full  | 3 V to 3.6 V | 4.5 |       | 8.2           |    |
| Turn-off time                | $t_{OFF}$      | $V_{COM} = 2\text{ V}$ ,<br>$R_L = 300\ \Omega$ , | $C_L = 35\text{ pF}$ ,<br>See Figure 17                | 25°C  | 3.3 V        | 1.1 | 2.4   | 4.7           | ns |
|                              |                |   |  | Full  | 3 V to 3.6 V | 0.3 |       | 5             |    |
| Charge injection             | $Q_C$          | $V_{GEN} = 0$ ,<br>$R_{GEN} = 0$ ,                | $C_L = 0.1\text{ nF}$ ,<br>See Figure 21               | 25°C  | 3.3 V        |     | 1     | pC            |    |
| NO, NC<br>OFF capacitance    | $C_{NO(OFF)}$  | $V_{NO}$ or $V_{NC} = V_+$ or GND,<br>Switch OFF, | See Figure 16  | 25°C  | 3.3 V        |     | 6     | pF            |    |
| COM<br>OFF capacitance       | $C_{COM(OFF)}$ | $V_{COM} = V_+$ or GND,<br>Switch OFF,            | See Figure 16  | 25°C  | 3.3 V        |     | 9.5   | pF            |    |
| NO, NC<br>ON capacitance     | $C_{NO(ON)}$   | $V_{NO}$ or $V_{NC} = V_+$ or GND,<br>Switch ON,  | See Figure 16  | 25°C  | 3.3 V        |     | 18.5  | pF            |    |
| COM<br>ON capacitance        | $C_{COM(ON)}$  | $V_{COM} = V_+$ or GND,<br>Switch ON,             | See Figure 16  | 25°C  | 3.3 V        |     | 18.5  | pF            |    |
| Digital input<br>capacitance | $C_I$          | $V_I = V_+$ or GND,                               | See Figure 16  | 25°C  | 3.3 V        |     | 3     | pF            |    |
| Bandwidth                    | BW             | $R_L = 50\ \Omega$ ,<br>Switch ON,                | See Figure 18  | 25°C  | 3.3 V        |     | 320   | MHz           |    |
| OFF isolation                | $O_{ISO}$      | $R_L = 50\ \Omega$ ,<br>$f = 10\text{ MHz}$ ,     | Switch OFF,<br>See Figure 19                           | 25°C  | 3.3 V        |     | -64   | dB            |    |
| Crosstalk                    | $X_{TALK}$     | $R_L = 50\ \Omega$ ,<br>$f = 10\text{ MHz}$ ,     | Switch ON,<br>See Figure 20                            | 25 °C | 3.3 V        |     | -68   | dB            |    |
| Total harmonic<br>distortion | THD            | $R_L = 600\ \Omega$ ,<br>$C_L = 50\text{ pF}$ ,   | $f = 20\text{ Hz to }20\text{ kHz}$ ,<br>See Figure 22 | 25°C  | 3.3 V        |     | 0.035 | %             |    |
| <b>Supply</b>                |                |   |  |       |              |     |       |               |    |
| Positive supply<br>current   | $I_+$          | $V_I = V_+$ or GND,                               | Switch ON or OFF                                       | 25°C  | 3.6 V        | 0.1 | 1     | $\mu\text{A}$ |    |
|                              |                |   |  | Full  |              |     | 5     |               |    |

(1) The algebraic convention, whereby the most negative value is a minimum and the most positive value is a maximum

### Electrical Characteristics for 2.5-V Supply<sup>(1)</sup>

$V_+ = 2.3 \text{ V to } 2.7 \text{ V}$ ,  $T_A = -40^\circ\text{C to } 85^\circ\text{C}$  (unless otherwise noted)

| PARAMETER                                  | SYMBOL                     | TEST CONDITIONS  | $T_A$                        | $V_+$        | MIN   | TYP              | MAX              | UNIT |
|--|----------------------------|--|------------------------------|--------------|-------|------------------|------------------|------|
| <b>Analog Switch</b>                       |                            |  |                              |              |       |                  |                  |      |
| Analog signal range                        | $V_{COM}, V_{NO}, V_{NC}$  |  |                              |              | 0     |                  | $V_+$            | V    |
| ON-state resistance                        | $r_{on}$                   | $0 \leq (V_{NO} \text{ or } V_{NC}) \leq V_+$ ,<br>$I_{COM} = -8 \text{ mA}$ ,   | Switch ON,<br>See Figure 13  | 25°C<br>Full | 2.3 V | 20               | 40               | Ω    |
| ON-state resistance match between channels | $\Delta r_{on}$            | $V_{NO} \text{ or } V_{NC} = 1.6 \text{ V}$ ,<br>$I_{COM} = -8 \text{ mA}$ ,   | Switch ON,<br>See Figure 13  | 25°C<br>Full | 2.3 V | 1.1              | 6                | Ω    |
| ON-state resistance flatness               | $r_{on(Flat)}$             | $0 \leq (V_{NO} \text{ or } V_{NC}) \leq V_+$ ,<br>$I_{COM} = -8 \text{ mA}$ ,   | Switch ON,<br>See Figure 13  | 25°C<br>Full | 2.3 V | 15               | 20               | Ω    |
| NO, NC OFF leakage current                 | $I_{NO(OFF)}, I_{NC(OFF)}$ | $V_{NO} \text{ or } V_{NC} = 0.5 \text{ V}$ ,<br>$V_{COM} = 2.2 \text{ V}$ ,<br>or<br>$V_{NO} \text{ or } V_{NC} = 2.2 \text{ V}$ ,<br>$V_{COM} = 0.5 \text{ V}$ , | Switch OFF,<br>See Figure 14 | 25°C<br>Full | 2.7 V | -100             | 3.5<br>200       | nA   |
| COM OFF leakage current                    | $I_{COM(OFF)}$             | $V_{COM} = 0.5 \text{ V}$ ,<br>$V_{NO} \text{ or } V_{NC} = 2.2 \text{ V}$ ,<br>or<br>$V_{COM} = 2.2 \text{ V}$ ,<br>$V_{NO} \text{ or } V_{NC} = 0.5 \text{ V}$ , | Switch OFF,<br>See Figure 14 | 25°C<br>Full | 2.7 V | -100             | -2<br>200        | nA   |
| NO, NC ON leakage current                  | $I_{NO(ON)}, I_{NC(ON)}$   | $V_{NO} \text{ or } V_{NC} = 0.5 \text{ V}$ ,<br>$V_{COM} = \text{Open}$ ,<br>or<br>$V_{NO} \text{ or } V_{NC} = 2.2 \text{ V}$ ,<br>$V_{COM} = \text{Open}$ ,     | Switch ON,<br>See Figure 15  | 25°C<br>Full | 2.7 V | -100             | 4<br>200         | nA   |
| COM ON leakage current                     | $I_{COM(ON)}$              | $V_{COM} = 0.5 \text{ V}$ ,<br>$V_{NO} \text{ or } V_{NC} = \text{Open}$ ,<br>or<br>$V_{COM} = 2.2 \text{ V}$ ,<br>$V_{NO} \text{ or } V_{NC} = \text{Open}$ ,     | Switch ON,<br>See Figure 15  | 25°C<br>Full | 2.7 V | -100             | -2<br>200        | nA   |
| <b>Digital Control Inputs (IN, EN)</b>     |                            |  |                              |              |       |                  |                  |      |
| Input logic high                           | $V_{IH}$                   |  |                              | Full         |       | $V_+ \times 0.7$ | 5.5              | V    |
| Input logic low                            | $V_{IL}$                   |  |                              | Full         |       | 0                | $V_+ \times 0.3$ | V    |
| Input leakage current                      | $I_{IH}, I_{IL}$           | $V_I = 5.5 \text{ V or } 0$  |                              | 25°C<br>Full | 2.7 V | -0.1             | 0.05<br>0.1      | μA   |
|  |                            |  |                              |              |       | -1               | 1                |      |

(1) The algebraic convention, whereby the most negative value is a minimum and the most positive value is a maximum

**TS5A2053**  
**SINGLE-CHANNEL 10-Ω SPDT ANALOG SWITCH**  
**WITH ENABLE**



SCDS183A – JANUARY 2005 – REVISED APRIL 2006

**Electrical Characteristics for 2.5-V Supply<sup>(1)</sup> (continued)**

$V_+ = 2.3 \text{ V to } 2.7 \text{ V}$ ,  $T_A = -40^\circ\text{C to } 85^\circ\text{C}$  (unless otherwise noted)

| PARAMETER                    | SYMBOL                         | TEST CONDITIONS                                     | $T_A$   | $V_+$ | MIN            | TYP | MAX  | UNIT |               |
|------------------------------|--------------------------------|---|---|-------|----------------|-----|------|------|---------------|
| <b>Dynamic</b>               |                                |   |   |       |                |     |      |      |               |
| Turn-on time                 | $t_{ON}$                       | $V_{COM} = 1.5 \text{ V}$ ,<br>$R_L = 300 \Omega$ , | $C_L = 35 \text{ pF}$ ,<br>See Figure 17                  | 25°C  | 2.5 V          | 5.9 | 7.1  | 9.3  | ns            |
|                              |                                |   |   | Full  | 2.3 V to 2.7 V | 5.1 |      | 10   |               |
| Turn-off time                | $t_{OFF}$                      | $V_{COM} = 1.5 \text{ V}$ ,<br>$R_L = 300 \Omega$ , | $C_L = 35 \text{ pF}$ ,<br>See Figure 17                  | 25°C  | 2.5 V          | 2.1 | 3.2  | 5.1  | ns            |
|                              |                                |   |   | Full  | 2.3 V to 2.7 V | 1.2 |      | 5.2  |               |
| Charge injection             | $Q_C$                          | $V_{GEN} = 0$ ,<br>$R_{GEN} = 0$                    | $C_L = 0.1 \text{ nF}$ ,<br>See Figure 21                 | 25°C  | 2.5 V          |     | 0.5  | pC   |               |
| NO, NC<br>OFF capacitance    | $C_{NO(OFF)}$<br>$C_{NC(OFF)}$ | $V_{NO}$ or $V_{NC} = V_+$ or GND,<br>Switch OFF,   | See Figure 16   | 25°C  | 2.5 V          |     | 6.5  | pF   |               |
| COM<br>OFF capacitance       | $C_{COM(OFF)}$                 | $V_{COM} = V_+$ or GND,<br>Switch OFF,              | See Figure 16   | 25°C  | 2.5 V          |     | 10   | pF   |               |
| NO, NC<br>ON capacitance     | $C_{NO(ON)}$<br>$C_{NC(ON)}$   | $V_{NO}$ or $V_{NC} = V_+$ or GND,<br>Switch ON,    | See Figure 16   | 25°C  | 2.5 V          |     | 18.5 | pF   |               |
| COM<br>ON capacitance        | $C_{COM(ON)}$                  | $V_{COM} = V_+$ or GND,<br>Switch ON,               | See Figure 16   | 25°C  | 2.5 V          |     | 18.5 | pF   |               |
| Digital input<br>capacitance | $C_I$                          | $V_I = V_+$ or GND,                                 | See Figure 16   | 25°C  | 2.5 V          |     | 3    | pF   |               |
| Bandwidth                    | BW                             | $R_L = 50 \Omega$ ,<br>Switch ON,                   | See Figure 18   | 25°C  | 2.5 V          |     | 320  | MHz  |               |
| OFF isolation                | $O_{ISO}$                      | $R_L = 50 \Omega$ ,<br>$f = 10 \text{ MHz}$ ,       | Switch OFF,<br>See Figure 19                              | 25°C  | 2.5 V          |     | -64  | dB   |               |
| Crosstalk                    | X-TALK                         | $R_L = 50 \Omega$ ,<br>$f = 10 \text{ MHz}$ ,       | Switch ON,<br>See Figure 20                               | 25 °C | 2.5 V          |     | -68  | dB   |               |
| Total harmonic<br>distortion | THD                            | $R_L = 600 \Omega$ ,<br>$C_L = 50 \text{ pF}$ ,     | $f = 20 \text{ Hz to } 20 \text{ kHz}$ ,<br>See Figure 22 | 25°C  | 2.5 V          |     | 0.26 | %    |               |
| <b>Supply</b>                |                                |   |   |       |                |     |      |      |               |
| Positive supply<br>current   | $I_+$                          | $V_I = V_+$ or GND,                                 | Switch ON or OFF  | 25°C  | 2.7 V          | 0.1 | 1    | 5    | $\mu\text{A}$ |
|                              |                                |   |   | Full  |                |     |      |      |               |

(1) The algebraic convention, whereby the most negative value is a minimum and the most positive value is a maximum



**Electrical Characteristics for 1.8-V Supply<sup>(1)</sup>**
 $V_+ = 1.65\text{ V to }1.95\text{ V}$ ,  $T_A = -40^\circ\text{C to }85^\circ\text{C}$  (unless otherwise noted)

| PARAMETER   | SYMBOL                     | TEST CONDITIONS  | $T_A$                        | $V_+$        | MIN    | TYP               | MAX               | UNIT |
|---|----------------------------|--|------------------------------|--------------|--------|-------------------|-------------------|------|
| <b>Analog Switch</b>  |                            |  |                              |              |        |                   |                   |      |
| Analog signal range   | $V_{COM}, V_{NO}, V_{NC}$  |  |                              |              | 0      |                   | $V_+$             | V    |
| ON-state resistance   | $r_{on}$                   | $0 \leq (V_{NO} \text{ or } V_{NC}) \leq V_+$ ,<br>$I_{COM} = -4\text{ mA}$ ,  | Switch ON,<br>See Figure 13  | 25°C<br>Full | 1.65 V | 85                | 120               | Ω    |
| ON-state resistance match between channels                      | $\Delta r_{on}$            | $V_{NO} \text{ or } V_{NC} = 1.15\text{ V}$ ,<br>$I_{COM} = -4\text{ mA}$ ,  | Switch ON,<br>See Figure 13  | 25°C<br>Full | 1.65 V | 2                 | 7.5               | Ω    |
| ON-state resistance flatness                                    | $r_{on(Flat)}$             | $0 \leq (V_{NO} \text{ or } V_{NC}) \leq V_+$ ,<br>$I_{COM} = -4\text{ mA}$ ,  | Switch ON,<br>See Figure 13  | 25°C<br>Full | 1.65 V | 76                | 100               | Ω    |
| NO, NC OFF leakage current                                      | $I_{NO(OFF)}, I_{NC(OFF)}$ | $V_{NO} \text{ or } V_{NC} = 0.3\text{ V}$ ,<br>$V_{COM} = 1.65\text{ V}$ ,<br>or<br>$V_{NO} \text{ or } V_{NC} = 1.65\text{ V}$ ,<br>$V_{COM} = 0.3\text{ V}$ , | Switch OFF,<br>See Figure 14 | 25°C<br>Full | 1.95 V | -100              | 3.5<br>100        | nA   |
| COM OFF leakage current   | $I_{COM(OFF)}$             | $V_{COM} = 0.3\text{ V}$ , $V_{NO} = 1.65\text{ V}$ ,<br>or<br>$V_{COM} = 1.65\text{ V}$ , $V_{NO} = 0.3\text{ V}$ ,   | Switch OFF,<br>See Figure 14 | 25°C<br>Full | 1.95 V | -100              | 1<br>100          | nA   |
| NO, NC ON leakage current                                       | $I_{NO(ON)}, I_{NC(ON)}$   | $V_{NO} \text{ or } V_{NC} = 0.3\text{ V}$ ,<br>$V_{COM} = \text{Open}$ ,<br>or<br>$V_{NO} \text{ or } V_{NC} = 1.65\text{ V}$ ,<br>$V_{COM} = \text{Open}$ ,    | Switch ON,<br>See Figure 15  | 25°C<br>Full | 1.95 V | -100              | 4<br>100          | nA   |
| COM ON leakage current  | $I_{COM(ON)}$              | $V_{COM} = 0.3\text{ V}$ ,<br>$V_{NO} \text{ or } V_{NC} = \text{Open}$ ,<br>or<br>$V_{COM} = 1.65\text{ V}$ ,<br>$V_{NO} \text{ or } V_{NC} = \text{Open}$ ,    | Switch ON,<br>See Figure 15  | 25°C<br>Full | 1.95 V | -100              | 1<br>100          | nA   |
| <b>Digital Control Inputs (<math>I_N, \overline{EN}</math>)</b> |                            |  |                              |              |        |                   |                   |      |
| Input logic high  | $V_{IH}$                   |  |                              | Full         |        | $V_+ \times 0.65$ | 5.5               | V    |
| Input logic low   | $V_{IL}$                   |  |                              | Full         |        | 0                 | $V_+ \times 0.35$ | V    |
| Input leakage current   | $I_{IH}, I_{IL}$           | $V_I = 5.5\text{ V or }0$  |                              | 25°C<br>Full | 1.95 V | -0.1              | 0.05<br>0.1       | μA   |

<sup>(1)</sup> The algebraic convention, whereby the most negative value is a minimum and the most positive value is a maximum

# TS5A2053 SINGLE-CHANNEL 10-Ω SPDT ANALOG SWITCH WITH ENABLE

SCDS183A – JANUARY 2005 – REVISED APRIL 2006

## Electrical Characteristics for 1.8-V Supply<sup>(1)</sup> (continued)

$V_+ = 1.65\text{ V to }1.95\text{ V}$ ,  $T_A = -40^\circ\text{C to }85^\circ\text{C}$  (unless otherwise noted)

| PARAMETER                    | SYMBOL                           | TEST CONDITIONS                                       | $T_A$  | $V_+$ | MIN              | TYP  | MAX  | UNIT          |    |
|------------------------------|----------------------------------|---|--|-------|------------------|------|------|---------------|----|
| <b>Dynamic</b>               |                                  |   |  |       |                  |      |      |               |    |
| Turn-on time                 | $t_{ON}$                         | $V_{COM} = 1.3\text{ V}$ ,<br>$R_L = 300\ \Omega$ ,   | $C_L = 35\text{ pF}$ ,<br>See Figure 17                | 25°C  | 1.8 V            | 10.2 | 11.8 | 14.5          | ns |
|                              |                                  |   |  | Full  | 1.65 V to 1.95 V | 8.4  |      | 15.5          |    |
| Turn-off time                | $t_{OFF}$                        | $V_{COM} = 1.3\text{ V}$ ,<br>$R_L = 300\ \Omega$ ,   | $C_L = 35\text{ pF}$ ,<br>See Figure 17                | 25°C  | 1.8 V            | 2.9  | 4.3  | 6.5           | ns |
|                              |                                  |   |  | Full  | 1.65 V to 1.95 V | 2.2  |      | 7             |    |
| Charge injection             | $Q_C$                            | $V_{GEN} = 0$ ,<br>$R_{GEN} = 0$                      | $C_L = 0.1\text{ nF}$ ,<br>See Figure 21               | 25°C  | 1.8 V            |      | 0.5  | pC            |    |
| NO, NC<br>OFF capacitance    | $C_{NO(OFF)}$ ,<br>$C_{NC(OFF)}$ | $V_{NO}$ or $V_{NC} = V_+$ or GND,<br>Switch OFF,     | See Figure 16  | 25°C  | 1.8 V            |      | 6.5  | pF            |    |
| COM<br>OFF capacitance       | $C_{COM(OFF)}$                   | $V_{COM} = V_+$ or GND,<br>Switch OFF,                | See Figure 16  | 25°C  | 1.8 V            |      | 10   | pF            |    |
| NO, NC<br>ON capacitance     | $C_{NO(ON)}$ ,<br>$C_{NC(ON)}$   | $V_{NO}$ or $V_{NC} = V_+$ or GND,<br>Switch ON,      | See Figure 16  | 25°C  | 1.8 V            |      | 19   | pF            |    |
| COM<br>ON capacitance        | $C_{COM(ON)}$                    | $V_{COM} = V_+$ or GND,<br>Switch ON,                 | See Figure 16  | 25°C  | 1.8 V            |      | 14   | pF            |    |
| Digital input<br>capacitance | $C_I$                            | $V_I = V_+$ or GND,                                   | See Figure 16  | 25°C  | 1.8 V            |      | 3    | pF            |    |
| Bandwidth                    | BW                               | $R_L = 50\ \Omega$ ,<br>Switch ON,                    | See Figure 18  | 25°C  | 1.8 V            |      | 320  | MHz           |    |
| OFF isolation                | $O_{ISO}$                        | $R_L = 50\ \Omega$ ,<br>$f = 10\text{ MHz}$ ,         | Switch OFF,<br>See Figure 19                           | 25°C  | 1.8 V            |      | -64  | dB            |    |
| Crosstalk                    | $X_{TALK}$                       | $R_L = 50\ \Omega$ ,<br>$f = 10\text{ MHz}$ ,         | Switch ON,<br>See Figure 20                            | 25 °C | 1.8 V            |      | -68  | dB            |    |
| Total harmonic<br>distortion | THD                              | $R_L = 10\text{ k}\Omega$ ,<br>$C_L = 50\text{ pF}$ , | $f = 20\text{ Hz to }20\text{ kHz}$ ,<br>See Figure 22 | 25°C  | 1.8 V            |      | 2.6  | %             |    |
| <b>Supply</b>                |                                  |   |  |       |                  |      |      |               |    |
| Positive supply<br>current   | $I_+$                            | $V_I = V_+$ or GND,                                   | Switch ON or OFF                                       | 25°C  | 1.95 V           | 0.1  | 1    | $\mu\text{A}$ |    |
|                              |                                  |   |  | Full  |                  |      | 5    |               |    |

(1) The algebraic convention, whereby the most negative value is a minimum and the most positive value is a maximum

TYPICAL PERFORMANCE

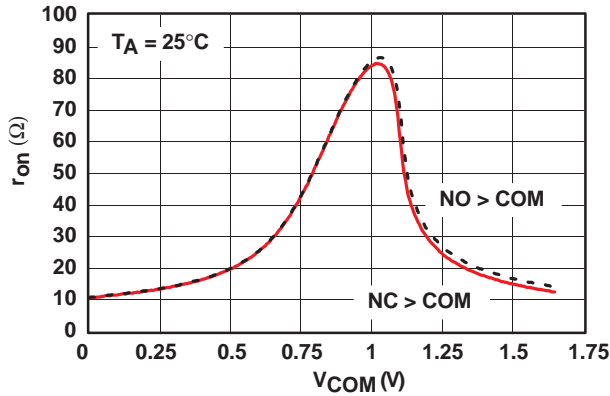


Figure 1A.  $r_{on}$  vs  $V_{COM}$  ( $V_+ = 1.65$  V)

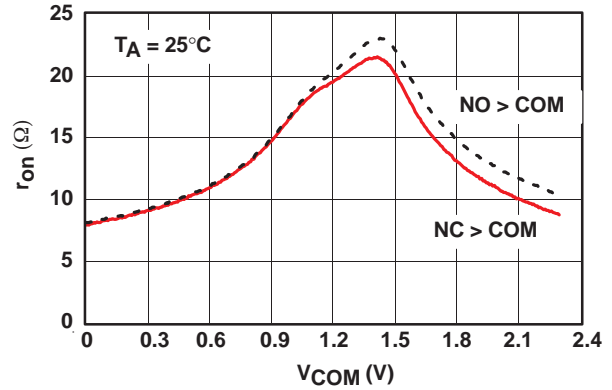


Figure 1B.  $r_{on}$  vs  $V_{COM}$  ( $V_+ = 2.3$  V)

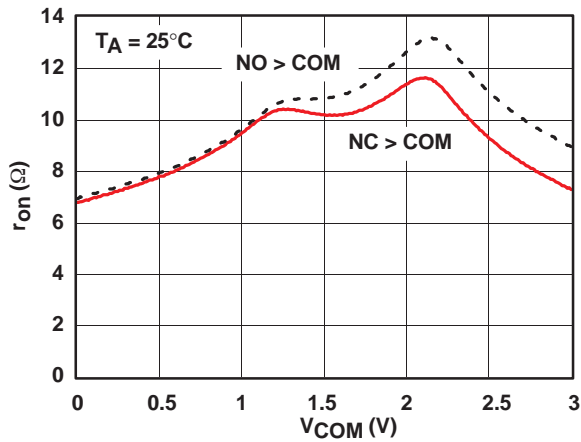


Figure 1C.  $r_{on}$  vs  $V_{COM}$  ( $V_+ = 3$  V)

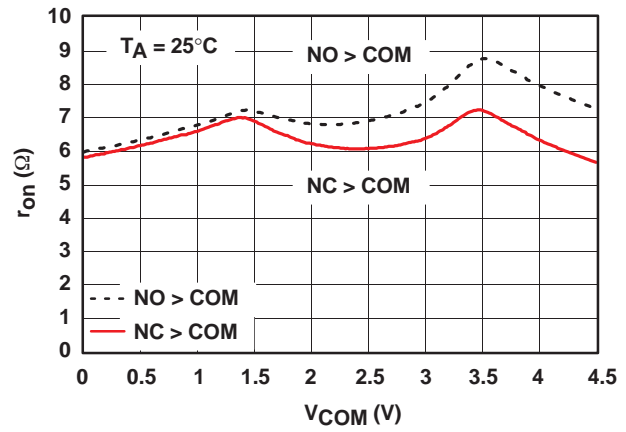


Figure 1D.  $r_{on}$  vs  $V_{COM}$  ( $V_+ = 4.5$  V)

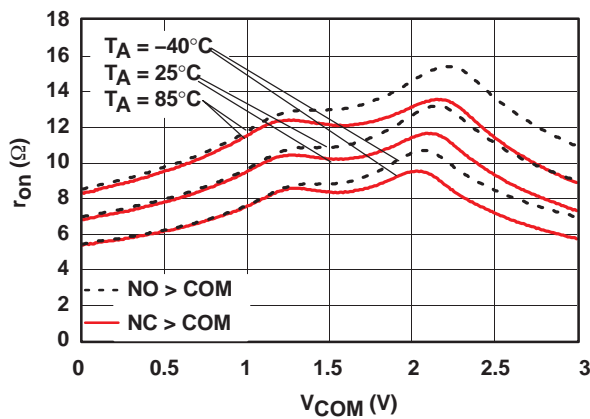


Figure 2.  $r_{on}$  vs  $V_{COM}$  ( $V_+ = 3$  V)

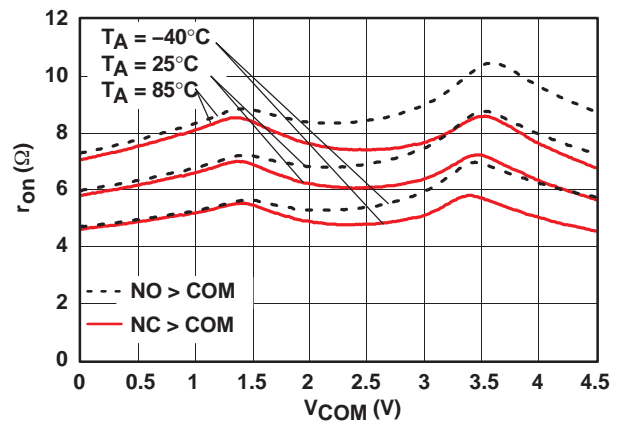
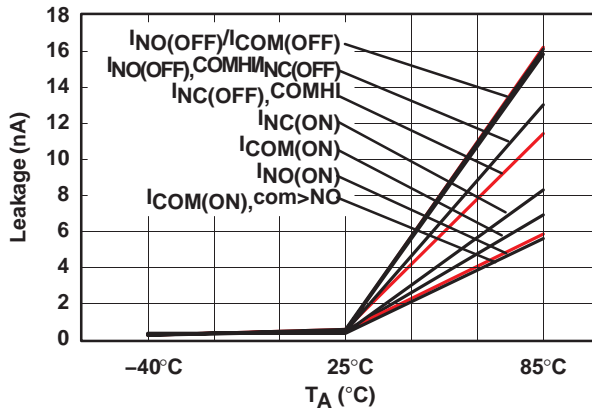
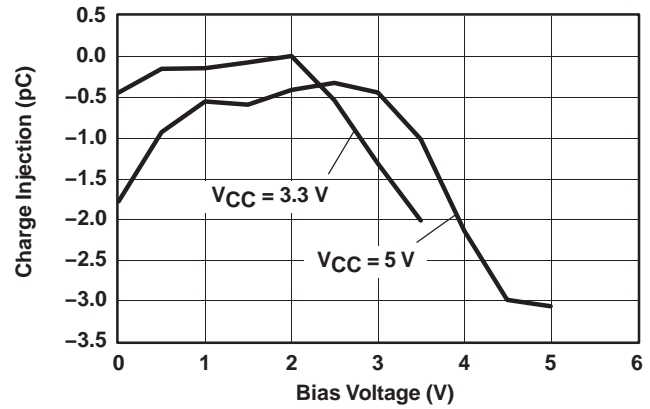


Figure 3.  $r_{on}$  vs  $V_{COM}$  ( $V_+ = 4.5$  V)

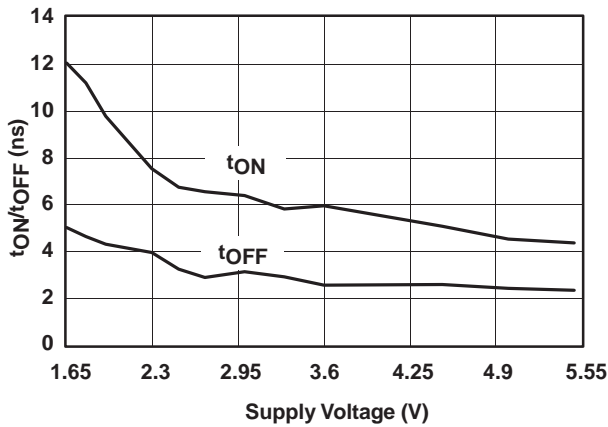
**TYPICAL PERFORMANCE (continued)**



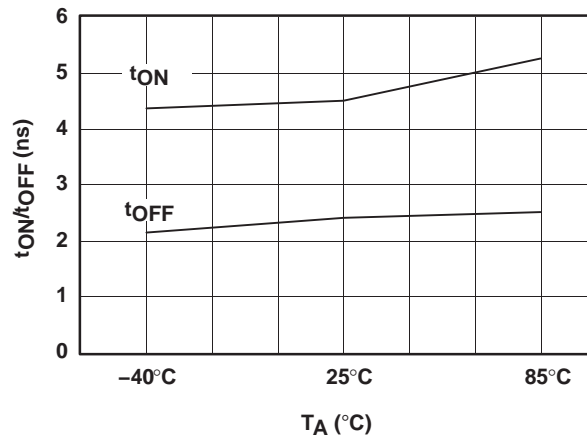
**Figure 4. Leakage Current vs Temperature**  
 ( $V_+ = 5.5\text{ V}$ )



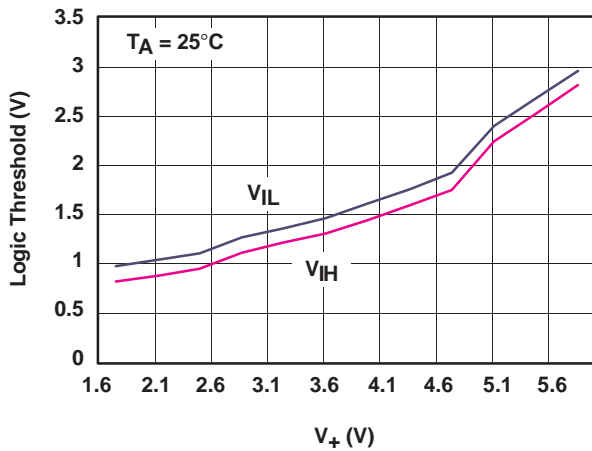
**Figure 5. Charge Injection ( $Q_C$ ) vs  $V_{COM}$**



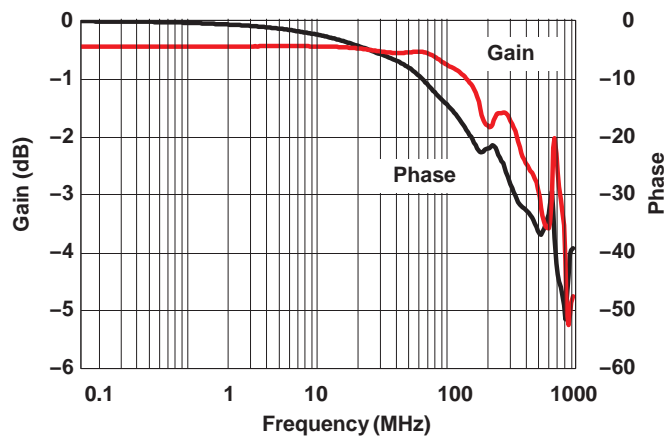
**Figure 6.  $t_{ON}$  and  $t_{OFF}$  vs  $V_+$**



**Figure 7.  $t_{ON}$  and  $t_{OFF}$  vs Temperature**  
 ( $V_+ = 5\text{ V}$ )



**Figure 8. Logic Threshold vs  $V_+$**



**Figure 9. Bandwidth**  
 ( $V_+ = 5\text{ V}$ )

**TYPICAL PERFORMANCE (continued)**

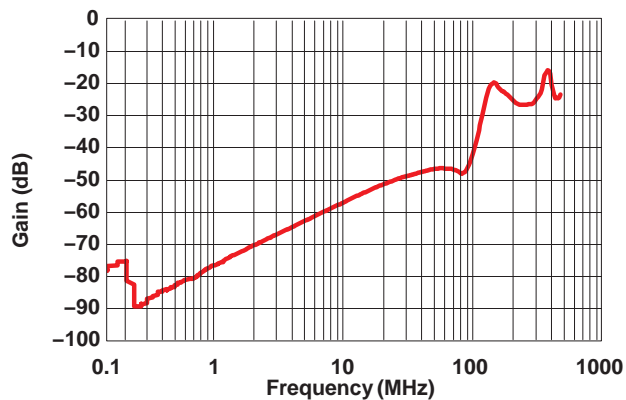


Figure 10. OFF Isolation ( $V_+ = 5\text{ V}$ )

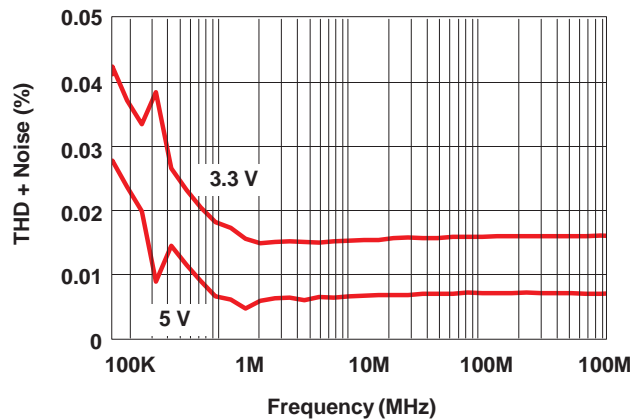


Figure 11. Total Harmonic Distortion vs Frequency

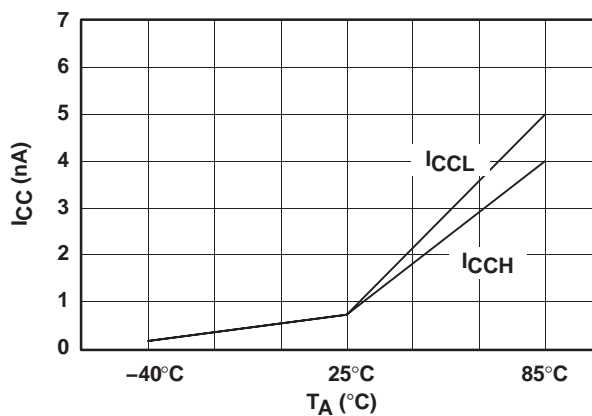


Figure 12. Power-Supply Current vs Temperature ( $V_+ = 5\text{ V}$ )

# TS5A2053 SINGLE-CHANNEL 10-Ω SPDT ANALOG SWITCH WITH ENABLE

SCDS183A – JANUARY 2005 – REVISED APRIL 2006

## PIN DESCRIPTION

| PIN NUMBER | NAME                   | DESCRIPTION                                |
|------------|------------------------|--|
| 1          | COM                    | Common                                     |
| 2          | $\overline{\text{EN}}$ | Chip enable (active low)                   |
| 3          | GND                    | Digital ground                             |
| 4          | GND                    | Digital ground                             |
| 5          | IN                     | Digital control to connect COM to NC or NO |
| 6          | NO                     | Normally open                              |
| 7          | NC                     | Normally closed                            |
| 8          | V <sub>+</sub>         | Power supply                               |

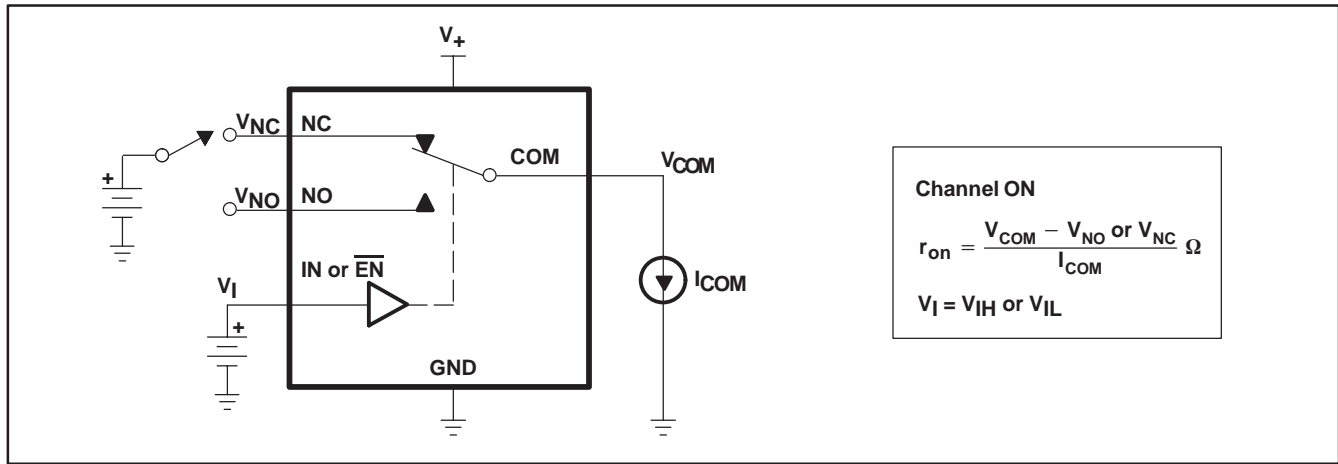
## PARAMETER DESCRIPTION

| SYMBOL                            | DESCRIPTION   |
|-----------------------------------|---|
| V <sub>COM</sub>                  | Voltage at COM  |
| V <sub>NC</sub>                   | Voltage at NC   |
| V <sub>NO</sub>                   | Voltage at NO   |
| r <sub>on</sub>                   | Resistance between COM and NC or COM and NO ports when the channel is ON  |
| Δr <sub>on</sub>                  | Difference of r <sub>on</sub> between channels in a specific device   |
| r <sub>on(flat)</sub>             | Difference between the maximum and minimum value of r <sub>on</sub> in a channel over the specified range of conditions   |
| I <sub>NC(OFF)</sub>              | Leakage current measured at the NC port, with the corresponding channel (NC to COM) in the OFF state  |
| I <sub>NO(OFF)</sub>              | Leakage current measured at the NO port, with the corresponding channel (NO to COM) in the OFF state  |
| I <sub>NC(ON)</sub>               | Leakage current measured at the NC port, with the corresponding channel (NC to COM) in the ON state and the output (COM) open   |
| I <sub>NO(ON)</sub>               | Leakage current measured at the NO port, with the corresponding channel (NO to COM) in the ON state and the output (COM) open   |
| I <sub>COM(OFF)</sub>             | Leakage current measured at the COM port, with the corresponding channel (COM to NO or COM to NC) in the OFF state and the output (NC or NO) open   |
| I <sub>COM(ON)</sub>              | Leakage current measured at the COM port, with the corresponding channel (COM to NO or COM to NC) in the ON state and the output (NC or NO) open  |
| V <sub>IH</sub>                   | Minimum input voltage for logic high for the control input (IN, $\overline{\text{EN}}$ )  |
| V <sub>IL</sub>                   | Maximum input voltage for logic low for the control input (IN, $\overline{\text{EN}}$ )   |
| V <sub>I</sub>                    | Voltage at the control input (IN, $\overline{\text{EN}}$ )  |
| I <sub>IH</sub> , I <sub>IL</sub> | Leakage current measured at the control input (IN, $\overline{\text{EN}}$ )   |
| t <sub>ON</sub>                   | Turn-on time for the switch. This parameter is measured under the specified range of conditions and by the propagation delay between the digital control (IN) signal and analog output (COM, NC, or NO) signal when the switch is turning ON.   |
| t <sub>OFF</sub>                  | Turn-off time for the switch. This parameter is measured under the specified range of conditions and by the propagation delay between the digital control (IN) signal and analog output (COM, NC, or NO) signal when the switch is turning OFF.   |
| Q <sub>C</sub>                    | Charge injection is a measurement of unwanted signal coupling from the control (IN) input to the analog (NC, NO, or COM) output. This is measured in coulomb (C) and measured by the total charge induced due to switching of the control input. Charge injection, $Q_C = C_L \times \Delta V_{COM}$ , C <sub>L</sub> is the load capacitance and ΔV <sub>COM</sub> is the change in analog output voltage. |

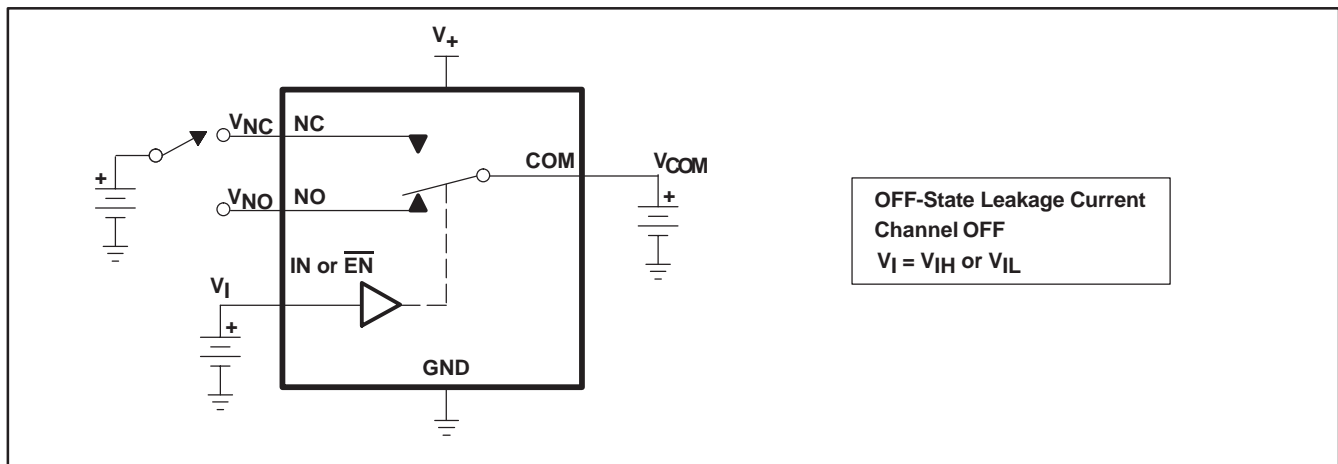
**PARAMETER DESCRIPTION (continued)**

| SYMBOL            | DESCRIPTION  |
|-------------------|--|
| CNC(OFF)          | Capacitance at the NC port when the corresponding channel (NC to COM) is OFF   |
| CNO(OFF)          | Capacitance at the NO port when the corresponding channel (NO to COM) is OFF   |
| CNC(ON)           | Capacitance at the NC port when the corresponding channel (NC to COM) is ON  |
| CNO(ON)           | Capacitance at the NO port when the corresponding channel (NO to COM) is ON  |
| CCOM(OFF)         | Capacitance at the COM port when the corresponding channel (COM to NC or COM to NO) is OFF   |
| CCOM(ON)          | Capacitance at the COM port when the corresponding channel (COM to NC or COM to NO) is ON  |
| C <sub>I</sub>    | Capacitance of control input (IN, EN)  |
| O <sub>ISO</sub>  | OFF isolation of the switch is a measurement of OFF-state switch impedance. This is measured in dB in a specific frequency, with the corresponding channel (NC to COM or NO to COM) in the OFF state.  |
| X <sub>TALK</sub> | Crosstalk is a measurement of unwanted signal coupling from an ON channel to an OFF channel (NC to NO or NO to NC). This is measured in a specific frequency and in dB.  |
| BW                | Bandwidth of the switch. This is the frequency where the gain of an ON channel is –3 dB below the DC gain.   |
| THD               | Total harmonic distortion describes the signal distortion caused by the analog switch. This is defined as the ratio of root mean square (RMS) value of the second, third, and higher harmonic to the absolute magnitude of fundamental harmonic. |
| I <sub>+</sub>    | Static power-supply current with the control (IN, EN) pin at V <sub>+</sub> or GND   |

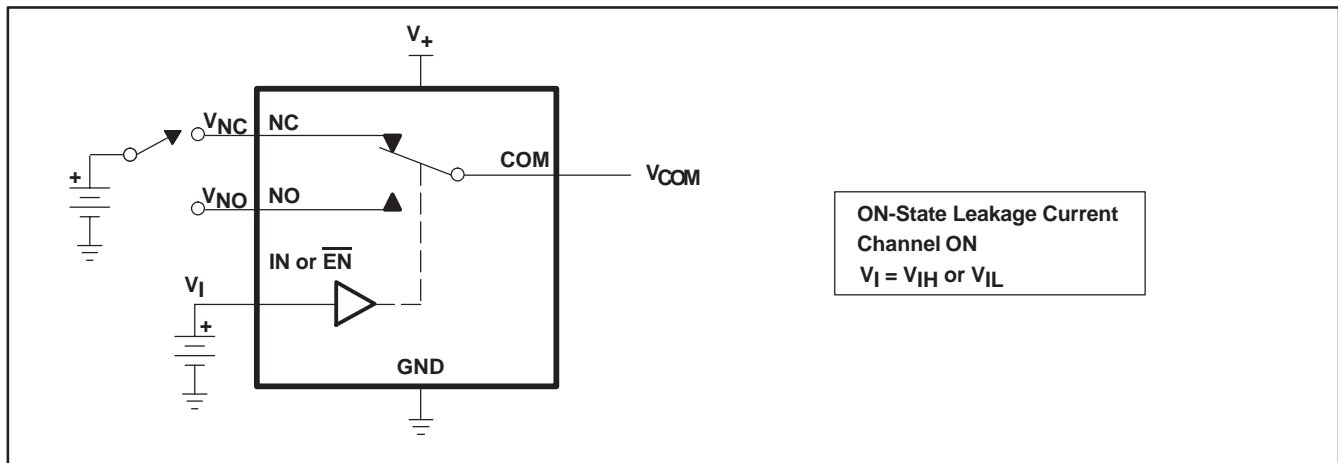
**PARAMETER MEASUREMENT INFORMATION**



**Figure 13. ON-State Resistance ( $r_{on}$ )**



**Figure 14. OFF-State Leakage Current ( $I_{NC(OFF)}$ ,  $I_{NO(OFF)}$ ,  $I_{COM(OFF)}$ )**



**Figure 15. ON-State Leakage Current ( $I_{COM(ON)}$ ,  $I_{NC(ON)}$ ,  $I_{NO(ON)}$ )**



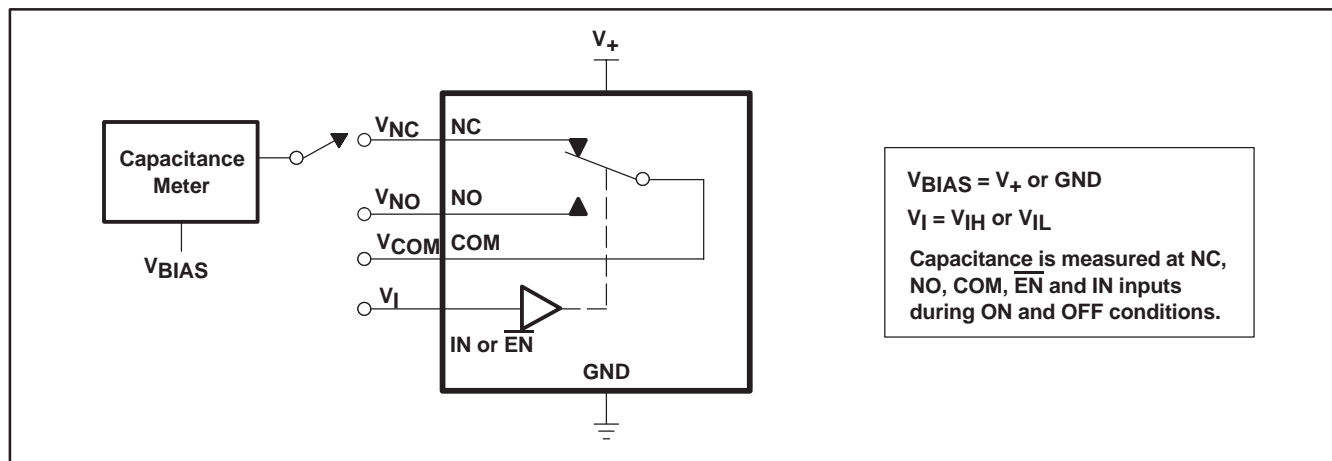
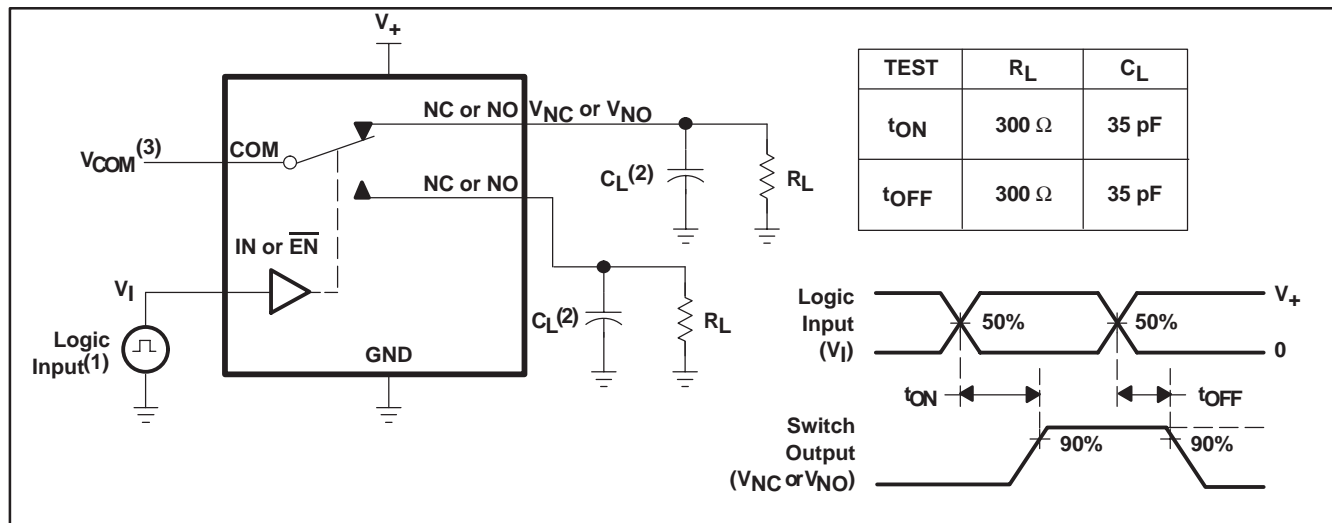


Figure 16. Capacitance ( $C_I$ ,  $C_{COM(OFF)}$ ,  $C_{COM(ON)}$ ,  $C_{NC(OFF)}$ ,  $C_{NO(OFF)}$ ,  $C_{NC(ON)}$ ,  $C_{NO(ON)}$ )



- (1) All input pulses are supplied by generators having the following characteristics: PRR  $\leq$  10 MHz,  $Z_O = 50 \Omega$ ,  $t_r < 5$  ns,  $t_f < 5$  ns.
- (2)  $C_L$  includes probe and jig capacitance.
- (3) See Electrical Characteristics for  $V_{COM}$ .

Figure 17. Turn-On ( $t_{ON}$ ) and Turn-Off Time ( $t_{OFF}$ )

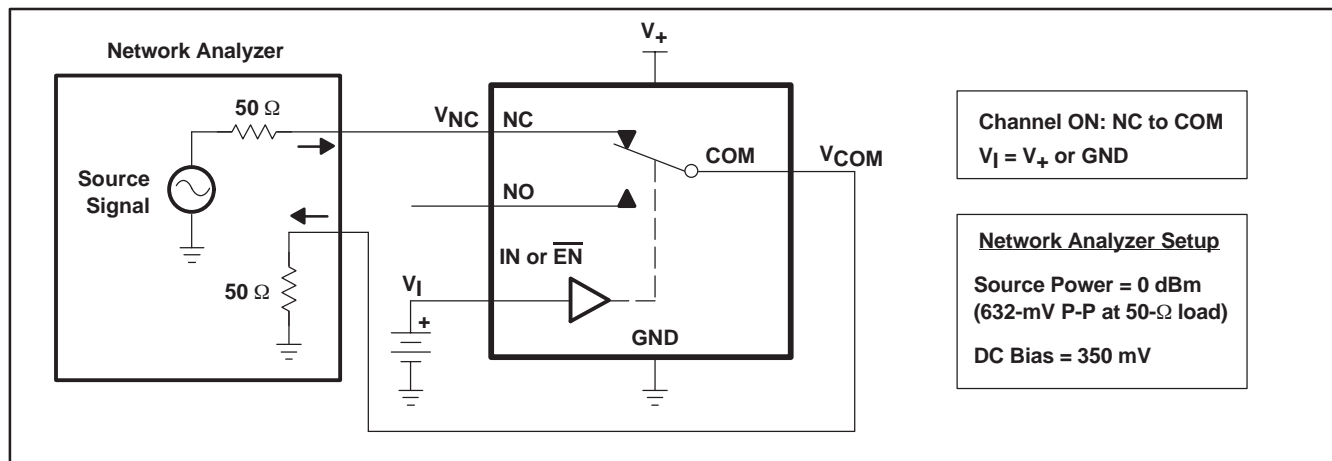


Figure 18. Bandwidth (BW)

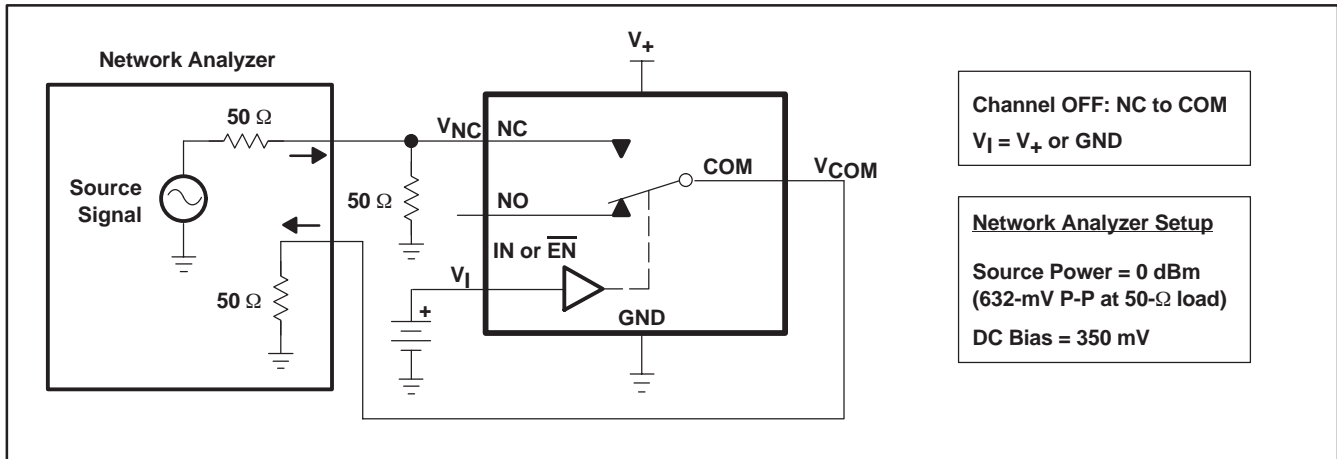


Figure 19. OFF Isolation ( $O_{ISO}$ )

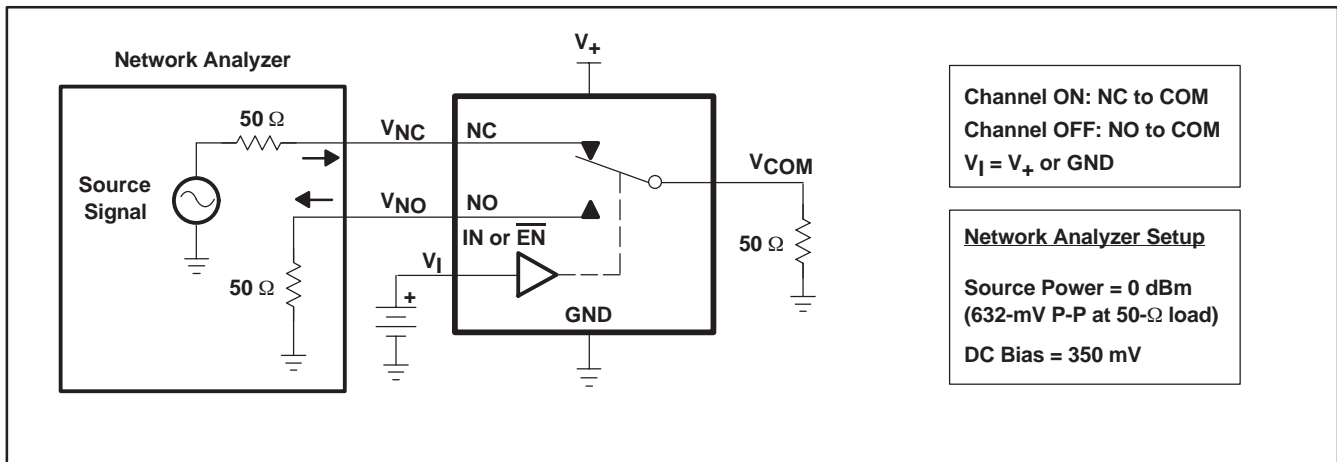
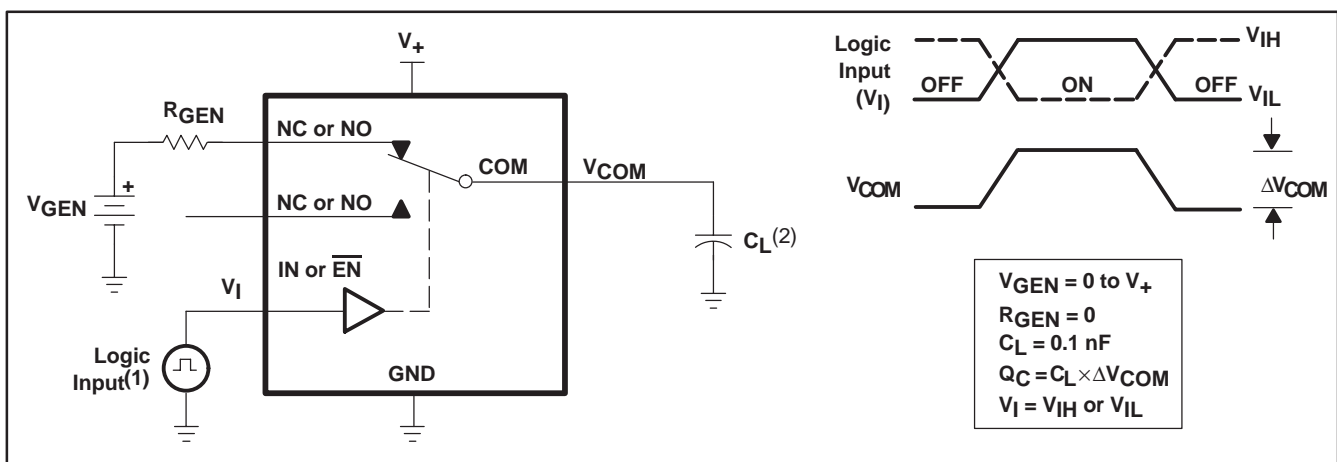


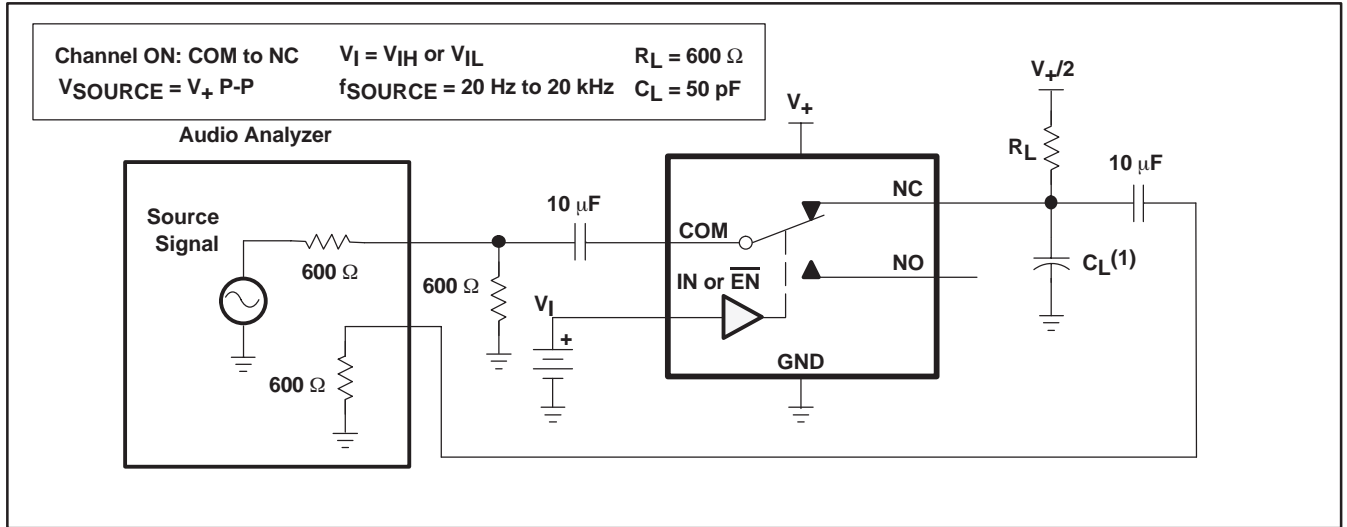
Figure 20. Crosstalk ( $X_{TALK}$ )



(1) All input pulses are supplied by generators having the following characteristics: PRR ≤ 10 MHz,  $Z_O = 50 \Omega$ ,  $t_r < 5 \text{ ns}$ ,  $t_f < 5 \text{ ns}$ .

(2)  $C_L$  includes probe and jig capacitance.

Figure 21. Charge Injection ( $Q_C$ )



(1)  $C_L$  includes probe and jig capacitance.

**Figure 22. Total Harmonic Distortion (THD)**

**PACKAGING INFORMATION**

| Orderable Device | Status<br>(1) | Package Type | Package Drawing | Pins | Package Qty | Eco Plan<br>(2) | Lead finish/<br>Ball material<br>(6) | MSL Peak Temp<br>(3) | Op Temp (°C) | Device Marking<br>(4/5) | Samples                 |
|------------------|---------------|--------------|-----------------|------|-------------|-----------------|--------------------------------------|----------------------|--------------|-------------------------|-------------------------|
| TS5A2053DCTR     | ACTIVE        | SM8          | DCT             | 8    | 3000        | RoHS & Green    | NIPDAU                               | Level-1-260C-UNLIM   | -40 to 85    | JAF<br>(R, Z)           | <a href="#">Samples</a> |
| TS5A2053DCTRE4   | ACTIVE        | SM8          | DCT             | 8    | 3000        | TBD             | Call TI                              | Call TI              | -40 to 85    |                         | <a href="#">Samples</a> |
| TS5A2053DCUR     | ACTIVE        | VSSOP        | DCU             | 8    | 3000        | RoHS & Green    | NIPDAU   SN                          | Level-1-260C-UNLIM   | -40 to 85    | (AF, JAFQ, JAFR)<br>JZ  | <a href="#">Samples</a> |
| TS5A2053DCURG4   | ACTIVE        | VSSOP        | DCU             | 8    | 3000        | RoHS & Green    | NIPDAU                               | Level-1-260C-UNLIM   | -40 to 85    | JAFR                    | <a href="#">Samples</a> |

(1) The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSELETE:** TI has discontinued the production of the device.

(2) **RoHS:** TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

**RoHS Exempt:** TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

**Green:** TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead finish/Ball material - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

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**TAPE AND REEL INFORMATION**

**QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE**


\*All dimensions are nominal

| Device         | Package Type | Package Drawing | Pins | SPQ  | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|----------------|--------------|-----------------|------|------|--------------------|--------------------|---------|---------|---------|---------|--------|---------------|
| TS5A2053DCTR   | SM8          | DCT             | 8    | 3000 | 177.8              | 12.4               | 3.45    | 4.4     | 1.45    | 4.0     | 12.0   | Q3            |
| TS5A2053DCTR   | SM8          | DCT             | 8    | 3000 | 180.0              | 13.0               | 3.35    | 4.5     | 1.55    | 4.0     | 12.0   | Q3            |
| TS5A2053DCUR   | VSSOP        | DCU             | 8    | 3000 | 180.0              | 9.0                | 2.25    | 3.4     | 1.0     | 4.0     | 8.0    | Q3            |
| TS5A2053DCUR   | VSSOP        | DCU             | 8    | 3000 | 180.0              | 8.4                | 2.25    | 3.35    | 1.05    | 4.0     | 8.0    | Q3            |
| TS5A2053DCURG4 | VSSOP        | DCU             | 8    | 3000 | 180.0              | 8.4                | 2.25    | 3.35    | 1.05    | 4.0     | 8.0    | Q3            |

**TAPE AND REEL BOX DIMENSIONS**

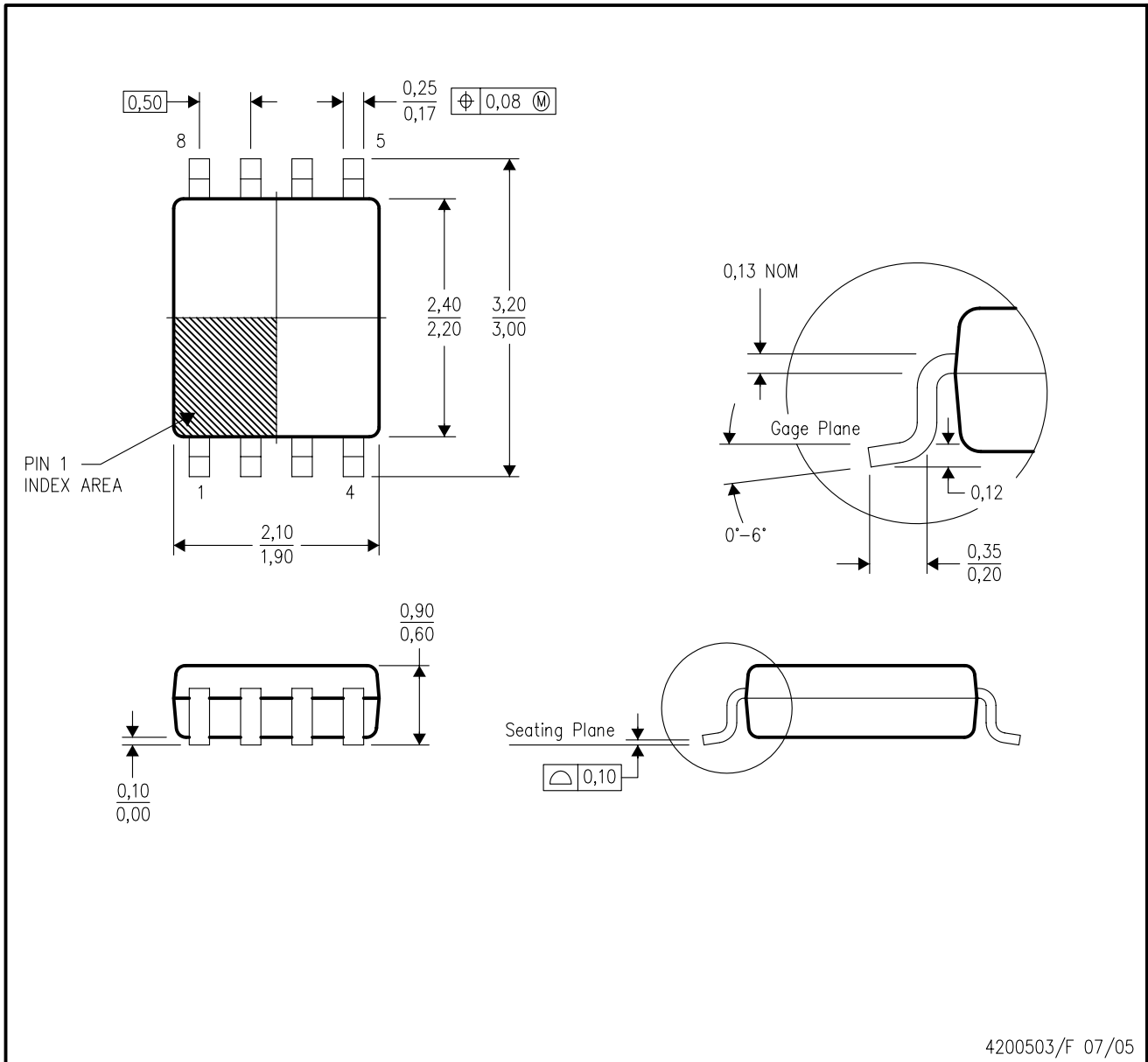

\*All dimensions are nominal

| Device         | Package Type | Package Drawing | Pins | SPQ  | Length (mm) | Width (mm) | Height (mm) |
|----------------|--------------|-----------------|------|------|-------------|------------|-------------|
| TS5A2053DCTR   | SM8          | DCT             | 8    | 3000 | 183.0       | 183.0      | 20.0        |
| TS5A2053DCTR   | SM8          | DCT             | 8    | 3000 | 182.0       | 182.0      | 20.0        |
| TS5A2053DCUR   | VSSOP        | DCU             | 8    | 3000 | 182.0       | 182.0      | 20.0        |
| TS5A2053DCUR   | VSSOP        | DCU             | 8    | 3000 | 202.0       | 201.0      | 28.0        |
| TS5A2053DCURG4 | VSSOP        | DCU             | 8    | 3000 | 202.0       | 201.0      | 28.0        |

# MECHANICAL DATA

## DCU (R-PDSO-G8)

## PLASTIC SMALL-OUTLINE PACKAGE (DIE DOWN)



- NOTES:
- A. All linear dimensions are in millimeters.
  - B. This drawing is subject to change without notice.
  - C. Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.15 per side.
  - D. Falls within JEDEC MO-187 variation CA.

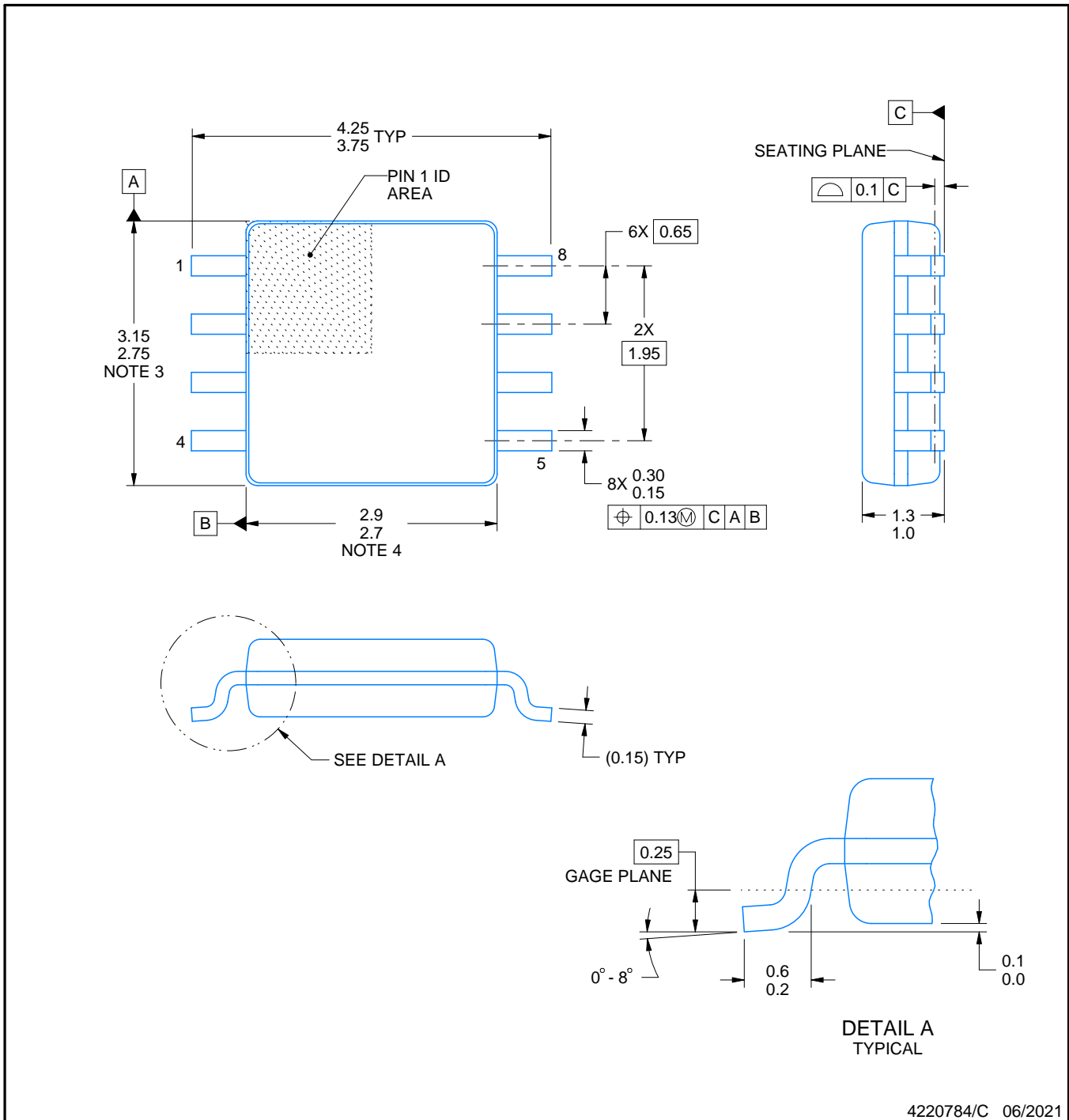
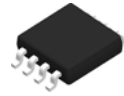


DCU (S-PDSO-G8)

PLASTIC SMALL OUTLINE PACKAGE (DIE DOWN)



- NOTES:
- A. All linear dimensions are in millimeters.
  - B. This drawing is subject to change without notice.
  - C. Publication IPC-7351 is recommended for alternate designs.
  - D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.
  - E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.



4220784/C 06/2021

NOTES:

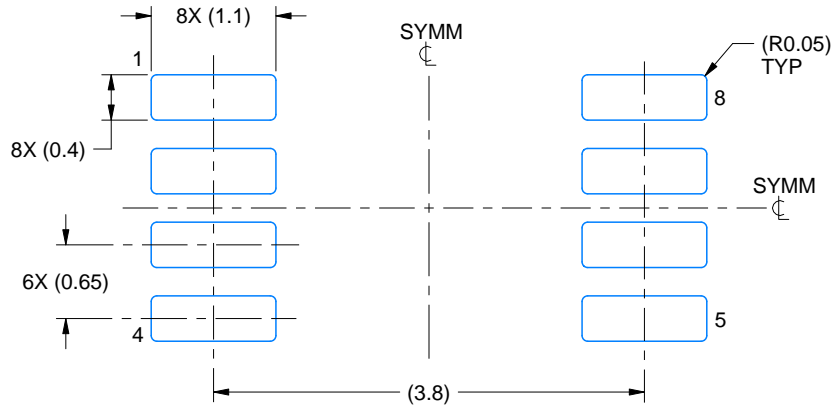
1. All linear dimensions are in millimeters. Dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. This dimension does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.15 mm per side.
4. This dimension does not include interlead flash. Interlead flash shall not exceed 0.25 mm per side.

# EXAMPLE BOARD LAYOUT

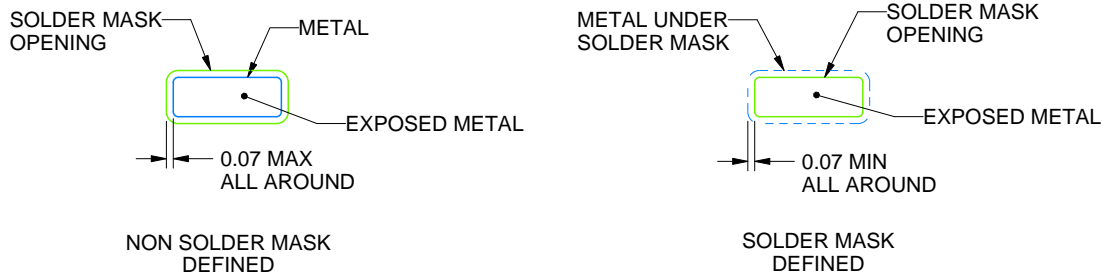
DCT0008A

SSOP - 1.3 mm max height

SMALL OUTLINE PACKAGE



LAND PATTERN EXAMPLE  
EXPOSED METAL SHOWN  
SCALE:15X



SOLDER MASK DETAILS

4220784/C 06/2021

NOTES: (continued)

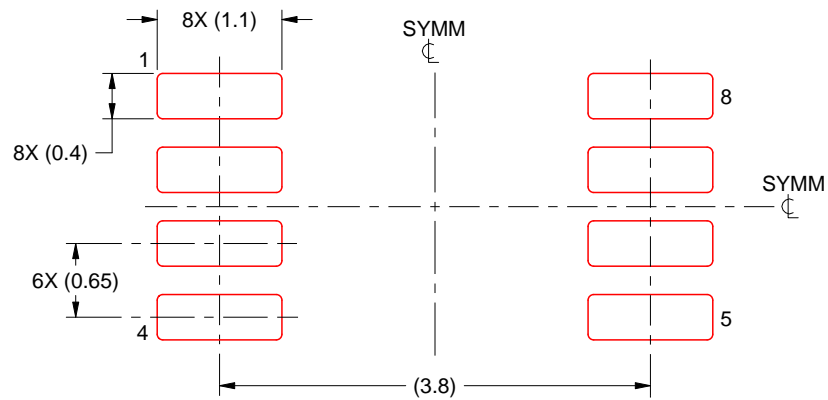
- 5. Publication IPC-7351 may have alternate designs.
- 6. Solder mask tolerances between and around signal pads can vary based on board fabrication site.

# EXAMPLE STENCIL DESIGN

DCT0008A

SSOP - 1.3 mm max height

SMALL OUTLINE PACKAGE



SOLDER PASTE EXAMPLE  
BASED ON 0.125 mm THICK STENCIL  
SCALE:15X

4220784/C 06/2021

NOTES: (continued)

7. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
8. Board assembly site may have different recommendations for stencil design.

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