

Understanding Inverting Buck-Boost Power Stages in Switch Mode Power Supplies

ABSTRACT

A switching power supply consists of the power stage and the control circuit. The power stage performs the basic power conversion from the input voltage to the output voltage and includes switches and the output filter. This application report addresses the inverting buck-boost power stage only and does not cover control circuits. Detailed steady-state and small-signal analysis of the inverting buck-boost power stage operating in continuous and discontinuous mode is presented. The application report concentrates on the non synchronous version (using a diode as second switch) as a synchronous inverting buck-boost converter acts for all load currents like a non-synchronous version in continuous conduction mode. Variations in the standard inverting buck-boost power stage and a discussion of the power stage component requirements are included.

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1 Introduction

The three basic switching power supply topologies in common use are the buck, boost, and inverting buck-boost. These topologies are non-isolated, so the input and output voltages share a common ground. There are, however, isolated derivations of these non-isolated topologies. The power supply topology refers to how the switches, output inductor, and output capacitor are connected. Each topology has unique properties. These properties include the steady-state voltage conversion ratios, the nature of the input and output currents, and the character of the output voltage ripple. Another important property is the frequency response of the duty-cycle-to-output-voltage transfer function.

The inverting buck-boost is a popular non-isolated, inverting power stage topology. Power supply designers choose the inverting buck-boost power stage because the output voltage is inverted from the input voltage, and the output voltage can be either higher or lower than the input voltage. The topology gets its name from producing an output voltage that can be higher (like a boost power stage) or lower (like a buck power stage) in magnitude than the input voltage. However, the output voltage is opposite in polarity from the input voltage. The input current for an inverting buck-boost power stage is discontinuous or pulsating due to the power switch (Q1) current that pulses from zero to I_L every switching cycle. The output current for an inverting buck-boost power stage is also discontinuous or pulsating because the output diode only conducts during a portion of the switching cycle. The output capacitor supplies the entire load current for the rest of the switching cycle.

This report describes steady-state operation of the inverting buck-boost converter in continuous-mode and discontinuous-mode operation with ideal waveforms given. The duty-cycle-to-output-voltage transfer function is given after an introduction of the PWM switch model.

Figure 1 shows a simplified schematic of the inverting buck-boost power stage with a drive circuit block included. The power switch, Q1, is an n-channel MOSFET. The output diode is CR1. The inductor, L, and capacitor, C, form the effective output filter. The capacitor ESR, R_C , (equivalent series resistance) and the inductor DC resistance, R_L , are included in the analysis. The resistor, R, represents the load seen by the power stage output.

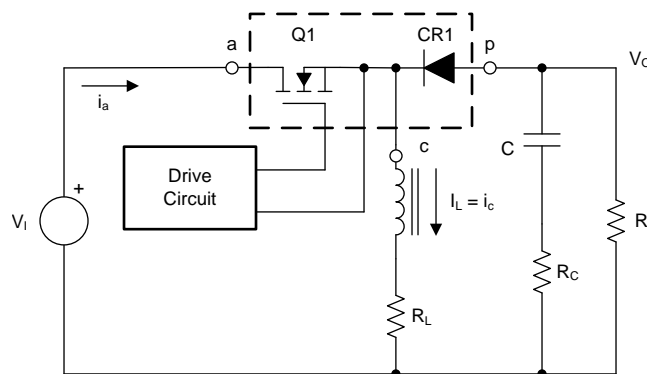


Figure 1. Inverting Buck-Boost Power Stage Schematic

During the normal operation of the inverting buck-boost power stage, Q1 is repeatedly switched on and off with the on- and off-times governed by the control circuit. This switching action gives rise to a train of pulses at the junction of Q1, CR1, and L. Although the inductor, L, is connected to the output capacitor, C, only when CR1 conducts, an effective LC output filter is formed. It filters the train of pulses to produce a DC output voltage.

2 Inverting Buck-Boost Stage Steady-state Analysis

A power stage can operate in continuous or discontinuous inductor current mode. Continuous inductor current mode is characterized by current flowing continuously in the inductor during the entire switching cycle in steady-state operation. Discontinuous inductor current mode is characterized by the inductor current being zero for a portion of the switching cycle. It starts at zero, reaches a peak value, and returns to zero during each switching cycle. The two different modes are discussed in greater detail later and design guidelines for the inductor value to maintain a chosen mode of operation as a function of rated load are given. It is desirable for a converter to stay in one mode only over its expected operating conditions because the power stage frequency response changes significantly between the two different modes of operation.

For this analysis, an n-channel power MOSFET is used and a positive voltage, $V_{GS(ON)}$, is applied from the Gate to the Source terminals of Q1 by the drive circuit to turn ON the FET. The advantage of using an n-channel FET is its lower $R_{DS(on)}$, but the drive circuit is more complicated because a floating drive is required. For the same die size, a p-channel FET has a higher $R_{DS(on)}$, but usually does not require a floating drive circuit.

In [Figure 1](#), the transistor Q1 and diode CR1 are drawn inside a dashed-line box with terminals labeled a, p, and c. This is explained fully in [Section 3](#).

2.1 Inverting Buck-Boost Steady-state Continuous Conduction Mode Analysis

The following is a description of steady-state operation in continuous conduction mode. The main goal of this section is to provide a derivation of the voltage conversion relationship for the continuous conduction mode inverting buck-boost power stage. This is important because it shows the output voltage depends on duty cycle and input voltage or conversely, how the duty cycle can be calculated based on input voltage and output voltage. Steady-state implies that the input voltage, output voltage, output load current, and duty-cycle are fixed and not varying. Capital letters are generally given to variable names to indicate a steady-state quantity.

In the continuous conduction mode, the inverting buck-boost converter assumes two states per switching cycle. The ON State is when Q1 is ON and CR1 is OFF. The OFF State is when Q1 is OFF and CR1 is ON. A simple linear circuit can represent each of the two states where the switches in the circuit are replaced by their equivalent circuit during each state. [Figure 2](#) shows the circuit diagram for each of the two states.

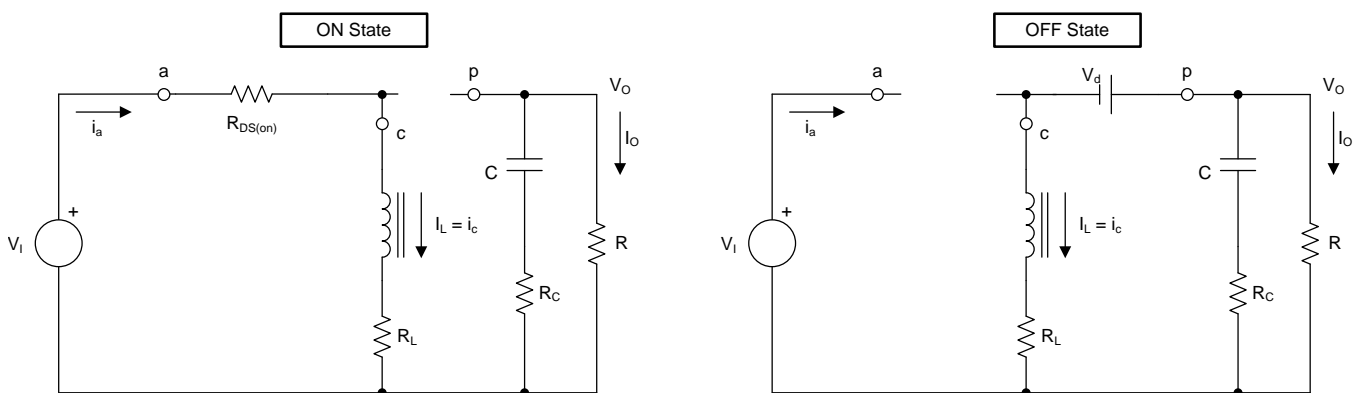


Figure 2. Inverting Buck-Boost Power Stage States

The duration of the ON state is $D \times T_S = T_{ON}$ where D is the duty cycle, set by the control circuit, expressed as a ratio of the switch ON time to the time of one complete switching cycle, T_S . The duration of the OFF state is called T_{OFF} . Since there are only two states per switching cycle for continuous conduction mode, T_{OFF} is equal to $(1-D) \times T_S$. The quantity $(1-D)$ is sometimes called D' . [Figure 3](#) shows these times and the waveforms.

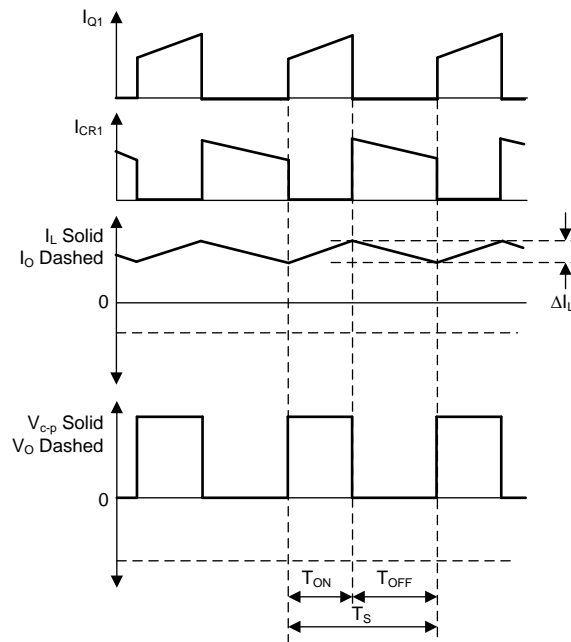


Figure 3. Continuous Mode Inverting Buck-Boost Power Stage Waveforms

Referring to [Figure 2](#), during the ON state, Q1 presents a low resistance, $R_{DS(on)}$, from its drain to source. It also exhibits a small voltage drop of $V_{DS} = I_L \times R_{DS(on)}$. There is also a small voltage drop across the DC resistance of the inductor equal to $I_L \times R_L$. Thus, the input voltage, V_I , minus losses, $(V_{DS} + I_L \times R_L)$, is applied across the inductor, L . CR1 is OFF during this time because it is reverse biased. The inductor current, I_L , flows from the input source, V_I , through Q1 and to ground. During the ON state, the voltage applied across the inductor is constant and equal to $V_I - V_{DS} - I_L \times R_L$. [Figure 2](#) shows the polarity convention for the current. The inductor current increases as a result of the applied voltage when adopting this polarity convention for the current I_L . Also, since the applied voltage is essentially constant, the inductor current increases linearly. [Figure 3](#) shows this increase in inductor current during T_{ON} . [Equation 1](#) shows a version of the familiar relationship and how to calculate the amount that the inductor current increases.

$$V_L = L \times \frac{di_L}{dt} \Rightarrow \Delta I_L = \frac{V_L}{L} \times \Delta T \quad (1)$$

[Equation 2](#) shows how the inductor current increases during the ON state.

$$\Delta I_L(+) = \frac{V_I - (V_{DS} + I_L \times R_L)}{L} \times T_{ON} \quad (2)$$

This quantity, $\Delta I_L(+)$, is referred to as the inductor ripple current. Notice that during this period, all of the output load current is supplied by the output capacitor, C .

Referring to [Figure 2](#), when Q1 is OFF, it presents a high impedance from its drain to source. Therefore, since the current flowing in the inductor L cannot change instantaneously, the current shifts from Q1 to CR1. Due to the decreasing inductor current, the voltage across the inductor reverses polarity until rectifier CR1 becomes forward biased and turns ON. The voltage applied across L becomes $(V_O - V_d - I_L \times R_L)$ where the quantity, V_d , is the forward voltage drop of CR1. The inductor current, I_L , now flows from the output capacitor and load resistor combination through CR1 and to ground. Notice that the orientation of CR1 and the direction of current flow in the inductor means that the current flowing in the output capacitor and load resistor combination causes V_O to be a negative voltage. During the OFF state, the voltage applied across the inductor is constant and equal to $(V_O - V_d - I_L \times R_L)$. Maintaining the same polarity

convention, this applied voltage is negative (or opposite in polarity from the applied voltage during the ON time), because the output voltage V_O is negative. Hence, the inductor current decreases during the OFF time. Also, since the applied voltage is essentially constant, the inductor current decreases linearly. [Figure 3](#) shows this decrease in inductor current during T_{OFF} . [Equation 3](#) shows how to calculate the inductor current decrease during the OFF state.

$$\Delta I_L(-) = \frac{-(V_O - V_d - I_L \times R_L)}{L} \times T_{OFF} \quad (3)$$

This quantity, $\Delta I_L(-)$, is also referred to as the inductor ripple current. In steady-state conditions, the current increase, $\Delta I_L(+)$, during the ON time and the current decrease during the OFF time, $\Delta I_L(-)$, must be equal. Otherwise, the inductor current has a net increase or decrease from cycle to cycle, which would not be a steady-state condition. Therefore, these two equations can be equated and solved for V_O to obtain the continuous conduction mode inverting buck-boost voltage conversion relationship.

[Equation 4](#) shows how to solve for V_O .

$$V_O = - \left[(V_I - V_{DS}) \times \frac{T_{ON}}{T_{OFF}} - V_d - I_L \times R_L \times \frac{T_{ON} + T_{OFF}}{T_{OFF}} \right] \quad (4)$$

[Equation 5](#) shows how to solve for V_O while substituting T_S for $T_{ON} + T_{OFF}$, and using $D = T_{ON} / T_S$ and $(1 - D) = T_{OFF} / T_S$.

$$V_O = - \left[(V_I - V_{DS}) \times \frac{D}{1 - D} - V_d - \frac{I_L \times R_L}{1 - D} \right] \quad (5)$$

Notice that in simplifying the above, $T_{ON} + T_{OFF}$ is assumed to be equal to T_S . This is true only for continuous conduction mode seen in the discontinuous conduction mode analysis.

NOTE: An important observation must be made here. Setting the two values of ΔI_L equal to each other is precisely equivalent to balancing the volt-seconds on the inductor. The volt-seconds applied to the inductor is the product of the voltage applied and the time that the voltage is applied. This is the best way to calculate unknown values such as V_O or D in terms of known circuit parameters, and this method is applied repeatedly in this paper. Volt-second balance on the inductor is a physical necessity and must be understood at least as well as Ohms Law.

In the above equations for $\Delta I_L(+)$ and $\Delta I_L(-)$, the output voltage is implicitly assumed to be constant with no AC ripple voltage during the ON time and the OFF time. This is a common simplification and involves two separate effects. First, the output capacitor is assumed to be large enough that its voltage change is negligible. Second, the voltage due to the capacitor ESR is also assumed to be negligible. These assumptions are valid because the AC ripple voltage is designed to be much less than the DC part of the output voltage.

The above voltage conversion relationship for V_O illustrates the fact that V_O can be adjusted by adjusting the duty cycle, D . This relationship approaches zero as D approaches zero and increases without bound as D approaches 1. A common simplification is to assume V_{DS} , V_d , and R_L are small enough to ignore. [Equation 6](#) shows a simplification of the above equations when you set V_{DS} , V_d , and R_L to zero.

$$V_O = -V_I = \frac{D}{1 - D} \quad (6)$$

A simplified, qualitative way to visualize the circuit operation is to consider the inductor as an energy storage element. When Q1 is on, energy is added to the inductor. When Q1 is off, the inductor delivers some of its energy to the output capacitor and load. The output voltage is controlled by setting the on-time of Q1. For example, by increasing the on-time of Q1, the amount of energy delivered to the inductor is increased. More energy is then delivered to the output during the off-time of Q1, resulting in an increase in the output voltage.

Unlike the buck power stage, the average of the inductor current is not equal to the output current. To relate the inductor current to the output current, referring to [Figure 2](#) and [Figure 3](#), note that the inductor delivers current to the output only during the off state of the power stage. This current averaged over a complete switching cycle is equal to the output current because the average current in the output capacitor must be equal to zero. [Equation 7](#) or [Equation 8](#) calculates the relationship between the average inductor current and the output current for the continuous mode inverting buck-boost power stage.

$$I_{L(Avg)} \times \frac{T_{OFF}}{T_S} = I_{L(Avg)} \times (1 - D) = -I_O \quad (7)$$

or

$$I_{L(Avg)} = \frac{-I_O}{(1 - D)} \quad (8)$$

Another important observation is that the average inductor current is proportional to the output current, and since the inductor ripple current, ΔI_L , is independent of output load current, the minimum and the maximum values of the inductor current track the average inductor current exactly. For example, if the average inductor current decreases by 2 A due to a load current decrease, then the minimum and maximum values of the inductor current decrease by 2 A (assuming continuous conduction mode is maintained).

The forgoing analysis is for the inverting buck-boost power stage operation in continuous inductor current mode. [Section 2.2](#) is a description of steady-state operation in discontinuous conduction mode. The main result is a derivation of the voltage conversion relationship for the discontinuous conduction mode inverting buck-boost power stage.

2.2 Inverting Buck-Boost Steady-state Discontinuous Conduction Mode Analysis

This section investigates what happens when the load current is decreased and the conduction mode changes from continuous to discontinuous. Recall for continuous conduction mode, the average inductor current tracks the output current, meaning if the output current decreases, then so does the average inductor current. In addition, the minimum and maximum peaks of the inductor current follow the average inductor current exactly.

If the output load current is reduced below the critical current level, the inductor current is zero for a portion of the switching cycle. This is evident from the waveforms in [Figure 3](#) as the inductor ripple current is independent on the load current. In a non-synchronous inverting buck-boost power stage, if the inductor current attempts to fall below zero, it just stops at zero (due to the unidirectional current flow in CR1) and remains there until the beginning of the next switching cycle. This operating mode is called discontinuous conduction mode. A power stage operating in discontinuous conduction mode has three unique states during each switching cycle as opposed to two states for continuous conduction mode. [Figure 4](#) shows the inductor current condition where the power stage is at the boundary between continuous and discontinuous mode. This is where the inductor current just falls to zero and the next switching cycle begins immediately after the current reaches zero. [Figure 4](#) shows the absolute values of I_O and $I_{O(Crit)}$. Note that there are absolute values because I_O and I_L have opposite polarities.

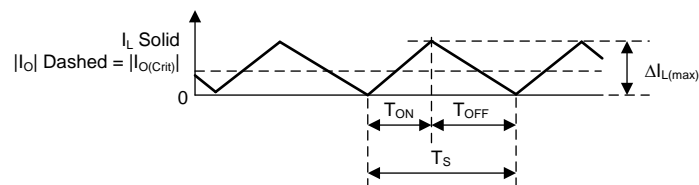


Figure 4. Boundary Between Continuous and Discontinuous Mode

Further reduction in output load current puts the power stage into discontinuous conduction mode. [Figure 5](#) illustrates this condition. The discontinuous mode power stage frequency response is different from the continuous mode frequency response and is given in [Section 3](#). Also, the input to output relationship is different, as the following derivations show.

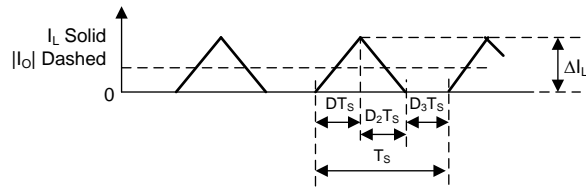


Figure 5. Discontinuous Current Mode

To begin the derivation of the discontinuous conduction mode inverting buck-boost power stage voltage conversion ratio, recall that there are three unique states that the converter assumes during discontinuous conduction mode operation. The ON State is when Q1 is ON and CR1 is OFF. The OFF State is when Q1 is OFF and CR1 is ON. The IDLE state is when both Q1 and CR1 are OFF. The first two states are identical to those of the continuous mode case. Figure 2 shows the circuits that are applicable except that $T_{OFF} \neq (1-D) \times T_S$. The remainder of the switching cycle is the IDLE state. In addition, the DC resistance of the output inductor, the output diode forward voltage drop, and the power MOSFET ON-state voltage drop are all assumed to be small enough to omit.

The duration of the ON state is $T_{ON} = D \times T_S$ where D is the duty cycle, set by the control circuit and expressed as a ratio of the switch ON time to the time of one complete switching cycle, T_S . The duration of the OFF state is $T_{OFF} = D_2 \times T_S$. The IDLE time is the remainder of the switching cycle and is given as $T_S - T_{ON} - T_{OFF} = D_3 \times T_S$. Figure 6 shows these times with the corresponding waveforms. Without going through the detailed explanation as before, the equations for the inductor current increase and decrease are given below. Equation 9 shows how to find the inductor current increase during the ON state.

$$\Delta I_L (+) = \frac{V_I}{L} \times T_{ON} = \frac{V_I}{L} \times D \times T_S = I_{PK} \quad (9)$$

The ripple current magnitude, $\Delta I_L(+)$, is also the peak inductor current, I_{pk} , because in discontinuous mode, the current starts at zero each cycle. Equation 10 gives the inductor current decrease during the OFF state.

$$\Delta I_L (-) = \frac{-V_O}{L} \times T_{OFF} = \frac{-V_O}{L} \times D_2 \times T_S \quad (10)$$

As in the continuous conduction mode case, the current increase, $\Delta I_L(+)$, during the ON time and the current decrease during the OFF time, $\Delta I_L(-)$, are equal. Therefore, these two equations can be equated and solved for V_O to obtain the first of two equations to be used to solve for the voltage conversion ratio.

$$V_O = -V_I \times \frac{T_{ON}}{T_{OFF}} = -V_I \times \frac{D}{D_2} \quad (11)$$

The output current (the output voltage V_O divided by the output load R). It is the average over one switching cycle of the inductor current during the time when CR1 conducts ($D_2 \times T_S$).

$$\frac{V_O}{R} = I_O = \frac{-I_{PK}}{2} \times D_2 \quad (12)$$

Equation 13 shows the result of substituting the relationship for I_{PK} ($\Delta I_L(+)$) into Equation 12.

$$\frac{V_O}{R} = I_O = -\frac{1}{2} \times (-1) \times \left(\frac{V_I}{L} \times D \right) \times D_2 \quad (13)$$

$$\frac{V_O}{R} = \frac{-V_I \times D \times D_2 \times T_S}{2 \times L} \quad (14)$$

Now, there are two equations. The one for the output current (V_O divided by R) just derived and the one for the output voltage, both in terms of V_I , D, and D_2 . Solve each equation for D_2 and set the two equations equal to each other. Using the resulting equation, an expression for the output voltage, V_O , can be derived.

Equation 15 calculates the discontinuous conduction mode inverting buck-boost voltage conversion relationship.

$$V_O = -V_I \times \frac{D}{\sqrt{K}}$$

where

$$K = \frac{2 \times L}{R \times T_S} \quad (15)$$

• K is defined as

The above relationship shows one of the major differences between the two conduction modes. For the discontinuous conduction mode, the voltage conversion relationship is a function of the input voltage, duty cycle, power stage inductance, the switching frequency, and the output load resistance. For continuous conduction mode, the voltage conversion relationship is only dependent on the input voltage and duty cycle.

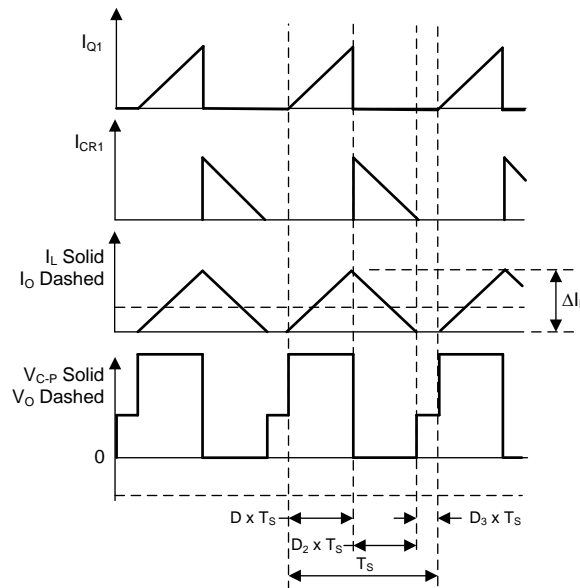


Figure 6. Discontinuous Mode Inverting Buck-Boost Converter Waveforms

In typical applications, the inverting buck-boost power stage is operated in either continuous conduction mode or discontinuous conduction mode. For a particular application, one conduction mode is chosen and the power stage is designed to maintain the same mode. [Section 2.3](#) gives inductance relationships for the power stage that allow it to operate in only one conduction mode, given ranges for input and output voltage and output load current.

2.3 Critical Inductance

The previous analyses for the inverting buck-boost converter have been for continuous and discontinuous conduction modes of steady-state operation. The conduction mode of a converter is a function of input voltage, output voltage, output current, and the value of the inductor. An inverting buck-boost converter can be designed to operate in continuous mode for load currents above a certain level, usually 5% to 10% of full load. Usually, the input voltage range, the output voltage, and load current are defined by the converter specification. This leaves the inductor value as the design parameter to maintain continuous conduction mode.

The minimum value of inductor to maintain continuous conduction mode can be determined by the following procedure.

1. Define $I_{O(\text{crit})}$ as the minimum output current to maintain continuous conduction mode, normally referred to as the critical current. [Figure 4](#) shows this value. Since you are working toward a minimum value for the inductor, it is more straightforward to perform the derivation using the inductor current. [Equation 16](#) calculates the minimum average inductor current to maintain continuous conduction mode.

$$I_{L(\text{min-avg})} = \frac{\Delta I_L}{2} = I_{O(\text{crit})} \quad (16)$$

2. Calculate L such that the above relationship is satisfied. To solve the above equation, either relationship, $\Delta I_L(+)$ or $\Delta I_L(-)$ can be used for ΔI_L . Note that either relationship for ΔI_L is independent of the output current level. Here, $\Delta I_L(+)$ is used. The worst case condition (giving the largest L_{min}) is at maximum input voltage because this gives the maximum ΔI_L . Equation 17 substitutes Equation 2 into Equation 16 and is then solved for L_{min} .

$$L_{min} \geq \frac{1}{2} \times (V_{I(max)} - V_{DS} - I_L \times R_L) \times \frac{T_{ON(min)}}{I_{O(crit)}} \quad (17)$$

Equation 17 can be simplified by ignoring minor parasitic resistances and diode voltage drops. After substituting $T_{ON(min)}$ with Equation 6 and rearranged, Equation 18 is the result.

$$L_{min} \geq \frac{-V_O \times T_S}{2 \times I_{O(crit)}} \times \frac{-V_{I(max)}}{(V_O - V_{I(max)})} \quad (18)$$

Using the inductor value you just calculated guarantees continuous conduction mode operation for output load currents above the critical current level, $I_{O(crit)}$.

3 Inverting Buck-Boost Power Stage Small Signal Modeling

This section moves from a detailed circuit oriented analysis approach to more of a system-level investigation of the inverting buck-boost power stage. This section also presents techniques to assist you in accurately modeling the power stage as a component of the control loop of an inverting buck-boost power supply. The three major components of the power supply control loop are the power stage, the pulse width modulator, and the error amplifier. Figure 7 shows these components in a block diagram.

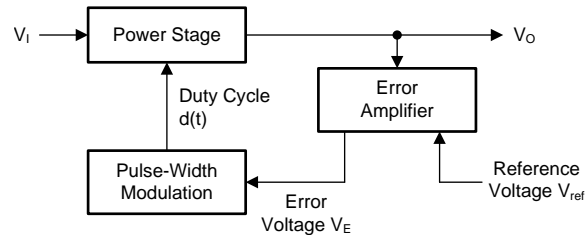


Figure 7. Power Supply Control Loop Components

Modeling the power stage presents one of the main challenges to the power supply designer. A popular technique involves modeling only the switching elements of the power stage. An equivalent circuit for these elements is derived and is called the PWM Switch Model where PWM means pulse width modulated. This approach is presented here.

Figure 7 shows the two inputs for the power stage: the input voltage and the duty cycle. The duty cycle is the control input. This input is a logic signal, which controls the switching action of the power stage and hence the output voltage. Most power stages have a nonlinear voltage conversion ratio versus duty cycle. Figure 8 shows a graph of the steady-state voltage conversion ratio for an inverting buck-boost power stage operating in continuous conduction mode as a function of steady-state duty cycle, D .

The nonlinear characteristics are a result of the switching action of the power stage switching components, Q1 and CR1. It is observed in the *Simplified Analysis of PWM Converters Using the Model of the PWM Switch: Parts I and II*, available in Section 7, that the only nonlinear components in a power stage are the switching devices – the remainder of the circuit consists of linear elements. It is also shown in the *Simplified Analysis of PWM Converters Using the Model of the PWM Switch: Parts I and II* that a linear model of only the nonlinear components can be derived by averaging the voltages and currents associated with these nonlinear components over one switching cycle. The model is then substituted into the original circuit for analysis of the complete power stage. Thus, a model of the switching devices is given and is called the PWM switch model.

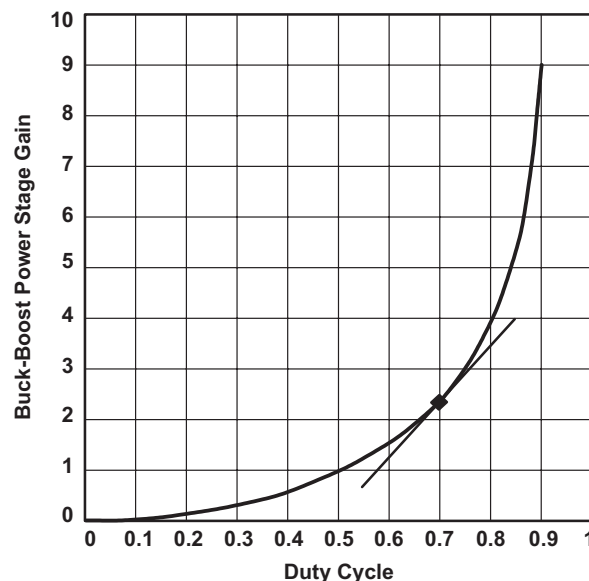


Figure 8. Inverting Buck-Boost Power Stage Gain Versus Duty Cycle

The basic objective behind modeling power stages is to represent the non-linear behavior of power stages as linear about an operating point. You want linearity so that you can apply the many analysis tools available for linear systems. Referring to Figure 8, if you choose the operating point of $D = 0.7$, a straight line can be constructed that is tangent to the original curve at the point where $D = 0.7$. This is an illustration of linearization about an operating point, a technique used to derive the PWM switch model. Qualitatively, you can see that if the variations in duty cycle are kept small, a linear model accurately represents the nonlinear behavior of the power stage being analyzed.

Since a power stage can operate in one of two conduction modes such as continuous conduction mode (CCM) or discontinuous conduction mode (DCM), there is a PWM switch model for the two conduction modes. The DCM PWM switch model is derived here. The CCM PWM switch model is derived in the [Understanding Buck Power Stages in Switchmode Power Supplies Application Report](#).

3.1 Inverting Buck-Boost Continuous Conduction Mode Small-Signal Analysis

To model the inverting buck-boost power stage operation in CCM, use the CCM PWM switch model derived in the [Understanding Buck Power Stages in Switchmode Power Supplies Application Report](#). The PWM switch model is inserted into the power stage circuit by replacing the switching elements. Figure 9 shows the CCM PWM Switch model, which is useful for determining the DC operating point of a power stage and for finding AC transfer functions of a power stage.

Referring to Figure 1, the power transistor, Q1, and the catch diode, and CR1, are drawn inside a dashed-line box. These are the components that are replaced by the PWM switch equivalent circuit. Terminal labels a (active), p (passive), and c (common) are used for the PWM switch model.

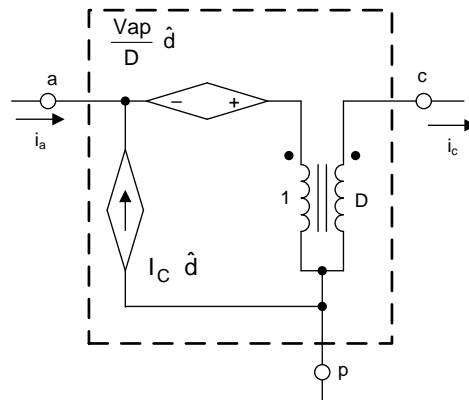
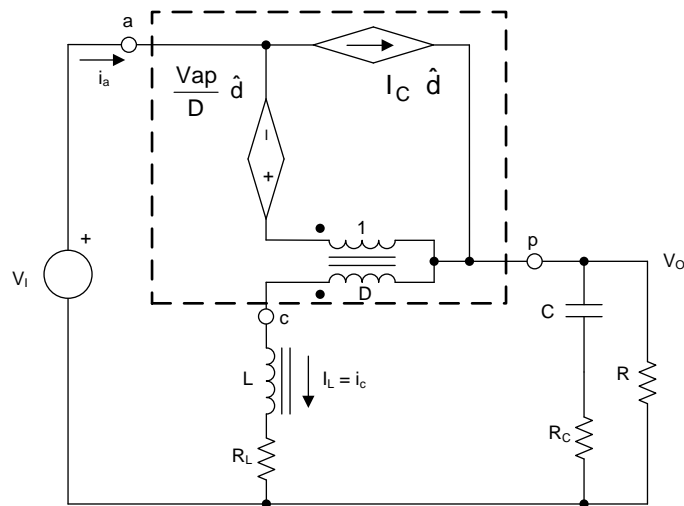


Figure 9. DC and Small Signal CCM PWM Switch Model

The a terminal is the terminal connected to the active switch. The p terminal is the terminal of the passive switch. The c terminal is the terminal that is common to both the active and passive switches, and the above terminal definitions can be used. In addition, substituting the PWM switch model (that you derive) into other power stage topologies also produces a valid model for that particular power stage. To use the PWM switch model in other power stages, substitute the model in Figure 9 into the power stage in the appropriate orientation.

In the PWM switch model in Figure 9 and subsequent occurrences of the model, the capital letters indicate steady-state (or DC) quantities dependent on the operating point of the circuit under study. The lowercase letters indicate time varying quantities and can indicate a quantity with a DC component and an AC component. The lowercase letters with a caret (hat) indicate the small AC variations of that particular

variable. For example, D represents the steady-state duty cycle, \hat{d} represents small AC variations of the duty cycle, and d or $d(t)$ represents the complete duty cycle including any DC component and AC variations.


Figure 10. CCM Inverting Buck-Boost Small Signal AC Power Stage Model

An example DC analysis is given to illustrate how simple power stage analysis becomes with the PWM switch model. For DC analysis, \hat{d} is zero, L is a short and C is an open. Equation 19 shows the result of a simple loop equation.

$$-V_I + \frac{V_{cp}}{D} - V_O = 0 \rightarrow V_{cp} = (V_I - V_O) \times D \quad (19)$$

By using the following relationships:

$$I_L = \frac{-I_O}{1-D} \quad (20)$$

$$I_O = \frac{V_O}{R} \quad (21)$$

And $V_{cp} + V_O - I_L \times R_L = 0$, Equation 22 shows the result solved for V_{cp} .

$$V_{cp} = -V_O \times \left(1 + \frac{R_L}{R \times (1-D)} \right) \quad (22)$$

Equation 23 shows the result of setting the two loop equations solved for V_{cp} equal. It is solved for V_O to get the steady-state voltage conversion relationship for the inverting buck-boost power stage operating in CCM, taking the inductor DC resistance, R_L into account.

$$V_O = -V_I \times \frac{D}{1-D} \times \frac{1}{1 + \frac{R_L}{R \times (1-D)^2}} \quad (23)$$

The above equation is usually expressed as a ratio of the output voltage, V_O , to the input voltage, V_I , and is usually called M. Equation 24 shows this ratio.

$$M = \frac{V_O}{V_I} = -\frac{D}{1-D} \times \frac{1}{1 + \frac{R_L}{R \times (1-D)^2}} \quad (24)$$

When $R_L = 0$, as assumed earlier, M is equal to the steady-state I/O transfer function previously calculated. With the PWM switch parameters (V_{ap} and I_c) determined from the DC analysis, an AC analysis can be performed. For AC analysis, the following transfer functions can be calculated: open-loop line-to-output, open-loop input impedance, open-loop output impedance, and open-loop control-to-output. The control-to-output, or duty-cycle-to-output, is the transfer function most used for control loop analysis. To determine this transfer function, use the results from the DC analysis for operating point information. This information determines the parameter values of the dependent sources, see [Equation 25](#) and [Equation 26](#) as an example.

$$V_{ap} = V_I - V_O = V_I \times (1 - M)$$

and

$$I_c = I_L = \frac{-I_O}{1 - D} = \frac{-V_O}{R \times (1 - D)} = \frac{-M \times V_I}{R \times (1 - D)} \quad (25)$$

The above two equations are then used with loop equations to derive the duty-cycle-to-output voltage transfer function. Set the input voltage equal to zero since you only want the AC component of the transfer function. Without going through all the details, the following equations show that the transfer function can be put as in [Equation 27](#).

$$\frac{\hat{v}_O}{\hat{d}}(s) = G_{do} \times \frac{\left(1 + \frac{s}{\omega_{z1}}\right) \times \left(1 - \frac{s}{\omega_{z2}}\right)}{1 + \frac{s}{\omega_o \times Q} + \frac{s^2}{\omega_o^2}}$$

where

- $G_{do} \approx \frac{V_I}{(1 - D)^2}$
- $S_{z1} = \frac{1}{R_C \times C}$
- $S_{z2} \approx \frac{(1 - D)^2 \times R}{D \times L}$
- $\omega_o \approx \frac{1 - D}{\sqrt{L \times C}}$
- $Q \approx \frac{(1 - D) \times R}{\sqrt{\frac{L}{C}}}$

(27)

3.2 Inverting Buck-Boost Discontinuous Conduction Mode Small-Signal Analysis

The section discusses modeling the power stage when it is operating in discontinuous conduction mode (DCM). This mode is quite different from the continuous conduction mode just covered. Start with the derivation of the PWM switch model for DCM for the inverting buck-boost power stage. This derivation can also be found in the *Fundamentals of Power Electronics paper*. This reference is available in [Section 7](#). The waveforms, that are averaged, are the voltage across Q1, v_{ac} , the voltage across CR1, v_{cp} , the current in Q1, i_a , and the current in CR1, i_p . [Figure 6](#) shows the waveforms.

Some basic relationships that are used repeatedly, are stated first. [Equation 28](#) and [Equation 29](#) give the terminal currents averaged over one switching cycle.

$$\langle i_a \rangle = \frac{i_{pk}}{2} \times d \quad (28)$$

$$\langle i_p \rangle = \frac{i_{pk}}{2} \times d_2$$

where

- variables in brackets (for example $\langle i_a \rangle$) represent quantities that are averaged over one switching cycle (29)

Since the average over one switching cycle of the voltage across the inductor is zero, the following average voltage relationships hold:

$$\langle v_{ac} \rangle = V_I + (i_c \times R_L) \quad (30)$$

$$\langle v_{cp} \rangle = -V_O - (i_c \times R_L) \quad (31)$$

Since the value of $i_c \times R_L$ is small compared to V_I and V_O , ignore these values for modeling the power stage to make the formulae much easier to manipulate. During the time period $d \times T_S$, the current i_a starts at a value of zero and ends at the value of i_{pk} . Since the voltage across the inductor during this time is constant and equal to $V_I = \langle v_{ac} \rangle$, Equation 32 holds.

$$V_I = L \frac{\Delta i_a}{\Delta t} = L \times \frac{i_{pk}}{d \times T_S} \Rightarrow \langle v_{ac} \rangle = L \times \frac{i_{pk}}{d \times T_S} \quad (32)$$

Similarly, during the time period $d_2 \times T_S$, the current i_p starts at a value of i_{pk} and ends at zero. Also since the voltage across the inductor is equal to $-V_O = \langle v_{cp} \rangle$, Equation 33 holds.

$$V_O = L \frac{\Delta i_p}{\Delta t} = L \times \frac{-i_{pk}}{d_2 \times T_S} \Rightarrow \langle v_{cp} \rangle = L \times \frac{i_{pk}}{d_2 \times T_S} \quad (33)$$

With Equation 28, Equation 29, Equation 32, and Equation 33, begin with the derivation of the input side (v_{ac} side) of the PWM switch model. Solve Equation 32 for i_{pk} and use $V_I = \langle v_{ac} \rangle$, then substitute into Equation 28 to get Equation 34.

$$\langle i_a \rangle = V_I \times \frac{d^2 \times T_S}{2 \times L} \quad (34)$$

Note that the average current flowing into terminal a is proportional to the input voltage, V_I . Define an effective resistance as Equation 35.

$$R_e = \frac{V_I}{\langle i_a \rangle} = \frac{2 \times L}{d^2 \times T_S} \quad (35)$$

As the input looks like an equivalent resistance, you can talk about an apparent input power of V_I^2 / R_e , which is used next.

To begin the derivation of the output side (v_{cp} side), start with Equation 33, solve Equation 32 for i_{pk} , use $V_I = \langle v_{ac} \rangle$, and then substitute back into Equation 33. Equation 36 is the result.

$$\langle v_{cp} \rangle = \frac{\langle v_{ac} \rangle \times d}{d_2} \quad (36)$$

Solve the above equation for d_2 and substitute into Equation 29 and also use i_{pk} from and substitute it into Equation 32. Equation 37 is the result after some rearranging.

$$\langle i_p \rangle = \frac{\langle v_{ac} \rangle^2 \times d^2 \times T_S}{\langle v_{cp} \rangle \times 2 \times L} \quad (37)$$

Finally, use $\langle v_{ac} \rangle = V_I$ and substitute in the above equation. Equation 38 is the result and gives the output side relationship.

$$\langle i_p \rangle \times \langle v_{cp} \rangle = V_I^2 \times \frac{d^2 \times T_S}{2 \times L} = \frac{V_I^2}{R_e} \quad (38)$$

This equation shows that the average output current times the average output voltage is equal to the apparent input power.

Implement the above input and output relationships into an equivalent circuit model. This model is useful for determining the DC operating point of a power supply. The input port is simply modeled with a resistor, R_e . The output port is modeled as a dependent power source. This power source delivers power equal to that dissipated by the input resistor, R_e . [Figure 11](#) shows the constructed equivalent circuit.

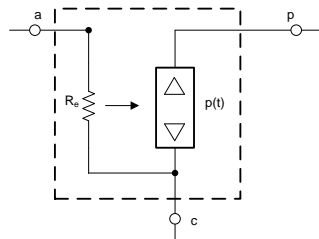


Figure 11. DCM PWM Switch Model

To illustrate discontinuous conduction mode power supply steady-state analysis using this model, examine the inverting buck-boost converter. The analysis proceeds like the CCM case. The equivalent circuit is substituted into the original circuit. The inductor is treated as a short circuit and the capacitor is treated as an open circuit. [Figure 12](#) shows the DCM inverting buck-boost converter model schematic, ignoring the inductor DC resistance, R_L , and the equivalent series resistance of the output capacitor, R_C .

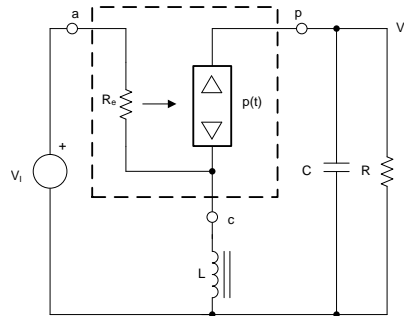


Figure 12. DCM Inverting Buck-Boost Converter Model

The apparent power dissipated in R_e is determined as [Equation 39](#).

$$P_{Re} = \frac{V_I^2}{R_e} \quad (39)$$

The dependent power source delivers the above amount of power to the output load resistor, R . Calculate the voltage gain as a function of D by equating the two powers from [Equation 38](#) and [Equation 39](#) as shown in [Equation 40](#) through [Equation 42](#).

$$\frac{V_I^2}{R_e} = \frac{V_O^2}{R} \quad (40)$$

$$\frac{V_o}{V_I} = \pm \sqrt{\frac{R}{R_e}} \quad (41)$$

Choose the negative root and substitute R_e by [Equation 35](#).

$$-\sqrt{\frac{R}{R_e}} = -\sqrt{\frac{R}{\frac{R}{D^2 \times T_S}}} = D \times \sqrt{\frac{R \times T_S}{2 \times L}} \quad (42)$$

[Equation 43](#) gives the voltage conversion relationship for the DCM inverting buck-boost.

$$\frac{V_o}{V_I} = -D \times \sqrt{\frac{R \times T_S}{2 \times L}} \quad (43)$$

Recall from earlier that:

$$K = \frac{2 \times L}{R \times T_S} \quad (44)$$

and making the substitution in the above equation, the result is identical to that obtained by balancing the inductor volt-seconds in the inverting buck-boost steady-state discontinuous conduction mode analysis section.

The steady-state voltage conversion relationship for the DCM inverting buck-boost becomes [Equation 45](#).

$$\frac{V_O}{V_I} = -\frac{D}{\sqrt{K}} \quad (45)$$

To derive the small signal model, the circuit in [Figure 12](#) is perturbed and linearized following the procedure similar to the one in the CCM derivation. Refer to the [Fundamentals of Power Electronics](#) to see the detail of derivation. [Figure 13](#) shows the resulting small signal model for the inverting buck-boost power stage operating in DCM.

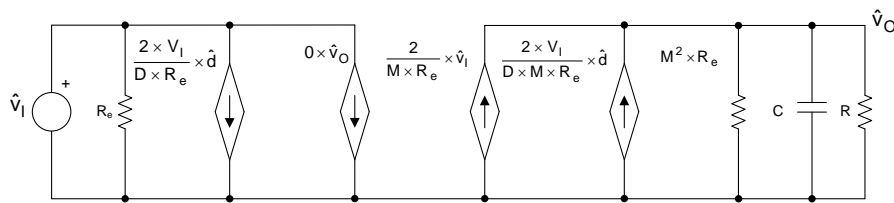


Figure 13. Small Signal DCM Inverting Buck-Boost Power Stage Model

[Equation 46](#) gives the duty-cycle-to-output transfer function for the inverting buck-boost power stage operating in DCM.

$$\frac{\hat{v}_O}{\hat{d}} = G_{do} \times \frac{1}{1 + \frac{s}{\omega_p}}$$

where

- $G_{do} = \frac{V_O}{D}$
- $D = -M \times \sqrt{K}$
- $M = \frac{V_O}{V_I}$
- $K = \frac{2 \times L}{R \times T_S}$

and

- $\omega_p = \frac{2}{R \times C}$ (46)

The expression for G_{do} can be simplified to [Equation 47](#).

$$G_{do} = -V_I \times \sqrt{\frac{R \times T_S}{2 \times L}} \quad (47)$$

4 Variations of the Inverting Buck-Boost Power Stage

4.1 Flyback Power Stage

A transformer-coupled variation of the traditional inverting buck-boost power stage is the flyback power stage. This power stage operates like the traditional inverting buck-boost power stage, except that the single winding inductor is replaced with a two (or more) winding coupled inductor. The power switch, Q1 in Figure 14, applies the input voltage to the primary side (L_{PRI}) of the coupled inductor. Energy is stored until Q1 is turned off. Energy is then delivered to the output capacitor and load resistor combination from the secondary side (L_{SEC}) of the coupled inductor through the output diode CR1. This power stage provides electrical isolation of the input voltage from the output voltage. Besides providing electrical isolation, the isolation transformer can step-down (or step-up) the input voltage to the secondary. The transformer turns ratio can be designed so that reasonable duty cycles are obtained for almost any input voltage and output voltage combination, thus avoiding extremely small or extremely high duty cycle values.

The flyback power stage also eliminates two characteristics which sometimes make the standard inverting buck-boost power stage unattractive, that is the output voltage is opposite in polarity from the input voltage and the power switch requires a floating drive. Besides providing isolation, the coupled inductor secondary can be connected to produce an output voltage of either positive or negative polarity. In addition, since the power switch is in series with the primary of the coupled inductor, the power switch can be connected so that the source is ground referenced instead of connecting the drain to the input voltage as in the standard inverting buck-boost power stage.

The flyback power stage is popular in 48-V input telecom applications and 110-V AC or 220-V AC off-line applications for output power levels up to approximately 50 W. The exact power rating of the flyback power stage, of course, is dependent on the input voltage and output voltage combination, its operating environment, and many other factors. Additional output voltages can be generated by simply adding another winding to the coupled inductor along with an output diode and output capacitor. Obtaining multiple output voltages from a single power stage is another advantage of the flyback power stage.

Figure 14 shows a simplified schematic of the flyback power stage with a drive circuit block included. In Figure 14, the secondary winding of the coupled inductor is connected to produce a positive output voltage. The power switch, Q1, is an n-channel MOSFET. The diode, CR1, is usually called the output diode. The secondary inductance, L_{SEC} , and capacitor, C, make up the output filter. The capacitor ESR, R_C , (equivalent series resistance) is not included. The resistor, R, represents the load seen by the power supply output.

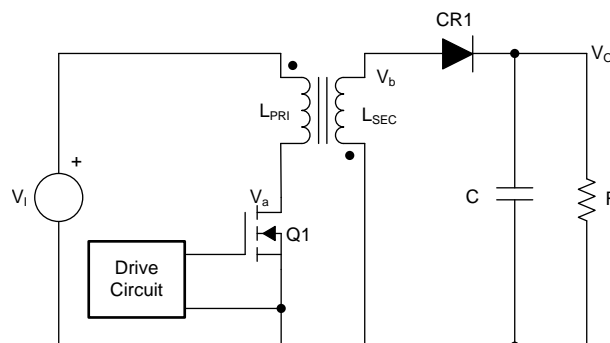
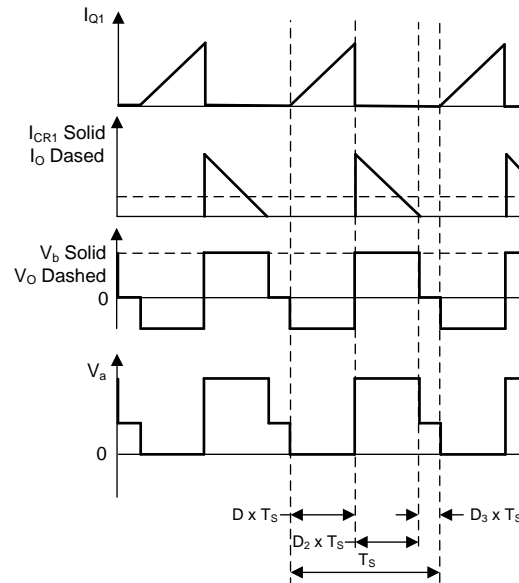


Figure 14. Flyback Power Stage Schematic

Figure 15 shows the important waveforms for the flyback power stage operating in DCM.


Figure 15. Discontinuous Mode Flyback Waveforms

Equation 48 calculates the simplified voltage conversion relationship for the flyback power stage operating in CCM (ignoring parasitics).

$$V_O = V_I \times \frac{N_s}{N_p} \times \frac{D}{1-D}$$

where

- N_s is the number of secondary side turns
 - N_p is the number of primary side turns
- (48)

Equation 49 calculates the simplified voltage conversion relationship for the flyback power stage operating in DCM (ignoring parasitics).

$$V_O = V_I \times \frac{N_s}{N_p} \times \frac{D}{\sqrt{K}}$$

where

- K is defined as
$$K = \frac{2 \times L_{SEC}}{R \times T_S}$$
- (49)

Equation 50 calculates the simplified duty-cycle-to-output transfer function for the flyback power stage operating in CCM.

$$\frac{\hat{v}_O}{\hat{d}}(s) = G_{do} \times \frac{N_S}{N_P} \times \frac{\left(1 + \frac{s}{\omega_{z1}}\right) \times \left(1 + \frac{s}{\omega_{z2}}\right)}{1 + \frac{s}{\omega_o \times Q} + 1 + \frac{s^2}{\omega_o^2}}$$

where

- $G_{do} \approx \frac{V_I}{(1-D)^2}$
- $\omega_{z1} = \frac{1}{R_C \times C}$
- $\omega_{z2} \approx \frac{(1-D)^2 \times R}{D \times L_{SEC}}$
- $\omega_o \approx \frac{(1-D)^2 \times R}{\sqrt{L_{SEC} \times C}}$
- $Q \approx \frac{(1-D) \times R}{\sqrt{\frac{L_{SEC}}{C}}}$

(50)

Equation 51 calculates the simplified duty-cycle-to-output transfer function for the flyback power stage operating in DCM.

$$\frac{\hat{v}_O}{\hat{d}} = G_{do} \times \frac{1}{1 + \frac{s}{\omega_p}}$$

where

- $G_{do} = V_I \times \frac{N_S}{N_P} \times \sqrt{\frac{R \times T_S}{2 \times L_{SEC}}}$
- $\omega_p = \frac{2}{R \times C}$

(51)

5 Component Selection

This section discusses the function of each of the main components of the inverting buck-boost power stage. The electrical requirements and applied stresses are given for each power stage component.

The completed power supply, made up of a power stage and a control circuit, usually must meet a set of minimum performance requirements. This set of requirements is usually referred to as the power supply specification. Many times, the power supply specification determines individual component requirements.

5.1 Output Capacitance

In switching power supply power stages, the output capacitance stores energy in the electric field due to the voltage applied. Thus, qualitatively, the function of a capacitor is to maintain a constant voltage.

The value of the output capacitance of an inverting buck-boost power stage is generally selected to limit the output voltage ripple to the level required by the specification. The series impedance of the capacitor and the power stage output current determine the output voltage ripple. The three elements of the capacitor that contribute to its impedance (and output voltage ripple) are the equivalent series resistance (ESR), equivalent series inductance (ESL), and capacitance (C). The following gives guidelines for output capacitor selection.

Use [Equation 52](#) for continuous inductor current mode operation to determine the amount of capacitance needed as a function of the output load current, I_O , switching frequency, f_s , and desired output voltage ripple, ΔV_O . This equation assumes all the output voltage ripple is due to the capacitance of the capacitor because the output capacitor supplies the entire output load current during the power stage ON state.

$$C \geq \frac{I_{O(\text{Max})} \times D_{\text{Max}}}{f_s \times \Delta V_O}$$

where

- $I_{O(\text{Max})}$ is the maximum output current and D_{Max} is the maximum duty cycle (52)

Use [Equation 53](#) for discontinuous inductor current mode operation to determine the amount of capacitance needed. This equation assumes all the output voltage ripple is due to the capacitance of the capacitor.

$$C \geq \frac{I_{O(\text{Max})} \times \left(1 - \sqrt{\frac{2 \times L}{R \times T_S}} \right)}{f_s \times \Delta V_O}$$
 (53)

However, in many practical designs, to get the required ESR, a capacitor with much more capacitance than calculated in [Equation 53](#) must be selected. For continuous inductor current mode operation, use [Equation 54](#) to find the ESR needed to limit the ripple to ΔV_O . This equation assumes there is enough capacitance such that the ripple due to the capacitance can be ignored.

$$\text{ESR} \leq \frac{\Delta V_O}{\left(\frac{I_{O(\text{Max})}}{1 - D_{\text{Max}}} + \frac{\Delta I_L}{2} \right)}$$
 (54)

Use [Equation 55](#) for discontinuous inductor current mode operation to find the ESR needed to limit the ripple to the peak-to-peak ripple, ΔV_O . This equation assumes there is enough capacitance such that the ripple due to the capacitance can be ignored.

$$\text{ESR} \leq \frac{\Delta V_O}{\Delta I_L}$$
 (55)

Ripple current flowing through the ESR of a capacitor causes power dissipation in the capacitor. This power dissipation causes a temperature increase internal to the capacitor. Excessive temperature can seriously shorten the expected life of a capacitor. Capacitors have ripple current ratings that are dependent on ambient temperature and must not be exceeded. Referring to [Figure 3](#), the output capacitor ripple current is the inductor current, I_L , minus the output current, I_O . Use [Equation 56](#) to find the RMS value of the ripple current flowing in the output capacitance (continuous inductor current mode operation).

$$I_{C(RMS)} = I_O \times \sqrt{\frac{D}{1-D}} \quad (56)$$

ESL can be a problem by causing ringing in the low MHz-region but can be controlled by choosing low ESL capacitors, limiting lead length (PCB and capacitor), and replacing one large device with several smaller ones connected in parallel.

There are four capacitor technologies: low-impedance aluminum, polymer or solid tantalum and multi-layer ceramic capacitors (MLCCs) are suitable for low-cost commercial applications. Low-impedance aluminum electrolytics are the lowest cost and offer high capacitance in small packages, but ESR is higher than the other three. Polymer electrolytics have become popular for the power-supply industry in recent years. These capacitors offer the best of both worlds – a low ESR that is stable over the temperature range and high capacitance in a small package. MLCCs or solid tantalum chip capacitors are probably the best choice if a surface-mounted device is needed. These two types offer a low ESR that is relatively stable over the temperature range, high ripple-current capability, low ESL, surge-current testing, and a high ratio of capacitance to volume. If MLCCs are used, it is important to review the DC bias effect as they lose capacitance when DC voltage is applied.

5.2 Output Inductance

In switching power supply power stages, the function of inductors is to store energy. The energy is stored in their magnetic field due to the current flowing. Thus, qualitatively, the function of an inductor is usually to attempt to maintain a constant current or equivalently to limit the rate of change of current flow.

The value of output inductance of an inverting buck-boost power stage is generally selected to limit the peak-to-peak ripple current flowing in it. In doing so, the mode of operation of the power stage, continuous or discontinuous, is determined. The inductor ripple current is directly proportional to the applied voltage and the time that the voltage is applied, and it is inversely proportional to its inductance.

Many designers prefer to design the inductor themselves, but that topic is beyond the scope of this report. However, the following discusses the considerations necessary for selecting the appropriate inductor. See the [TI-Unitrode Magnetics Design Handbook](#) for details on inductor design.

In addition to the inductance, other important factors that need to be considered when selecting the inductor are its maximum DC or peak current and maximum operating frequency. Using the inductor within its DC current rating is important to ensure that it does not overheat or saturate. Operating the inductor at less than its maximum frequency rating ensures that the maximum core loss is not exceeded, resulting in overheating or saturation.

Magnetic component manufacturers offer a wide range of off-the-shelf inductors suitable for DC/DC converters, some of which are surface mountable. There are many types of inductors available; the most popular core materials are ferrites and powdered iron. Bobbin or rod-core inductors are readily available and inexpensive, but care must be exercised in using them, because they are more likely to cause noise problems than are other shapes. Multilayer inductors are becoming popular nowadays. Be aware that these might cause noise problems like Bobbin or rod-core inductors. Custom designs are also feasible, provided the volumes are sufficiently high.

Current flowing through an inductor causes power dissipation due to the DC resistance from the inductor. This power dissipation is easily calculated. Power is also dissipated in the core of the inductor due to the flux swing caused by the AC voltage applied across it, but this information is rarely directly given in manufacturer's data sheets. Occasionally, the maximum operating frequency of the inductor and applied volt-seconds ratings give you some guidance regarding core loss. The power dissipation causes a temperature increase in the inductor. Excessive temperature can cause degradation in the insulation of the winding and cause increased core loss. Care must be exercised to ensure all the maximum ratings from the inductor are not exceeded. Use to find the loss in the inductor.

$$P_{\text{inductor}} = \left(I_{L(RMS)} \right)^2 \times R_{Cu} + P_{\text{Core}}$$

where

- R_{Cu} is the winding resistance

(57)

5.3 Power Switch

In switching power supply power stages, the function of the power switch is to control the flow of energy from the input power source to the output voltage. In an inverting buck-boost power stage, the power switch (Q1 in [Figure 1](#)) connects the input to the inductor when the switch is turned on and disconnects when the switch is off. The power switch must conduct the current in the output inductor while on and block the difference between the input voltage and output voltage when off. Also, the power switch must change from one state to the other quickly in order to avoid excessive power dissipation during the switching transition.

The type of power switch considered in this report is a power MOSFET. Other power devices are available but in most instances, the MOSFET is the best choice in terms of cost and performance (when the drive circuits are considered). The two types of MOSFET available for use are the n-channel and the p-channel. P-channel MOSFETs are popular for use in inverting buck-boost power stages because driving the gate is simpler than the gate drive required for an n-channel MOSFET. Use [Equation 58](#) to find the power dissipated by the power switch, Q1.

$$P_{D(Q1)} = \left(I_{L(RMS)} \right)^2 \times r_{DS(on)} \times D + \frac{1}{2} \times (V_I - V_O) \times \left(\frac{I_O}{1-D} \right) \times (t_r - t_f) \times f_s + Q_{Gate} \times V_{GS} \times f_s$$

where

- t_r and t_f are the MOSFET turn-on and turn-off switching times
- Q_{Gate} is the MOSFET total gate charge (58)

After selecting p-channel or n-channel, other parameters to consider while selecting the appropriate MOSFET are the maximum drain-to-source breakdown voltage, $V_{(BR)DSS}$ and the maximum drain current, $I_{D(Max)}$.

The selected MOSFET must have a $V_{(BR)DSS}$ rating greater than the maximum difference between the input voltage and output voltage, and some margin must be added for transients and spikes. The selected MOSFET must also have an $I_{D(Max)}$ rating of at least two times the maximum power stage inductor current. However, many times this is not sufficient margin and the MOSFET junction temperature must be calculated to make sure that it is not exceeded. Use [Equation 59](#) to estimate the junction temperature.

$$T_J = T_A + P_D \times R_{\theta JA}$$

where

- T_A is the ambient or heatsink temperature
- $R_{\theta JA}$ is the thermal resistance from the MOSFET chip to the ambient air or heatsink (59)

5.4 Output Diode

The output diode conducts when the power switch turns off and provides a path for the inductor current. Important criteria for selecting the rectifier include: fast switching, breakdown voltage, current rating, low-forward voltage drop to minimize power dissipation, and appropriate packaging. Unless the application justifies the expense and complexity of a synchronous rectifier, the best solution for low-voltage outputs is usually a Schottky rectifier. The breakdown voltage must be greater than the maximum difference between the input voltage and output voltage, and some margin must be added for transients and spikes. The current rating must be at least two times the maximum power stage output current. Normally the current rating is much higher than the output current because power and junction temperature limitations dominate the device selection.

The voltage drop across the diode in a conducting state is primarily responsible for the losses in the diode. The power dissipated by the diode can be calculated as the product of the forward voltage and the output load current. The switching losses which occur at the transitions from conducting to non-conducting states are very small compared to conduction losses and are usually ignored. Use [Equation 60](#) to find the power dissipated by the rectifier.

$$P_{D(\text{Diode})} = V_D \times I_O \times (1 - D)$$

where

- V_D is the forward voltage drop of the catch rectifier (60)

Use [Equation 61](#) to estimate the junction temperature.

$$T_J = T_A + P_D \times R_{\theta JA} \tag{61}$$

6 Summary

This application report described and analyzed the operation of the inverting buck-boost power stage. The two modes of operation, continuous conduction mode and discontinuous conduction mode, are examined. Steady-state and small-signal are the two analyses performed on the inverting buck-boost power stage. The flyback power stage is presented as a variation of the basic inverting buck-boost power stage. The main results of the steady-state analyses are summarized below.

Use [Equation 62](#) to find the voltage conversion relationship for CCM.

$$V_O = - \left[(V_I - V_{DS}) \times \frac{D}{1-D} - V_d - \frac{I_L \times R_L}{1-D} \right] \quad (62)$$

or a slightly simpler version:

$$V_O = -V_I \times \frac{D}{1-D} \times \frac{1}{1 + \frac{R_L}{R \times (1-D)^2}} \quad (63)$$

which can be simplified to:

$$V_O = -V_I \times \frac{D}{1-D} \quad (64)$$

Use [Equation 65](#) to find the relationship between the average inductor current and the output current for the continuous mode inverting buck-boost power stage.

$$I_{L(Avg)} = \frac{-I_O}{(1-D)} \quad (65)$$

Use [Equation 66](#) to find the discontinuous conduction mode inverting buck-boost voltage conversion relationship.

$$V_O = -V_I \times \frac{D}{\sqrt{K}}$$

where K is defined as $K = \frac{2 \times L}{R \times T_S}$ (66)

The major results of the small-signal analyses are summarized below. Use [Equation 67](#) to find the small-signal duty-cycle-to-output transfer function for the inverting buck-boost power stage operating in CCM.

$$\frac{\hat{v}_O}{\hat{d}}(s) = G_{do} \times \frac{\left(1 + \frac{s}{\omega_{z1}}\right) \times \left(1 + \frac{s}{\omega_{z2}}\right)}{1 + \frac{s}{\omega_o \times Q} + \frac{s^2}{\omega_o^2}}$$

where

- $G_{do} \approx \frac{V_I}{(1-D)^2}$
- $s_{z1} = \frac{1}{R_C \times C}$
- $s_{z2} \approx \frac{(1-D)^2 \times R}{D \times L}$
- $\omega_o \approx \frac{1-D}{\sqrt{L \times C}}$
- $Q \approx \frac{(1-D) \times R}{\sqrt{L \times C}}$

(67)

Use [Equation 68](#) to find the small-signal duty-cycle-to-output transfer function for the inverting buck-boost power stage operating in DCM.

$$\frac{\hat{v}_O}{\hat{d}} = G_{do} \times \frac{1}{1 + \frac{s}{\omega_p}}$$

where

- $G_{do} = \frac{V_O}{D} = -\frac{V_I}{\sqrt{K}}$

and

- $\omega_p = \frac{2}{R \times C}$

(68)

The requirements for the inverting buck-boost power stage components based on voltage and current stresses applied during the operation of the inverting buck-boost power stage are presented in [Section 5](#). For further study, several references are given.

7 References

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